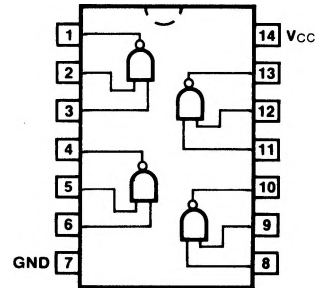


54/7439**QUAD 2-INPUT NAND BUFFER**

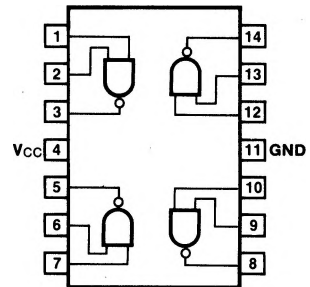
(With Open-Collector Output)

CONNECTION DIAGRAMS
PINOUT A**ORDERING CODE:** See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0\text{ V} \pm 5\%$, $T_A = 0^\circ\text{C to } +70^\circ\text{C}$	$V_{CC} = +5.0\text{ V} \pm 10\%$, $T_A = -55^\circ\text{C to } +125^\circ\text{C}$	
Plastic DIP (P)	A	7439PC		9A
Ceramic DIP (D)	A	7439DC	5439DM	6A
Flatpak (F)	B	7439FC	5439FM	3I

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.) HIGH/LOW
Inputs	1.0/1.0
Outputs	OC**/30

PINOUT B**DC AND AC CHARACTERISTICS:** See Section 3*

SYMBOL	PARAMETER		54/74		UNITS	CONDITIONS	
			Min	Max			
VOL	Output LOW Voltage	XM, XC		0.4	V	IO _L = 48 mA	V _{CC} = Min V _{IN} = 2.0 V Other Inputs = 2.0 V
		XM, XC		0.5		IO _L = 60 mA	
		XC		0.6		IO _L = 80 mA	
ICCH ICCL	Power Supply Current			8.5 54	mA	V _{IN} = Gnd V _{IN} = Open	V _{CC} = Max
tPLH tPHL	Propagation Delay			22 18	ns	Figs. 3-2, 3-4	

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ\text{C}$ and $V_{CC} = +5.0\text{ V}$.

**OC — Open Collector