

# AN6225FHN

## 800 MHz quadrature modulation IC for PDC

### ■ Overview

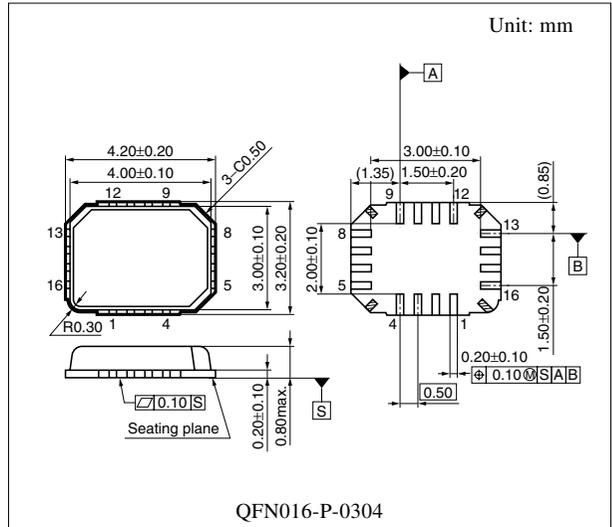
The AN6225FHN is an IC incorporating a quadrature modulator, a phase shifter and APC circuit for a mobile telephone.

### ■ Features

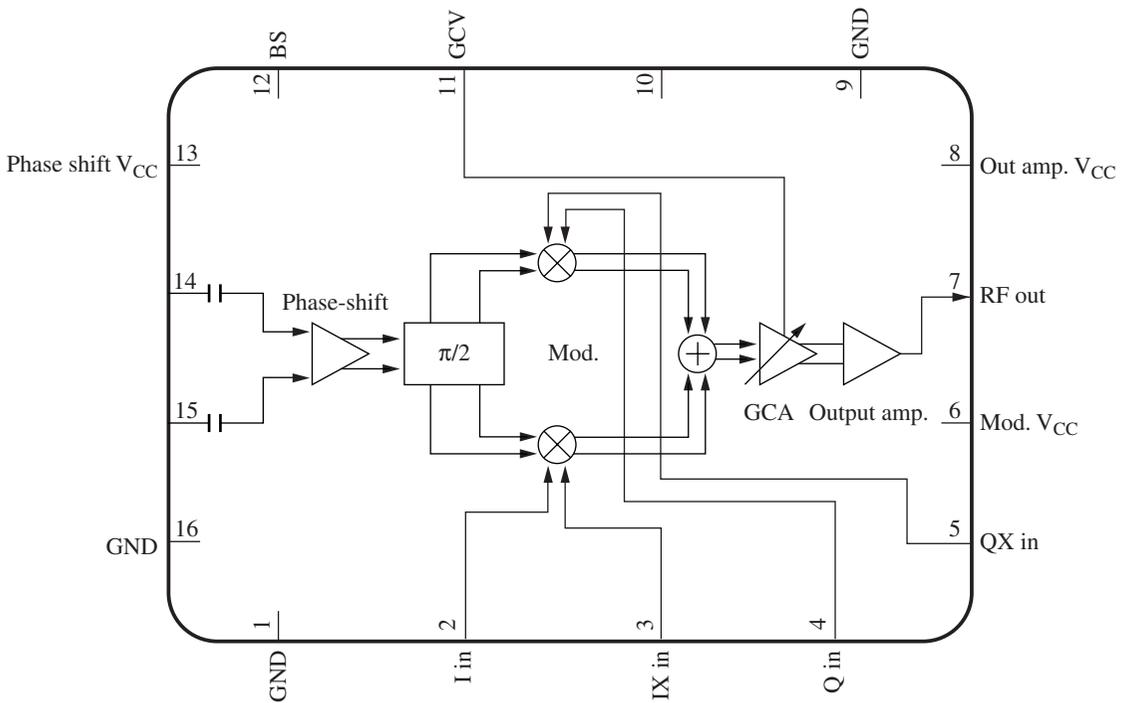
- Transmission output: -1 dBm
- Ultra mini-type 3 mm × 4 mm leadless package
- Quadrature modulation system

### ■ Applications

- Cellular telephone (PDC800 MHz)



### ■ Block Diagram



### ■ Pin Descriptions

Pin No.	Description	Pin No.	Description
1	GND	9	GND
2	I in	10	N.C.
3	IX in	11	GCV
4	Q in	12	Battery save
5	QX in	13	Phase shift V <sub>CC</sub>
6	Modulator V <sub>CC</sub>	14	TX local in 1
7	RF out	15	TX local in 2
8	Output amplifier V <sub>CC</sub>	16	GND

### ■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	5.0	V
Supply current	I <sub>CC</sub>	60	mA
Power dissipation *2	P <sub>D</sub>	113	mW
Operating ambient temperature *1	T <sub>opr</sub>	-30 to +80	°C
Storage temperature *1	T <sub>stg</sub>	-55 to +125	°C

Note) \*1: Except for the operating ambient temperature and storage temperature, all ratings are for T<sub>a</sub> = 25°C.

\*2: P<sub>D</sub> is the value at T<sub>a</sub> = 80°C without a heatsink. Use this device within the range of allowable power dissipation referring to

"■ Technical Data • P<sub>D</sub>—T<sub>a</sub> curves of QFN016-P-0304".

### ■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V <sub>CC</sub>	2.6 to 4.0	V

### ■ Electrical Characteristics at T<sub>a</sub> = 25°C

Unless otherwise specified, V<sub>CC1</sub>, V<sub>CC2</sub>, V<sub>CC3</sub> = 3.0 V, BS = 2.5 V, Lo input level is the setting value of a signal source (output impedance 50 Ω).

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Transmission output level 1 *1	P <sub>O1</sub>	Lo = 889 MHz, -20 dBm GCV = 2.2 V	-8.5	-5.5	-2.5	dBm
Transmission output level 2 *1	P <sub>O2</sub>	Lo = 960 MHz, -20 dBm GCV = 2.2 V	-8.5	-5.5	-2.5	dBm
Current consumption *1	I <sub>CC</sub>	Lo = 950 MHz, -20 dBm GCV = 2.2 V	20	26	34	mA
Sleep current *1	I <sub>SLP</sub>	BS = 0 V, GCV = 2.2 V	—	0	10	μA
Image leak *1	IL	Lo = 950 MHz, -20 dBm GCV = 2.2 V	—	-35	-30	dBc

### ■ Electrical Characteristics at $T_a = 25^\circ\text{C}$ (continued)

Unless otherwise specified,  $V_{CC1}$ ,  $V_{CC2}$ ,  $V_{CC3} = 3.0\text{ V}$ ,  $BS = 2.5\text{ V}$ ,  $Lo$  input level is the setting value of a signal source (output impedance  $50\ \Omega$ ).

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Carrier leak <sup>*1</sup>	CL	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-30	-25	dBc
Base band secondary distortion <sup>*1</sup>	BD	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-40	-30	dBc
Output level deviation <sup>*1</sup>	DPO	$Lo = 889\text{ MHz}$ to $960\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	-1.5	0	1.5	dB
GC variable width <sup>*1</sup>	PGC	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 0.9\text{ V}$ to $2.2\text{ V}$	—	-35	-25	dB
Modulation precision <sup>*2</sup>	EVM	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	2.0	3.5	%

### • Design reference data

Note) The characteristics listed below are theoretical values based on the IC design and are not guaranteed.

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Adjacent channel leak power suppression 1 (50 kHz detuning) <sup>*4</sup>	ACP1	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-68	-60	dBc
Adjacent channel leak power suppression 1 (100 kHz detuning) <sup>*4</sup>	ACP2	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-75	-65	dBc
Minimum output level <sup>*1</sup>	Pmin	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 0.9\text{ V}$	-50	-40	-30	dBm
LOX2 leak <sup>*1</sup>	LOL	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-40	-30	dBc
Transmission output level 3 <sup>*5</sup>	$P_{O3}$	$Lo = 889\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	-4.5	-1.0	2.5	dBm
Transmission output level 4 <sup>*5</sup>	$P_{O4}$	$Lo = 960\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	-4.5	-1.0	2.5	dBm
Adjacent channel leak power suppression 2 (50 kHz detuning) <sup>*6</sup>	ACP3	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-60	-50	dBc
Adjacent channel leak power suppression 2 (100 kHz detuning) <sup>*6</sup>	ACP4	$Lo = 950\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$	—	-75	-65	dBc
Receiving band noise <sup>*3</sup>	NRX	$Lo = 893\text{ MHz}$ , $-20\text{ dBm}$ $GCV = 2.2\text{ V}$ , $f = 885\text{ MHz}$	—	-131	-127	dBm/Hz

Note) \*1: IQ signal amplitude:  $0.18\text{ V[p-p]}$  (both phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [0000] continuous wave input.

$P_{O1}$  output frequency:  $889.002625\text{ MHz}$ ,  $P_{O2}$  output frequency:  $960.002625\text{ Hz}$ ,  $P_{min}$  output frequency:  $950.002625\text{ MHz}$

An output level be measured by a spectrum analyzer.

Setting of a spectrum analyzer: SPAN =  $20\text{ kHz}$ , RBW =  $300\text{ Hz}$ , VBW =  $30\text{ Hz}$ , ST =  $5\text{ s}$

(When inputting  $\pi/4$  QPSK-modulated [0000] continuous wave as IQ signal, the frequency for  $P_{O1}$ ,  $P_{O2}$  and  $P_{min}$  becomes  $Lo$  frequency + IQ signal frequency, which leads to the above value.)

$Lo$  input level is the setting value of a signal source (output impedance  $50\ \Omega$ ).

\*2: IQ signal amplitude:  $0.18\text{ V[p-p]}$  (double phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [PN9] continuous wave input.

The output level be measured by a spectrum analyzer. (By using a modulation precision measurement function.)

### ■ Electrical Characteristics at $T_a = 25^\circ\text{C}$ (continued)

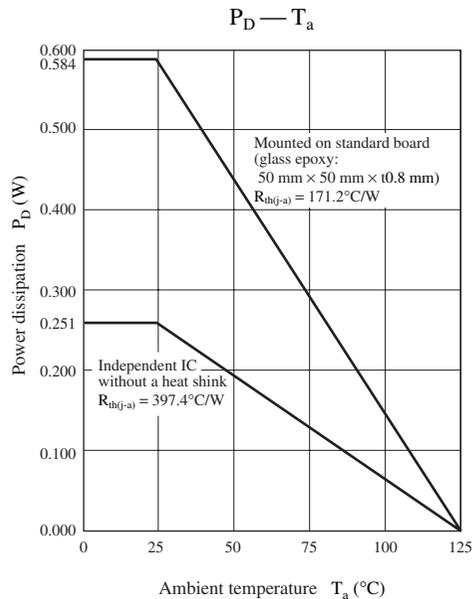
Unless otherwise specified,  $V_{CC1}$ ,  $V_{CC2}$ ,  $V_{CC3} = 3.0\text{ V}$ ,  $BS = 2.5\text{ V}$ ,  $Lo$  input level is the setting value of a signal source (output impedance  $50\ \Omega$ ).

Note) (continued)

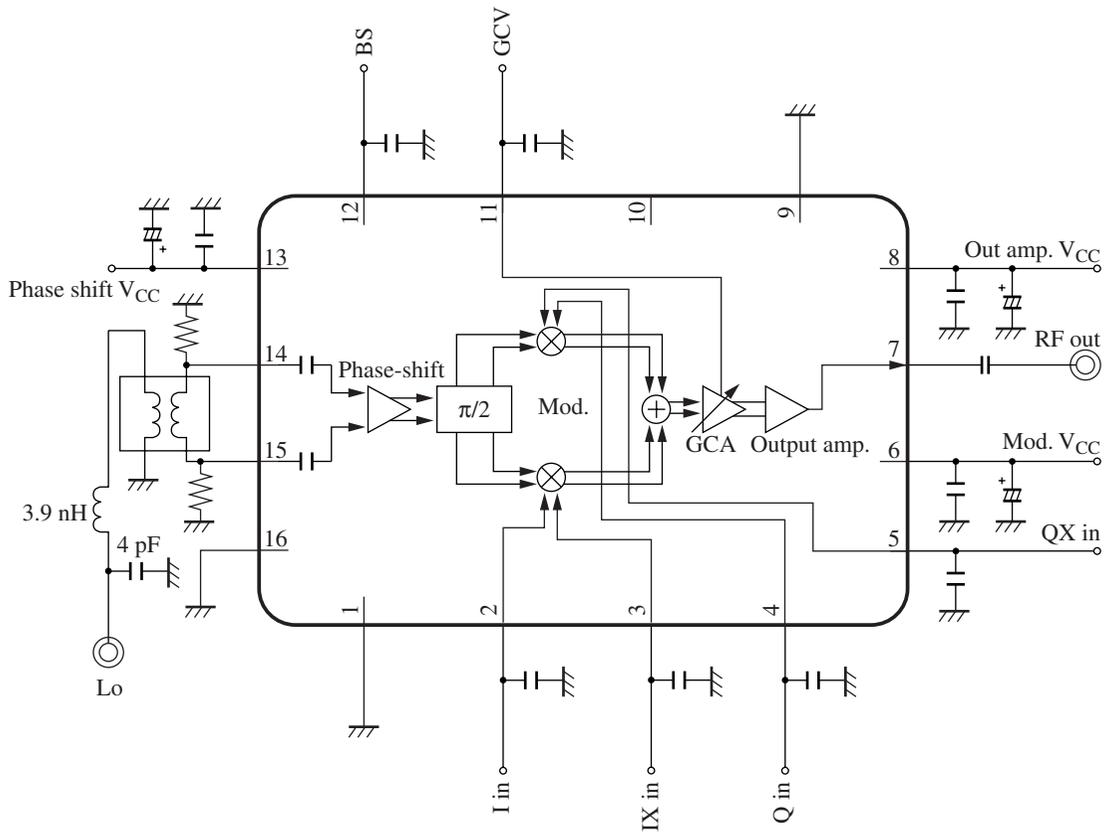
- \*3: IQ signal amplitude:  $0.31\text{ V[p-p]}$  (both phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [PN9] continuous wave input.  
A receiving band noise (dBm/Hz) can be determined by deducting  $10 \log(3\text{ kHz}) = 34.77$  from 885 MHz floor noise level (dBm) measured beforehand.  
Setting of a spectrum analyzer: SPAN =  $5\text{ kHz}$ , RBW =  $3\text{ kHz}$ , VBW =  $100\text{ Hz}$ , ST =  $50\text{ ms}$ , REFLEV =  $-20\text{ dBm}$ , ATT =  $0\text{ dB}$
- \*4: IQ signal amplitude:  $0.18\text{ V[p-p]}$  (both phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [PN9] continuous wave input.  
To be measured by a spectrum analyzer. (By using a leak power measurement function for an adjacent channel.)  
Setting of a spectrum analyzer: SPAN =  $250\text{ kHz}$ , RBW =  $1\text{ kHz}$ , VBW =  $1\text{ kHz}$ , ST =  $2\text{ s}$
- \*5: IQ signal amplitude:  $0.31\text{ V[p-p]}$  (both phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [0000] continuous wave input.  
 $P_{O3}$  output frequency:  $889.002625\text{ MHz}$ ,  $P_{O4}$  output frequency:  $960.002625\text{ Hz}$ .  
An output level be measured by a spectrum analyzer.  
Setting of a spectrum analyzer: SPAN =  $20\text{ kHz}$ , RBW =  $300\text{ Hz}$ , VBW =  $30\text{ Hz}$ , ST =  $5\text{ s}$   
(When inputting  $\pi/4$  QPSK-modulated [0000] continuous wave as IQ signal, the frequency for  $P_{O3}$ ,  $P_{O4}$  and  $P_{min}$  becomes  $Lo$  frequency + IQ signal frequency, which leads to the above value.)  
 $Lo$  input level is the setting value of a signal source (output impedance  $50\ \Omega$ ).
- \*6: IQ signal amplitude:  $0.31\text{ V[p-p]}$  (both phases), DC bias:  $1.5\text{ V}$ ,  $\pi/4$  QPSK-modulated [PN9] continuous wave input.  
To be measured by a spectrum analyzer. (By using a leak power measurement function for an adjacent channel.)  
Setting of a spectrum analyzer: SPAN =  $250\text{ kHz}$ , RBW =  $1\text{ kHz}$ , VBW =  $1\text{ kHz}$ , ST =  $2\text{ s}$

### ■ Technical Data

- $P_D - T_a$  curves of QFN016-P-0304



■ Application Circuit Example



## Request for your special attention and precautions in using the technical information and semiconductors described in this material

- (1) An export permit needs to be obtained from the competent authorities of the Japanese Government if any of the products or technologies described in this material and controlled under the "Foreign Exchange and Foreign Trade Law" is to be exported or taken out of Japan.
- (2) The technical information described in this material is limited to showing representative characteristics and applied circuit examples of the products. It does not constitute the warranting of industrial property, the granting of relative rights, or the granting of any license.
- (3) The products described in this material are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).  
Consult our sales staff in advance for information on the following applications:
  - Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
  - Any applications other than the standard applications intended.
- (4) The products and product specifications described in this material are subject to change without notice for reasons of modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the guaranteed values, in particular those of maximum rating, the range of operating power supply voltage and heat radiation characteristics. Otherwise, we will not be liable for any defect which may arise later in your equipment.  
Even when the products are used within the guaranteed values, redundant design is recommended, so that such equipment may not violate relevant laws or regulations because of the function of our products.
- (6) When using products for which dry packing is required, observe the conditions (including shelf life and after-unpacking standby time) agreed upon when specification sheets are individually exchanged.
- (7) No part of this material may be reprinted or reproduced by any means without written permission from our company.

## Please read the following notes before using the datasheets

- A. These materials are intended as a reference to assist customers with the selection of Panasonic semiconductor products best suited to their applications.  
Due to modification or other reasons, any information contained in this material, such as available product types, technical data, and so on, is subject to change without notice.  
Customers are advised to contact our semiconductor sales office and obtain the latest information before starting precise technical research and/or purchasing activities.
- B. Panasonic is endeavoring to continually improve the quality and reliability of these materials but there is always the possibility that further rectifications will be required in the future. Therefore, Panasonic will not assume any liability for any damages arising from any errors etc. that may appear in this material.
- C. These materials are solely intended for a customer's individual use.  
Therefore, without the prior written approval of Panasonic, any other use such as reproducing, selling, or distributing this material to a third party, via the Internet or in any other way, is prohibited.