# **Reversible motor driver BA6219B/BA6219BFP-Y**

The BA6219B and BA6219BFP-Y are reversible-motor drivers suitable for brush motors. Two logic inputs allow four output modes : forward, reverse, idling, and braking. The motor revolving speed can be set arbitrarily by controlling the voltage applied to the motor.

#### Applications

VCRs and cassette tape recorders

#### Features

- 1) Large output current. (Io=2.2A maximally)
- 2) Built-in thermal shutdown circuit.

- 3) Built-in output voltage setting pins.
- 4) Small standby circuit current.



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# BA6219B/BA6219BFP-Y

#### Absolute maximum ratings (Ta=25°C)

Parameter Power supply voltage		Symbol	Limits	Unit V
		VCC1, VCC2	24	
Power	BA6219B		2200*1	- mW
dissipation	BA6219BFP - Y	Pd –	1450*2	
Operating temperature		Topr	-20~75	Ĵ
Storage temperature		Tstg	-50~125	Ċ
Output current		lo	2.2*3	Α
Input voltage		Vin	-0.3~Vcc1	v

\*1 Reduce power by 22 mW for each degree above 25°C.

\*2 Mounted on a 90 X 50 X 1.6 mm glass epoxy board. Reduce power by 14.5 mW for each degree above 25°C.

\*3 500 μs pulse with a duty ratio of 1%.

#### Recommended operating conditions (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Operating power	Vcc1	0		10	
supply voltage	Vcc2	0		18	v

## Electrical characteristics (unless otherwise noted, Ta=25°C and Vcc=12V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Current consumption 1	Icc1	-	1.2	2.5	mA	Both input pins LOW
Current consumption 2	ICC2	-	16	35	mA	One input pin HIGH, the other LOW
Current consumption 3	lcca	_	25	60	mA	Both input pins HIGH
Input threshold voltage	Vтн	1.0	2.0	3.0	v	Low level is 1 V or less, HIGH level is 3 V or more
Output voltage HIGH	Vн	6.5		-	v	RL=60Ω, ZD=6.8V
Output voltage LOW	VL	_	_	1.2	v	RL==60 Ω

#### Electrical characteristic curves





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Fig.4 Output voltage vs. 4 pin voltage

# Measurement circuits

BA6219BFP-Y



# BA6219B



#### Circuit operation

Mada	tput	Out	Input	
Mode	OUT2	OUT1	IN2	IN1
Idling	OPEN	OPEN	L	L
Forward	L	Н	L	н
Reverse	н	L	Н	L
Braking	L	L	Н	н

Note: HIGH level input is 3.0 V or more LOW level input is 1.0 V or less



**Reversible DC Motor Drivers** 

DC Motor Drivers

# BA6219B/BA6219BFP-Y

Pin description
(BA6219BFP-Y)

Pin No.	Pin name	Function
1	-	NC
2	CD1	Capacitor connection pin for preventing both output transistors being turned on at the same time
3	_	NC
4	VR	Output HIGH voltage setting pin
5	-	NC
6	IN1	Logic input pin
7	GND	GND
8	IN2	Logic input pin
9	-	NC
10	Vcc1	Small signal section power supply pin
11	Vcc2	Motor output power supply pin
12	_	NC
13	CD2	Capacitor connection pin for preventing both output transistors being turned on at the same time
14	_	NC
15	OUT2	Motor output pin
16	_	NC
17	_	NC
18	_	NC
19	GND	GND
20	GND	GND
21	. –	NC
22	_	NC
23	_	NC
24	OUT1	Motor output pin
25	-	NC
Fin	GND	GND

Pin No.	Pin name	Function
1	GND	GND
2	OUT 1	Motor output pin
3	CD1	Capacitor connection pin for preventing both output transistors being turned on at the same time
4	Ve	Output HIGH voltage setting pin
5	IN1	Logic input pin
6	IN2	Logic input pin
7	Vcc1	Control circuit power supply pin
8	Vcc2	Output power supply pin
9	CD2	Capacitor connection pin for preventing both output transistors being turned on at the same time
10	OUT 2	Motor output pin

\* All the GND plns have to be connected

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## BA6219B/BA6219BFP-Y

Reversible DC Motor Drivers

DC Motor Drivers

#### Operation notes

(1) A schematic for the internal circuit associated with the output voltage setting pin (Pin 4) is shown in Fig. 7.

The maximum output voltage (Vomax.) is given by :  $V_{OMAX} = V_{CC1} - V_{SAT} (Q1) - V_{BE} (Q2) - V_{BE} (Q3) - V_{BE} (Q4)$ 

For the condition of Ve being equal to or less than Vomax, the relationship between the output voltage (Vo) and the pin 4 voltage is given by :

 $V_{O} = V_{R} + \{V_{BE (Q5)} + V_{BE (Q6)} + V_{BE (Q7)}\} - (V_{BE (Q2)} + V_{BE} (Q3) + V_{BE (Q4)}\}$ 

Though  $\Delta V_{\text{BE}}$  depends on the output power supply, Vo is nearly equal to Ve.



(2) Thermal shutdown circuit

The thermal shutdown circuit turns off the driver output if the chip temperature rises to about 180°C. The shutdown signal is not latched.

(3) Power supply impedance

When the motor stops or starts, a rush current flows in the V<sub>cc</sub> and GND lines. Depending on the way of connecting a power supply decoupling capacitor (100  $\mu$ F), the control input voltage may become a negative value or the supply voltage may drop to below the output voltage. This can cause erratic operations due to parasitic effects.

Make sure that pin voltages will not exceed the supply voltage by more than 0.3V or will not become less than the GND pin voltage by more than 0.3V.

(4)  $V_{CC1}$ ,  $V_{CC2}$ , and  $V_R$  are related to each other as shown in the output circuit diagram of Fig. 8. Their values should be kept within the following ranges.

Operating	supply	voltage	ranges

Pin .	Voltage	Unit
Vcc1	8~18	V
Vcc2	8~18	V
Va	Indicated in the following	-



 When the output voltage control pin (pin 4) is not used

 $V_{\text{R}} < V_{\text{CC1}} - V_{\text{SAT}} \left( \alpha_1 \right) + V_{\text{BE}} \left( \alpha_5 \right) + V_{\text{BE}} \left( \alpha_6 \right) + V_{\text{BE}} \left( \alpha_7 \right) \rightleftharpoons \\ V_{\text{CC1}} - 2.5V$ 

$$\begin{split} V_{\mathsf{R}} &< \mathsf{V}_{\mathsf{CC2}} - \left\{ \left(\mathsf{V}_{\mathsf{SAT}}\left(_{\mathsf{Q3}}\right) - \mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q3}}\right) - \mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q3}}\right) - \mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q2}}\right) + \left(\mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q5}}\right) + \mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q6}}\right) + \mathsf{V}_{\mathsf{BE}}\left(_{\mathsf{Q7}}\right)\right\} \rightleftharpoons \mathsf{V}_{\mathsf{CC2}} - \mathsf{1V} \end{split}$$



Output voltage control range

Fig.9

2) When pin 4 is not used, Vn is either short-circuited to Vcc1 or left OPEN.

(5) Input circuit



Figures in parentheses are for the BA6219BFP-Y

Fig.10

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# Application example(1) BA6219B



- Fig.11
- ZD : Zener diode to set output voltage. Use any zener diode that is suitable for your application.
- $R_c$ : Resistor used for reducing collector loss and limiting the short-circuit current. A resistance range of  $3 \sim 10 \Omega$  is recommended.
- C : Power supply filtering capacitor. Place as near as possible to the V<sub>CC1</sub> pin.
- CD: Capacitor to prevent both output transistors being turned on at the same time.
- CM: Capacitor to absorb surge voltage and prevent parasitic oscillations.

#### (2) BA6219BFP-Y





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