Video ICs

# VIF/SIF Signal Processor for Televisions and VCRs BH7370FS

The BH7370FS is a VIF/SIF signal processor that supports the M mode (used in the U.S. and Japan). An application of BiCMOS process and active filter technology, this IC has been designed with an internal VCO, AFT, sound bypass filter, sound trap and FM demodulator circuit. And thanks to Rohm's original automatic adjustment technology, the digital AFT circuit, internal tank circuit and filter circuit are all adjustment-free.

#### Applications

TVs, LCD TVs, and tuner blocks for multimedia devices.

#### Features

1)Internal adjustment-free IF tank coil and audio filter.

 Digital AFT circuit, for highly precise, highly stable frequencies.

- 3)The reference signal input from the tuner fixed at 4MHz, eliminating the need for an oscillator.
- No VCO coil is used, allowing for excellent resistance to magnetic noise.
- 5)A pulse count audio detector circuit with excellent S/N and linearity.

6)Internal B/W inverter circuit.

### ●Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	7.0	v
Power dissipation	Pd	650*	mW
Operating temperature	Topr	-15~75	Ů
Storage temperature	Tstg	-40~150	r

\* Reduce by 6.5 mW for each increase in Ta of 1 C over 25 C.

#### ●Operating conditions (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Power supply voltage	Vcc	4.5	—	5.5	V

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## Block diagram



## Pin descriptions

Pin No.	Pin Name	Function
1	NC	_
2	AGC-ADJ	Adjusting the RF AGC delay point
3	RF-AGC	RF AGC output
4	GND1	VIF GND
5	VIFA	VIFA input
6	VIFB	VIFB input
7	GND2	SIF and VCO GND
8	OSC-IN	4 MHz oscillator input
9	ECO	Reference filter detection
10	AFT-OUT	AFT output
11	AF-OUT	Audio output
12	AFT-FB	FB pin for PLL-FLL

Pin No.	Name	Function
13	VCO-CONT	Switching between 45.75 MHz (U.S.) and 58.75 MHz (Japan)
14	AFC	Audio detection filter and audio/video mute switch
15	Vo-4.5M	2nd SIF output
16	GND-D	CMOS ground
17	Vdd	CMOS VDD
18	Vcc	Bipolar Vcc
19	FS	F-counter output
20	PLL-FLT	PLL phase detection FILTER
21	VIDEO-OUT	VIDEO OUT
22	EQFLT	EQ FILTER
23	AGC-FLT	IF AGC FILTER
24	NC	. —

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Pin No.	Pin Name	IN/OUT	Voltage	Equivalent circuit	Function
1	NC				Internally open
2	AGC—ADJ	_	0.2V (13kΩ connection)	2 7.8K 4.5K GND	Adjusting the RFAGC delay point. A fixed resistor ( $20k \Omega$ ) is normally fixed between grounds. For greater precision, adjust with a variable resistor( $50k \Omega$ ).
3	RF-AGC	Ουτ	_		RF-AGC output (reverse). Because of open collector output, gain can be set with an attached resistor (the minimum value for pin 3's maximum sink current is 0.7 mA). Pin 3 voltage should remain below 7 V.
4	GND1	_	٥V	· ·	Bipolar ground for the VIF, AGC and video system.
5	VIFA VIFB	IN	3.4V	5	IF input. Usə equilibrium input.
7	GND2	-	οv		Bipolar ground for the SIF and VCO.
8	REF OSC—IN	IN	2V	B 20k 10k   20k 10k 30k   30k 30k 9	4 MHz reference oscillator input (input level = 25-100 mVpp, precision = 0.1% or less)
9	ECO	_	2.2V		Reference filter detection output.
10	AFT-OUT	OUT	_		AFT output (CMOS output). Vop and the ground each have an internal 100 kΩ resistor. Output is 1/2 Vop at the center frequency.

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Pin No.	Pin Name	IN/OUT	Voltage	Equivalent circuit	Function
11	AF—OUT	Ουτ	2.2V	₩ 200 ₩ 200 100 20k ↓ 100 20k ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	Audio signal output. A de-emphasis constant should normally be attached.
12	AFTFB	OUT	-		The Lo range of the AFT curve can be expanded by feeding back to PLL—FIL.
13	VCO-CONT	IN	-		Switching the internal VCO frequency. High = 45.75 MHs (U.S.) (connect to Vcc) Low = 58.75 MHz (Japan) (connect to ground)
14	AFC	_	2.2V		Automatic adjustment FB for the 500 kHz pulse counter FM DEMO. Attach a capacitor (roughly 4.7 $\mu$ F). Lowering this pin below 0.3 V Mutes Audio and Video.
15	VO4.5M	_	2.4V	500 500 500 500 500 500 500 500	2nd SIF output. Sound Filter characteristics can be changed by connecting this pin to the TRAP. To reduce color buzzing, attach a $3.5$ MHz trap. Internal impedance is approximately $500 \Omega$ . Attach a Buffer if output.
16	GND D	_	ov	_	C-MOS GND. Locate the pattern as far as possible from the Bipolar-GND
17	Voo	_	4.5V	_	C-MOS Voo. CMOS radiation can be greatly reduced by positioning a CR filte between this pin and Bipolar-Voo

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Pin No.	Pin Name	IN/OUT	Voltage	Equivalent circuit	Function
18	Vcc		5.0V	· _	Power supply for the entire bipolar system.
19	FS	_	_		Output of the F-counter's CMOS charge pump.
20	PLL-FLT	-	3.0V		Attached time constant of the PLL filter.
21	VIDEO-OUT	OUT	2.0V (SYNC)		Video output. Output via the sound trap, B/W noise inverter, and EQ amplifier.
22	EQ-FLT	_	2.0V (SYNC)		EQ filter (normally left open). For EQ characteristics, use an LCR series resonator to connect this pin to the ground.
23	AGC-FLT	_	4.0V		Attaching the filter time constant for the VIF and AGC.
24	NC			~	Internally open.

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# ●Electrical characteristics (unless otherwise noted, Ta=25°C, Vcc=5V, P=45.75MHz)

Parameter	Symbol	Min.	Тур	Max.	Unit	Conditions
Circuit current	lcc	–	50	-	mA	—
(Internal stabilization supply voltage)	(V <sub>REG</sub> )	_	(2.6)	-	V	_
(VIF)			[			
Input sensitivity	V <sub>VMIN</sub>		40	46	dB µ	Vvo= -3 dB
Max. allowable input level	VVMAX	100	110	-	dΒ μ	Vvo = +1 dB
AGC range	GR	62	70	-	dB	Vvo ≖ ±3 dB range
Quiescent video output voltage	V <sub>P21</sub>	_	3.4	-	V	No signal, V23 = Vcc
Video detection output level	V <sub>VO</sub>		1.1	-	V <sub>P-P</sub>	P = 90 dB μ, AM 87.5% MOD,
Peak voltage of the synchronization signal	V <sub>P21SY</sub>	_	2.0	÷	v	100% white video signal
Video output (DG)	DG		3.5	8	%	P = 90 dB μ ,AM 87.5% MOD,
Video output (DP)	DP		1	5	deg	3-step video signal
Sound trap attenuation	G <sub>VO4.5</sub>	-	45		dB	20LOG(VO4.5M/VO0.2M)
920 kHz beat level	920	_	46	—	dB	P=0, P/C=4, P/S=14dB
Video output S/N	S/N <sub>v</sub>	47	53	-	dB	$P = 90 \text{ dB } \mu$ , 100% white
White noise threshold voltage	V <sub>WTH</sub>		3.8	-	V	CW = 70 dB μ
White noise clamped voltage	V <sub>WCL</sub>	-	2.5		V	Change in frequency
Black noise threshold voltage	VBTH		1.6	_	V	and pin 23 voltage
Black noise clamped voltage	VBCL		2.5	-	V	
RF AGC max. sink current	PaSi	0.4	1.0	-	mA	$CW = 100 dB \mu AGC_{ADJ} = 13k$
RF AGC delay point	VRFAGC	83	88	93	dB	AGCadj=13k
(AFT)						
Maximum AFT voltage	V <sub>P10MAX</sub>	4.0	4.5	—	. V	CW=45.25MHz
Minimum AFT voltage	<b>VPIOMIN</b>	—	0.1	1.0	V	CW=46.25MHz
AFT detection sensitivity	Sτ	-125	±62.5	+125	kHz	-
VCO free-run frequency	Fvco	-250	±125	+250	kHz	—
(PLL)						
PLL capture range 1	fcu	+1.0	+2.0		MHz	CW=80dB µ
PLL capture range 2	fcL	-	-2.0	-1.0	MHz	Frequency change
PLL lock range 1	fLυ	+1.0	+2.0	-	MHz	
PLL lock range 2	fLL	—	-2.0	-1.0	MHz	
VCO control sensitivity	β	—	9.0	Ι	kHz/mV	
(SIF) P=45.75MHz/90dB µ	: S≕41.2	5MHz/70	dB µ			
Input sensitivity	V <sub>SMIN</sub>	_	24	39	dB μ	fm=400Hz, △f=25kHz
SIF max. allowable input level	VSMAX	75	-	-	dB μ	5% distortion
FM detection output level	Vso	330	490	660	mVrms	fm=400Hz,
Audio output S/N	SNAF	_	55		dB	fm=400Hz, △f=25kHz
Audio output distortion	THD	_	0.3	1.5	%	fm=400Hz, △f=25kHz
AMR	AMR	-	53	_	dB	
Muted video output voltage	VVMUTE	-	2.5	_	V	VP14=GND
Muted audio output voltage	VSMUTE	_	2.2	-	V	VP14=GND
Initial muting voltage	V <sub>14MUTE</sub>	-	-	0.3	V	
VO4.5 M output level	V <sub>VD4.5M</sub>	10	20	50	mV <sub>PP</sub>	P=90dB $\mu$ P/S=20dB, when using FET prob
4 MHz oscillator input	Vosc	25	- 1	100	mV <sub>PP</sub>	· · · ·

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#### Measurement circuit



Fig.1

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## Application example



Fig.2

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# External dimensions (Units: mm)





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#### Notes

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