NiCd or NiMH Gas Gauge Module with Charge-Control Output

Features

 Complete bq2014 Gas Gauge solution for NiCd or NiMH battery packs

JITRODE

- ► Charge-control output allows communication to external charge controller (bq2004)
- > Battery information available over a single-wire bidirectional serial port
- ► Battery state-of-charge monitoring for 4- to 12-cell series applications
- On-board regulator allows direct connection to the battery
- ▶ "L" version includes push-button activated LEDs to display state-of-charge information
- > Nominal capacity and cell chemistry pre-configured
- > Compact size for battery pack integration

General Description

The bq2114 Gas Gauge Module provides a complete and compact solution for capacity monitoring of NiCd and NiMH battery packs. Designed for battery pack integration, the bq2114 incorporates a bq2014 Gas Gauge IC, a current sense resistor, and all other components necessary to accurately monitor and display the capacity of 4 to 12 series cells. The bq2114L includes five surfacemounted LEDs to display remaining capacity in 20% increments of the learned capacity (relative mode). The LEDs are activated with the onboard push-button switch.

Contacts are provided on the bq2114 for direct connection to the battery stack (BAT+, BAT-), the serial communications port (DQ), the empty indicator (EMPTY), and the charge control output (CHG). Please refer to the bq2014 data sheet for the specifics on the operation of the Gas Gauge.

Unitrode configures the bq2114 based on the information requested in Table 1. The configuration defines the number of series cells, the nominal battery pack capacity, and the self-discharge rate.



A module development kit is also available for the bq2114. The bq2114B-KT or bq2114LB-KT includes one configured module and the following:

- 1) An interface board that allows connection to the serial port of an AT-compatible computer.
- 2) Menu-driven software with the bq2114 to display charge/discharge activity and to allow user interface to the bq2014 from any standard DOS PC.
- 3) Source code for the TSR.

Pin Descriptions

- P1 DQ/Serial communication port
- P2 BAT+/Battery positive/pack positive
- P3 CHG/Charge control output
- P4 EMPTY/Empty indicator output
- P5 GND/Ground
- P6 PACK-/Pack negative
- P7 BAT-/Battery negative



Figure 1. Module Connection Diagram



Contact:		Phone:			
Address:					
Sales Contact:					
Number of series battery cells (4–12)					
Battery type (NiCd or NiMH)					
Battery pack capacity (mAh)					
Discharge rate into load (3.0A max.)	Min	Avg	Max		
Charge rate (3.0A max.)					
LEDs and switch (Y/N)					
FAE approval:		Date:			

bq2114 Schematic



bq2114 Board



Symbol	Parameter	Minimum	Maximum	Unit	Conditions
V _{CC}	Relative to VSS	-0.3	+7.0	V	bq2014
All other pins	Relative to VSS	-0.3	+7.0	V	bq2014
PCD	Continuous sense resis-	-	3	W	Thru-hole sense resistor
	tor power dissipation	-	1	W	Surface-mount sense resistor
I _{CHG}	Continuous charge/ discharge current	-	3.0	А	
TOPR	Operating temperature	0	+70	°C	Commercial
T _{STR}	Storage Temperature	-40	+85	°C	

Absolute Maximum Ratings

Note: Permanent device damage may occur if **Absolute Maximum Ratings** are exceeded. Functional operation should be limited to the Recommended DC Operating Conditions detailed in this data sheet. Exposure to conditions beyond the operational limits for extended periods of time may affect device reliability.

Symbol	Parameter	Minimum	Typical	Typical Maximum		Conditions/Notes
NumCell	Number of series cells in battery pack	4	-	12	-	
BAT+	Positive terminal of pack	GND	NumCell * 1.2V	NumCell * 1.8V	v	
BAT-	Negative terminal of pack	GND - 0.3	-	GND+2.0	v	
I _{CC}	Supply current at BAT+ terminal (no external loads)	-	200	300	μΑ	
R _{DQ}	Internal pull-down	500k	-	-	Ω^1	
IOL	Open-drain sink current DQ, EMPTY, CHG	-	-	5.0	mA ¹	
VOL	Open-drain output low, DQ, EMPTY, CHG	-	-	0.5	V1	IOL < 5mA
VIHDQ	DQ input high	2.5	-	-	V1	
VILDQ	DQ input low	-	-	0.8	V1	
Vos	Voltage offset	-	-	150	μV^1	

DC Electrical Characteristics (T_A = T_{OPR})

Note: 1. Characterized on PCB, IC 100% tested.

Symbol	Parameter	Minimum	Typical	Maximum	Unit	Notes
VEDVF	Final empty warning	0.93	0.95	0.97	V	BAT+/NumCell ¹
$V_{\rm EDV1}$	First empty warning	1.03	1.05	1.07	V	BAT+/NumCell ¹
V _{MCV}	Maximum single-cell voltage	2.20	2.25	2.30	V	BAT+/NumCell ¹
VSRO	Sense resistor range	-300	-	+2000	mV	$V_{SR} + V_{OS}^2$
VSRQ	Valid charge	375	-	-	μV	$V_{SR} + V_{OS} ^{2, 3}$
V _{SRD}	Valid discharge	-	-	-300	μV	V_{SR} + V_{OS} ² , ³

DC Voltage Thresholds (TA = TOPR)

Notes: 1. At SB input of bq2014.

2. At SR input of bq2014.

3. Default value; value set in DMF register.

Data Sheet Revision History

Change No.	Page No.	Description			
1	2	Updated Table 1 to include 3.0A limit			
1	5	Added 3.0A maximum continuous charge/discharge current specification			

Note: Change 1 = May 1999 B changes from Sept. 1996.

Ordering Information



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