

## CD4072BM/CD4072BC Dual 4-Input OR Gate, CD4082BM/CD4082BC Dual 4-Input AND Gate

### General Description

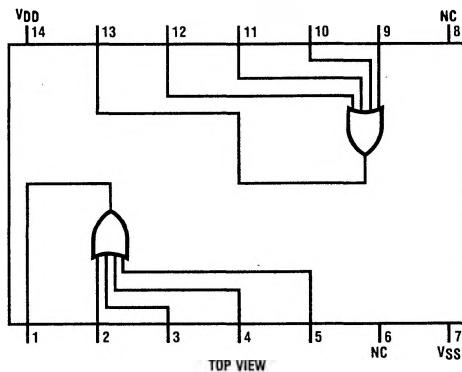
These dual gates are monolithic complementary MOS (CMOS) integrated circuits constructed with N-and P-channel enhancement mode transistors. They have equal source and sink current capabilities and conform to standard B series output drive. The devices also have buffered outputs which improve transfer characteristics by providing very high gain. All inputs are protected against static discharge with diodes to  $V_{DD}$  and  $V_{SS}$ .

### Features

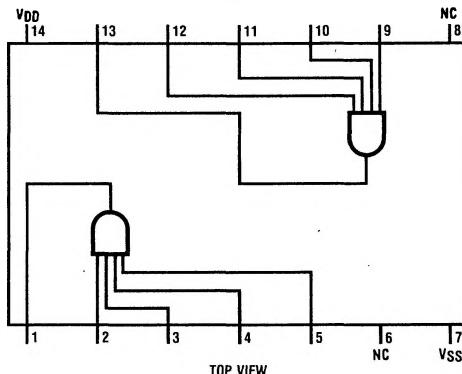
- Wide supply voltage range 3.0V to 15V
- High noise immunity 0.45  $V_{DD}$  (typ.)
- Low power TTL fanout of 2 driving 74L
- compatibility or 1 driving 74LS
- 5V-10V-15V parametric ratings
- Symmetrical output characteristics
- Maximum input leakage 1 $\mu$ A at 15V over full temperature range

### Connection Diagram

CD4072BM/CD4072BC



CD4082BM/CD4082BC



**Absolute Maximum Ratings** (Notes 1 and 2)

V <sub>DD</sub> Supply Voltage	-0.5 V to +18 V
V <sub>IN</sub> Input Voltage	-0.5 to V <sub>DD</sub> 0.5 V
T <sub>S</sub> Storage Temperature Range	-65°C to +150°C
P <sub>D</sub> Package Dissipation	500 mW
T <sub>L</sub> Lead Temperature (soldering, 10 seconds)	300°C

**Recommended Operating Conditions** (Note 2)

V <sub>DD</sub> Supply Voltage	3.0 to 15 V
V <sub>IN</sub> Input Voltage	0 V to V <sub>DD</sub> V
T <sub>A</sub> Operating Temperature Range	
CD4072BM, CD4082BM	-55°C to +125°C
CD4072BC, CD4082BC	-40°C to +85°C

**DC Electrical Characteristics** (Note 2) — CD4072BM, CD4082BM

Parameter	Conditions	-55°C		25°C			125°C		Units
		Min.	Max.	Min.	Typ.	Max.	Min.	Max.	
I <sub>DD</sub> Quiescent Device Current	V <sub>DD</sub> = 5.0 V		0.25		0.004	0.25		7.5	μA
	V <sub>DD</sub> = 10 V		0.5		0.005	0.5		15	μA
	V <sub>DD</sub> = 15 V		1.0		0.006	1.0		30	μA
V <sub>OL</sub> Low Level Output Voltage	V <sub>DD</sub> = 5.0 V		0.05		0	0.05		0.05	V
	V <sub>DD</sub> = 10 V		0.05		0	0.05		0.05	V
	V <sub>DD</sub> = 15 V		0.05		0	0.05		0.05	V
V <sub>OH</sub> High Level Output Voltage	V <sub>DD</sub> = 5.0 V	4.95		4.95	5.0		4.95		V
	V <sub>DD</sub> = 10 V	9.95		9.95	10		9.95		V
	V <sub>DD</sub> = 15 V	14.95		14.95	15		14.95		V
V <sub>IL</sub> Low Level Input Voltage	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 0.5 V or 4.5 V		1.5		2.25	1.5		1.5	V
	V <sub>DD</sub> = 10 V, V <sub>O</sub> = 1.0 V or 9.0 V		3.0		4.50	3.0		3.0	V
	V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V or 13.5 V		4.0		6.75	4.0		4.0	V
V <sub>IH</sub> High Level Input Voltage	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 0.5 V or 4.5 V	3.5		3.5	2.75		3.5		V
	V <sub>DD</sub> = 10 V, V <sub>O</sub> = 1.0 V or 9.0 V	7.0		7.0	5.50		7.0		V
	V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V or 13.5 V	11.0		11.0	8.25		11.0		V
I <sub>OL</sub> Low Level Output Current	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 0.4 V	0.64		0.51	0.88		0.36		mA
	V <sub>DD</sub> = 10 V, V <sub>O</sub> = 0.5 V	1.6		1.3	2.2		0.90		mA
	V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V	4.2		3.4	8.0		2.4		mA
I <sub>OH</sub> High Level Output Current	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 4.6 V	-0.64		-0.51	-0.88		-0.36		mA
	V <sub>DD</sub> = 10 V, V <sub>O</sub> = 9.5 V	-1.6		-1.3	-2.2		-0.90		mA
	V <sub>DD</sub> = 15 V, V <sub>O</sub> = 13.5 V	-4.2		-3.4	-8.0		-2.4		mA
I <sub>IN</sub> Input Current	V <sub>DD</sub> = 15 V, V <sub>IN</sub> = 0 V		-0.10		-10 <sup>-5</sup>	-0.10		-1.0	μA
	V <sub>DD</sub> = 15 V, V <sub>IN</sub> = 15 V		0.10		10 <sup>-5</sup>	0.10		1.0	μA

Note 1: "Absolute Maximum Ratings" are those beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Recommended Operating Conditions" and "Electrical Characteristics" provide conditions for actual device operation.

Note 2: V<sub>SS</sub> = 0 V unless otherwise specified.

**DC Electrical Characteristics** (Note 2) — CD4072BC, CD4082BC

Parameter	Conditions	-40°C		25°C		85°C		Units	
		Min.	Max.	Min.	Typ.	Max.	Min.		
I <sub>DD</sub> Quiescent Device Current	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V			1.0 2.0 4.0	0.004 0.005 0.006	1.0 2.0 4.0	7.5 15 30	μA μA μA	
V <sub>OL</sub> Low Level Output Voltage	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V			0.05 0.05 0.05	0 0 0	0.05 0.05 0.05	0.05 0.05 0.05	V V V	
V <sub>OH</sub> High Level Output Voltage	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V	4.95 9.95 14.95		4.95 9.95 14.95	5.0 10 15		4.95 9.95 14.95	V V V	
V <sub>IL</sub> Low Level Input Voltage	V <sub>DD</sub> = 5 V, V <sub>O</sub> = 0.5 V or 4.5 V V <sub>DD</sub> = 10 V, V <sub>O</sub> = 1.0 V or 9.0 V V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V or 13.5 V			1.5 3.0 4.0	2.25 4.50 6.75	1.5 3.0 4.0	1.5 3.0 4.0	V V V	
V <sub>IH</sub> High Level Input Voltage	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 0.5 V or 4.5 V V <sub>DD</sub> = 10 V, V <sub>O</sub> = 1.0 V or 9.0 V V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V or 13.5 V	3.5 7.0 11.0		3.5 7.0 11.0	2.75 5.50 8.25		3.5 7.0 11.0	V V V	
I <sub>OL</sub> Low Level Output Current	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 0.4 V V <sub>DD</sub> = 10 V, V <sub>O</sub> = 0.5 V V <sub>DD</sub> = 15 V, V <sub>O</sub> = 1.5 V	0.52 1.3 3.6		0.44 1.1 3.0	0.88 2.2 8.0		0.36 0.90 2.4	mA mA mA	
I <sub>OH</sub> High Level Output Current	V <sub>DD</sub> = 5.0 V, V <sub>O</sub> = 4.6 V V <sub>DD</sub> = 10 V, V <sub>O</sub> = 9.5 V V <sub>DD</sub> = 15 V, V <sub>O</sub> = 13.5 V	-0.52 -1.3 -3.6		-0.44 -1.1 -3.0	-0.88 -2.2 -8.0		-0.36 -0.90 -2.4	mA mA mA	
I <sub>IN</sub> Input Current	V <sub>DD</sub> = 15 V, V <sub>IN</sub> = 0 V V <sub>DD</sub> = 15 V, V <sub>IN</sub> = 15 V			-0.3 0.3	-10 <sup>-5</sup> 10 <sup>-5</sup>	-0.3 0.3		-1.0 1.0	μA μA

**AC Electrical Characteristics** T<sub>A</sub> = 25°C, C<sub>L</sub> = 50 pF, unless otherwise noted.

Parameter	Conditions	Min.	Typ.	Max.	Units
t <sub>PHL</sub> Propagation Delay, High to Low Level	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V		125 60 45	250 100 70	ns ns ns
t <sub>PLH</sub> Propagation Delay, Low to High Level	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V		125 60 45	250 100 70	ns ns ns
t <sub>THL</sub> , t <sub>TLH</sub> Transition Time	V <sub>DD</sub> = 5.0 V V <sub>DD</sub> = 10 V V <sub>DD</sub> = 15 V		100 50 40	200 100 80	ns ns ns
C <sub>IN</sub> Average Input Capacitance (Note 3)	Any Input		5.0	7.5	pF
C <sub>PD</sub> Power Dissipation Capacity (Note 4)	Any Gate		20		pF

Note 3: Capacitance is guaranteed by periodic testing.

Note 4: C<sub>PD</sub> determines the no load AC power consumption of any CMOS device. For complete explanation see 54C/74C Family Characteristics, Application Note AN-90.