## 18 W BTL Audio Power Amplifier

# **HITACHI**

### **Description**

The HA13118 is power IC designed for component car stereo amplifiers. At 13.2 V to 4 load, this power IC provides an output power of 18W with 10% distortion.

It is easy to design as this IC employs internal each protection circuit and the new small package.

### **Features**

- Small outline package, easy to mount
- Internal each protection circuits
  - Surge protection circuit
  - Thermal shut-down circuit
  - Ground fault protection circuit
  - Power supply fault protection circuit

### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Rating	Unit	Note
Operating supply voltage	V <sub>cc</sub>	18	V	
DC supply voltage	V <sub>cc</sub> (DC)	26	V	1
Peak supply voltage	V <sub>cc</sub> (peak)	50	V	2
Output current	lo (peak)	4	А	
Power dissipation	P <sub>T</sub>	15	W	
Thermal resistance	θj – c	3.5	°C/W	
Junction temperature	Tj	150	°C	
Operating temperature	Topr	-30 to +80	°C	
Storage temperature	Tstg	-55 to +125	°C	

Notes: 1. Value at t = 30 sec.

2. Value at width tw = 200 ms and rise time tr = 1 ms.



# Electrical Characteristics (V $_{CC}=13.2~V,~f=1~kHz,~R_{L}=4~,~Ta=25~^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Quiescent current	I <sub>Q</sub>	40	80	160	mA	Vin = 0
Input bias voltage	$V_{B}$	_	20	40	mV	Vin = 0
Output offset voltage	$\Delta V_{Q}$	_	_	+330	mV	Vin = 0
Voltage gain	G <sub>v</sub>	53	55	57	dB	Vin = −55 dBm
Output power	Pout	15	18	_	W	THD = 10 % R <sub>L</sub> = 4
		_	11	_	<del></del>	$R_L = 8$
Total harmonic distortion	THD	_	0.2	1.0	%	Pout = 1.5 W
Output noise voltage	WBN	_	1.0	2.0	mV	Rg = 10 k , BW = 20 Hz 20 kHz
Supply voltage rejection ratio	SVR	33	44	_	dB	f = 500 Hz
Input resistance	Rin	20	30	40	k	
Rolloff frequency	f <sub>L</sub>	_	20	_	Hz	$\Delta Gv = -3 \text{ dB}$ Low
	f <sub>H</sub>	10	20	40	kHz	from f = 1 kHz Ref. High

### **Block Diagram**

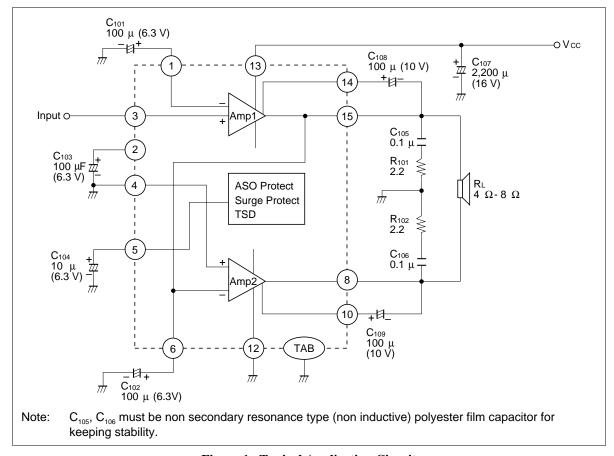


Figure 1 Typical Application Circuit

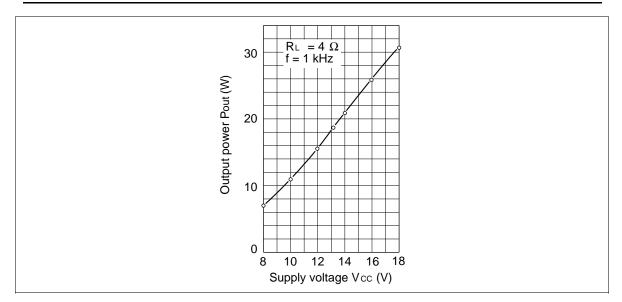


Figure 2 Output Power vs. Supply Voltage

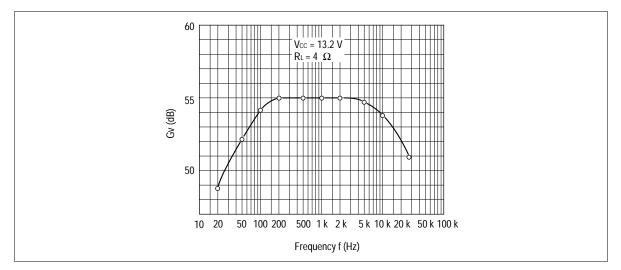


Figure 3 Voltage Gain vs. Frequency

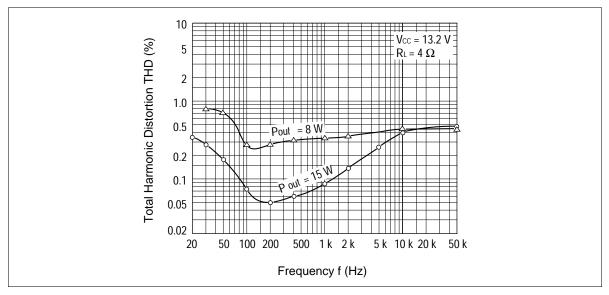


Figure 4 Total Harmonic Distortion vs. Frequency

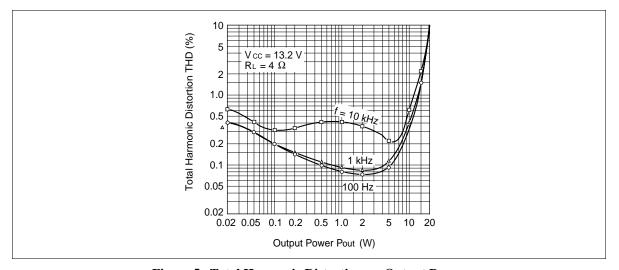


Figure 5 Total Harmonic Distortion vs. Output Power

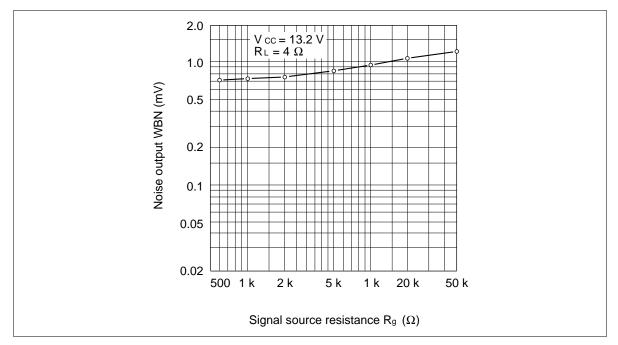


Figure 6 Noise Output vs. Signal Source Resistance

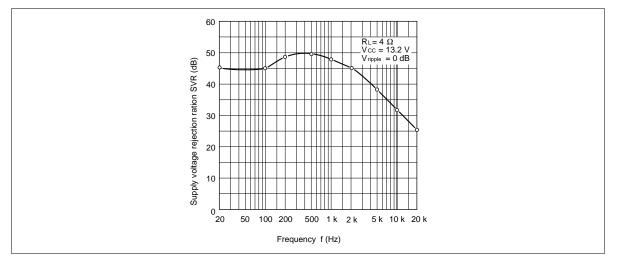
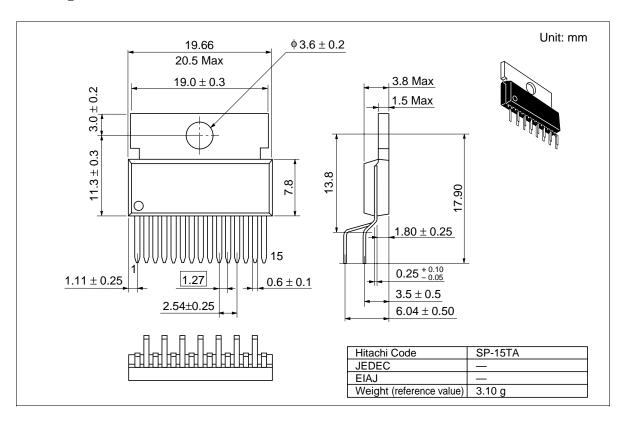


Figure 7 Supply Voltage Rejection Ratio vs. Frequency

## **Package Dimensions**



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