

# KA2S1265

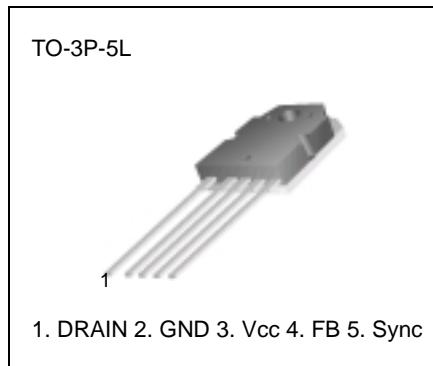
## Fairchild Power Switch(PS)

### Features

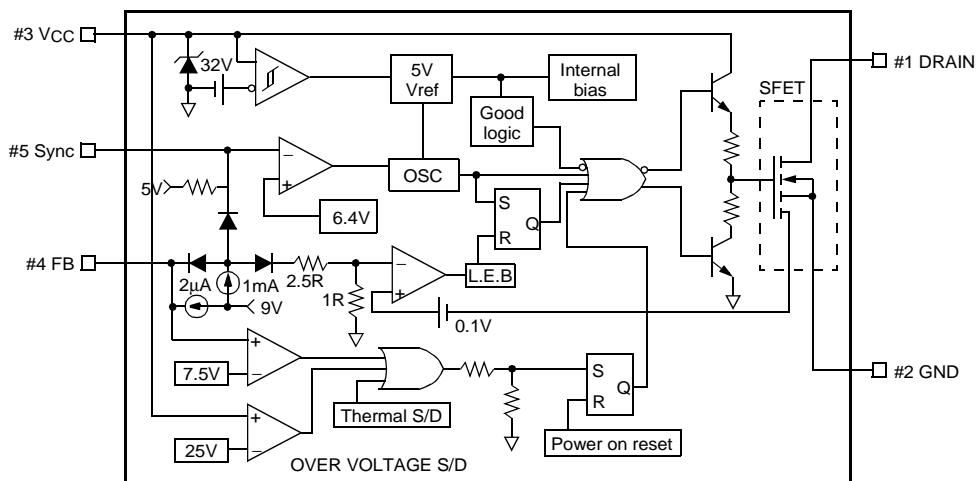
- Wide operating frequency range up to (150kHz)
- Pulse by pulse over current limiting
- Over load protection
- Over voltage protection (Min. 23V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- External sync terminal
- Latch up Mode

### Description

The SPS product family is specially designed for an offline SMPS with minimal external components. The SPS consist of high voltage power SenseFET and current mode PWM Controller IC. control IC features a trimmed oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, temperature compensated precision current sources for loop compensation and fault protection circuit. Compared to discrete MOSFET and controller or RCC switching converter solution, a SPS can reduce total component count, design size, weight and at the same time increase & efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective Monitor power supply.



### Internal Block Diagram



## Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit
Maximum Drain voltage <sup>(1)</sup>	V <sub>D,MAX</sub>	650	V
Drain-Gate voltage ( $R_{GS}=1M\Omega$ )	V <sub>DGR</sub>	650	V
Gate-source (GND) voltage	V <sub>GS</sub>	$\pm 30$	V
Drain current pulsed <sup>(2)</sup>	I <sub>DM</sub>	48.0	ADC
Single pulsed avalanche energy <sup>(3)</sup>	E <sub>AS</sub>	785	mJ
Continuous drain current ( $T_C=25^\circ C$ )	I <sub>D</sub>	12	ADC
Continuous drain current ( $T_C=100^\circ C$ )	I <sub>D</sub>	8.4	ADC
Maximum Supply voltage	V <sub>CC,MAX</sub>	30	V
Input voltage range	V <sub>FB</sub>	-0.3 to V <sub>SD</sub>	V
Total power dissipation	P <sub>D</sub>	269	W
	Derating	2.17	W/ $^\circ C$
Operating ambient temperature	T <sub>A</sub>	-25 to +85	$^\circ C$
Storage temperature	T <sub>STG</sub>	-55 to +150	$^\circ C$

**Notes:**

- 1.T<sub>j</sub>=25°C to 150°C
- 2.Repetitive rating: Pulse width limited by maximum junction temperature
- 3.L=10mH, V<sub>DD</sub>=50V, R<sub>G</sub>=27Ω, starting T<sub>j</sub>=25 °C

## Electrical Characteristics (SFET part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BVDSS	VGS=0V, ID=50µA	650	-	-	V
Zero gate voltage drain current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	µA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	mA
Static drain-source on resistance <sup>(note)</sup>	RDS(ON)	VGS=10V, ID=6.0A	-	0.72	-	W
Forward transconductance <sup>(note)</sup>	gfs	VDS=50V, ID=6.0A	5.7	-	-	S
Input capacitance	Ciss	VGS=0V, VDS=25V, f=1MHz	-	2700	-	pF
Output capacitance	Coss		-	300	-	
Reverse transfer capacitance	Crss		-	61	-	
Turn on delay time	td(on)	VDD=0.5BVDSS, ID=12.0A (MOSFET switching time are essentially independent of operating temperature)	-	18	-	nS
Rise time	tr		-	37	-	
Turn off delay time	td(off)		-	88	-	
Fall time	tf		-	36	-	
Total gate charge (gate-source+gate-drain)	Qg	VGS=10V, ID=12.0A, VDS=0.5BVDSS (MOSFET switching time are essentially independent of operating temperature)	-	-	140	nC
Gate-source charge	Qgs		-	20	-	
Gate-drain (Miller) charge	Qgd		-	69	-	

**Note:**

Pulse test: Pulse width ≤ 300µS, duty cycle ≤ 2%

$$S = \frac{1}{R}$$

## Electrical Characteristics (CONTROL part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
<b>UVLO SECTION</b>						
Start threshold voltage	V <sub>START</sub>	-	14	15	16	V
Stop operating voltage	V <sub>STOP</sub>	After turn on	9	10	11	V
<b>OSCILLATOR SECTION</b>						
Initial accuracy	F <sub>OSC</sub>	T <sub>a</sub> =25°C	18	20	22	kHz
Frequency change with temperature <sup>(2)</sup>	ΔF/ΔT	-25°C≤T <sub>a</sub> ≤+85°C	-	±5	±10	%
Maximum duty cycle	D <sub>max</sub>	-	92	95	98	%
<b>FEEDBACK SECTION</b>						
Feedback source current	I <sub>FB</sub>	T <sub>a</sub> =25°C, V <sub>fb</sub> =GND	0.8	1	1.2	mA
Shutdown Feedback voltage	V <sub>SD</sub>	-	6.9	7.5	8.1	V
Shutdown delay current	I <sub>delay</sub>	T <sub>a</sub> =25°C, 5V≤V <sub>fb</sub> ≤V <sub>SD</sub>	1.4	1.8	2.2	μA
<b>SYNC. &amp; SOFT START SECTION</b>						
Soft start voltage	V <sub>SS</sub>	V <sub>FB</sub> =2V	4.7	5.0	5.3	V
Soft start current	I <sub>SS</sub>	Sync & S/S=GND	0.8	-	-	mA
Sync threshold voltage <sup>(3)</sup>	V <sub>SYTH</sub>	V <sub>fb</sub> =5V	6.0	6.4	6.8	V
<b>REFERENCE SECTION</b>						
Output voltage <sup>(1)</sup>	V <sub>ref</sub>	T <sub>a</sub> =25°C	4.80	5.00	5.20	V
Temperature Stability <sup>(1)(2)</sup>	V <sub>ref</sub> /ΔT	-25°C≤T <sub>a</sub> ≤+85°C	-	0.3	0.6	mV/°C
<b>CURRENT LIMIT (SELF-PROTECTION) SECTION</b>						
Peak Current Limit	I <sub>OVER</sub>	Max. inductor current	7.04	8.00	8.96	A
<b>PROTECTION SECTION</b>						
Thermal shutdown temperature (T <sub>j</sub> ) <sup>(1)</sup>	T <sub>SD</sub>	-	140	160	-	°C
<b>TOTAL DEVICE SECTION</b>						
Start Up current	I <sub>START</sub>	V <sub>CC</sub> =14V	0.1	0.3	0.55	mA
Operating supply current (control part only)	I <sub>OP</sub>	T <sub>a</sub> =25°C	6	12	18	mA
V <sub>CC</sub> zener voltage	V <sub>Z</sub>	I <sub>CC</sub> =20mA	30	32.5	35	V

**NOTE:**

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS(water test) process
3. The amplitude of the sync. pulse is recommended to be between 2V and 3V for stable sync. function.

## Typical Performance Characteristics

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

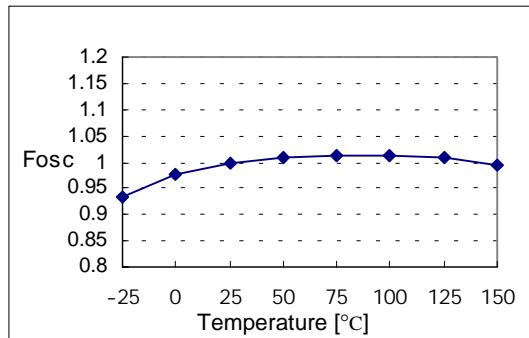


Figure 1. Operating Frequency

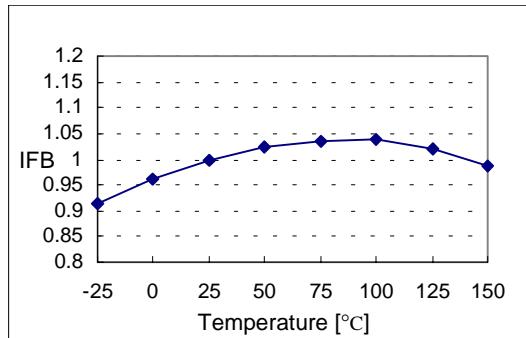


Figure 2. Feedback Source Current

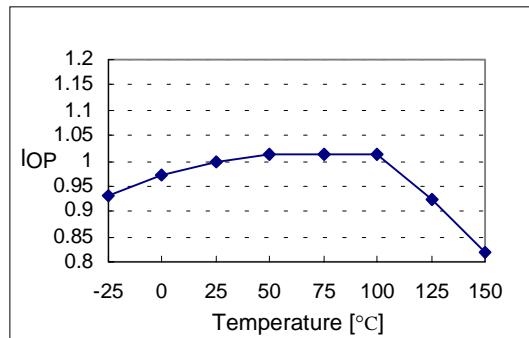


Figure 3. Operating Supply Current

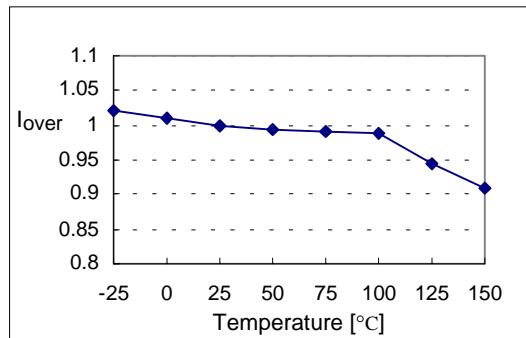


Figure 4. Peak Current Limit

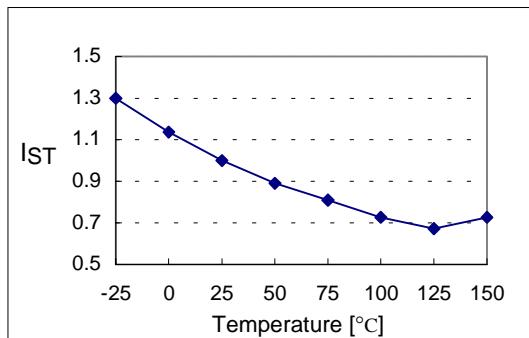


Figure 5. Start up Current

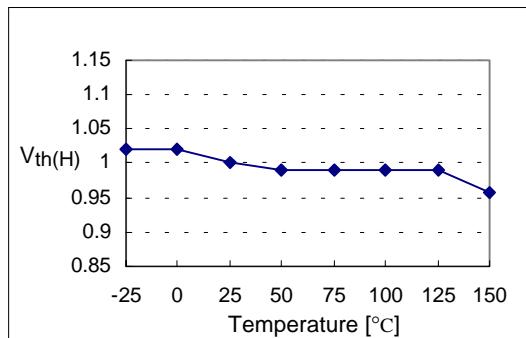


Figure 6. Start Threshold Voltage

## Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

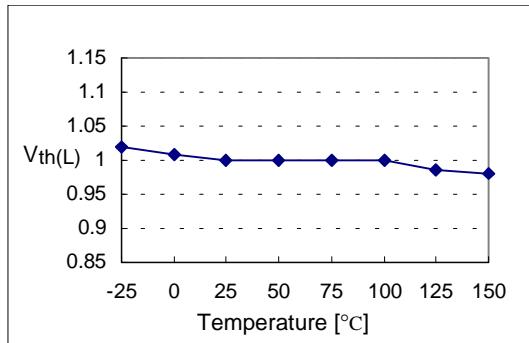


Figure 7. Stop Threshold Voltage

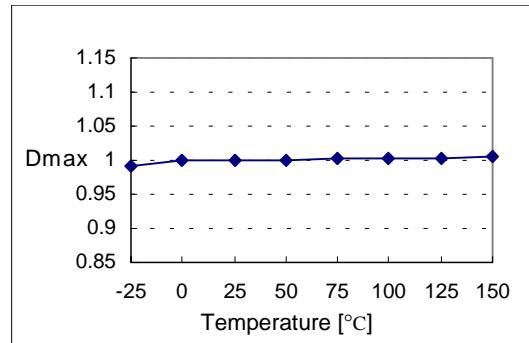


Figure 8. Maximum Duty Cycle

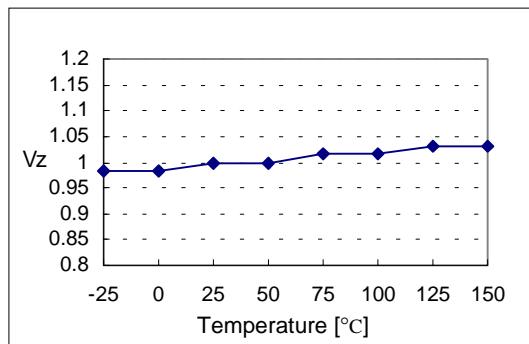


Figure 9. VCC Zener Voltage

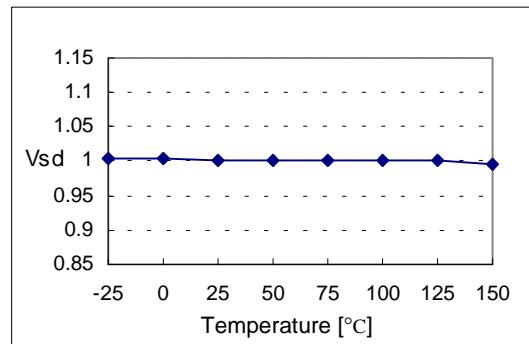


Figure 10. Shutdown Feedback Voltage

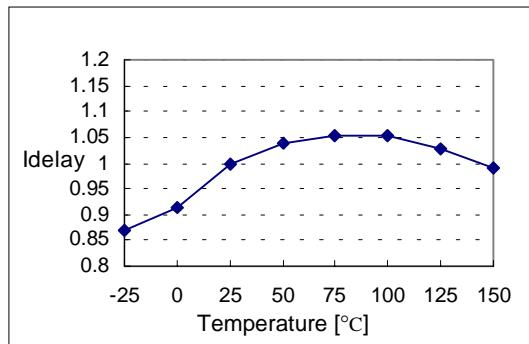


Figure 11. Shutdown Delay Current

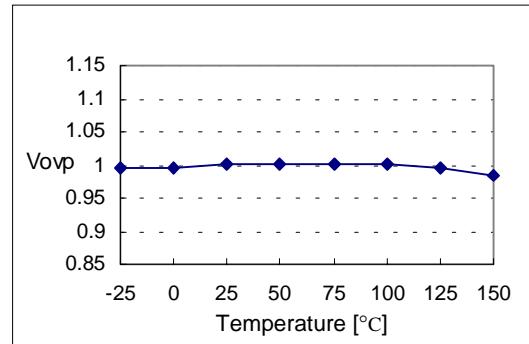


Figure 12. Over Voltage Protection

## Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

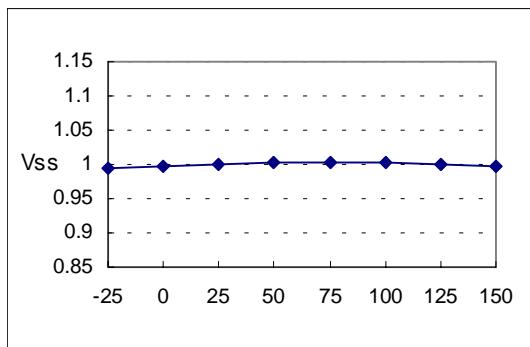


Figure13. Soft Start Voltage

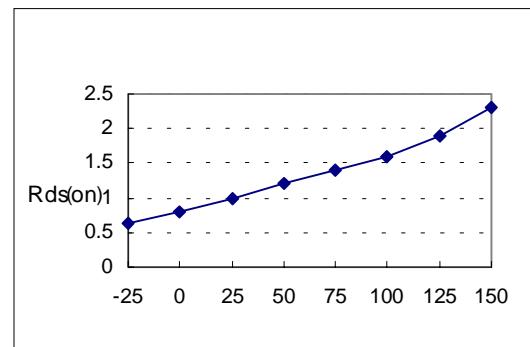
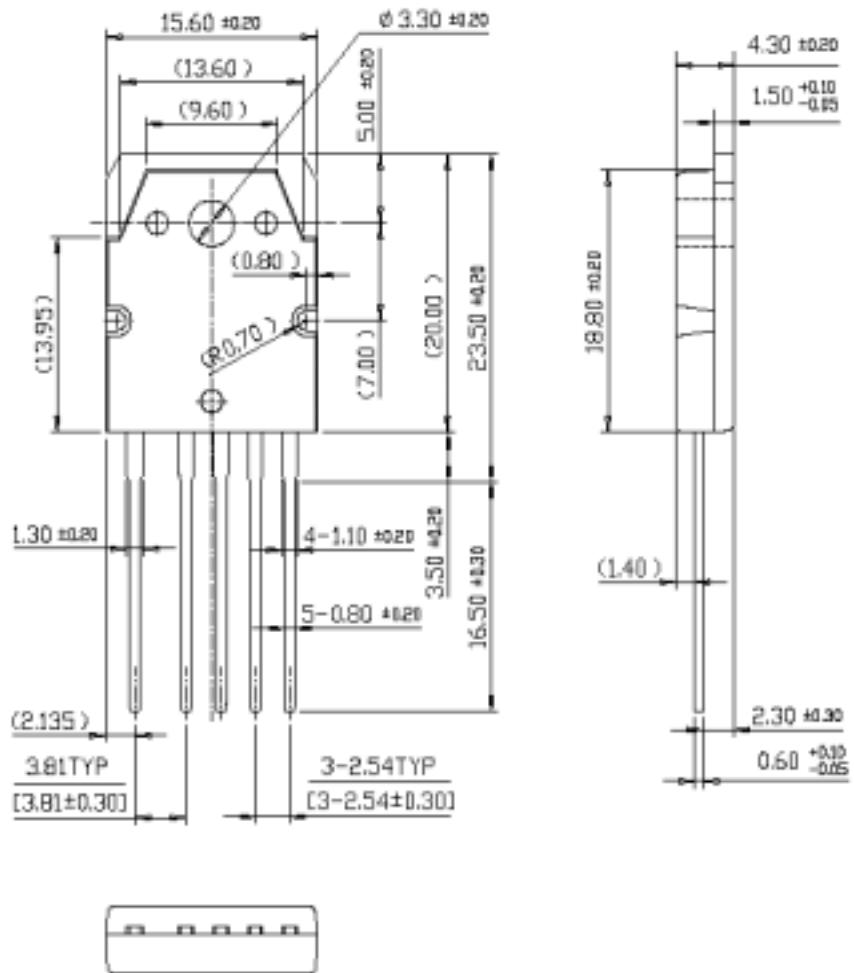
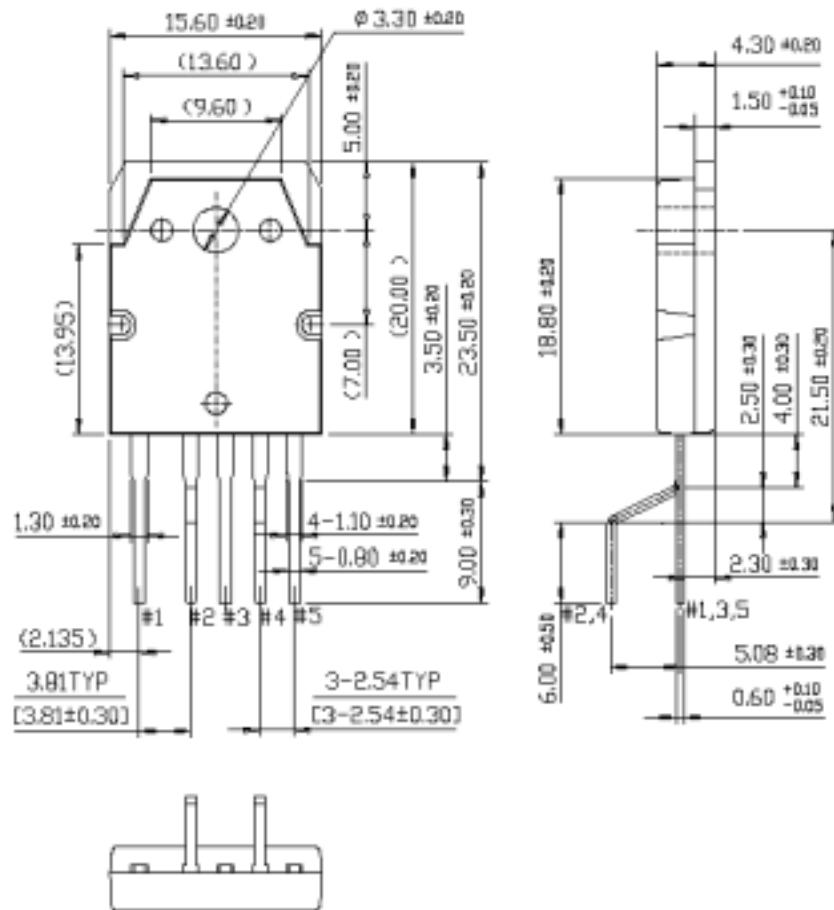


Figure 14. Static Drain-Source on Resistance

## Package Dimensions

TO-3P-5L



**Package Dimensions** (Continued)**TO-3P-5L (Forming)**

## Ordering Information

Product Number	Package	Rating	Operating Temperature
KA2S1265-TU	TO-3P-5L	650V,12A	-25°C to +85°C
KA2S1265-YDTU	TO-3P-5L(Forming)		

TU : Non Forming Type

YDTU : Forming Type



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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.