FAIRCHILD SEMICONDUCTOR®

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KA78RM33R
Low Dropout Voltage Regulator

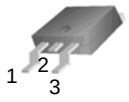
Features

- 0.5 A / 3.3 V Output Low-Dropout Voltage Regulator
- Low-Dropout Voltage (Max: 0.6 V)
- Over Current Protection, Thermal Shutdown
- SOA Protection, Short Circuit Protection

Description

The KA78RM33R is a low-dropout voltage regulator suitable for various electronic equipment. It provides constant voltage power source with surface mount type package (DPAK). The dropout voltage of KA78RM33R is below 0.6 V in full rated current 0.5A. This regulator has various functions such as an over current protection, a thermal shut down and the SOA (Safe operating Area) protection.

DPAK

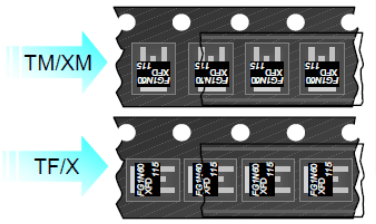


1. V_{IN} 2. GND 3. V_{OUT}

Ordering Information

Part Number	Operating Temperature Range	Top Mark	Package	Packing Method
KA78RM33RTF	-25 ~ +125°C	KA78RM33	DPAK	Tape and Reel
KA78RM33RTM		KA78RM33	DPAK	Tape and Reel

* Refer to below unit orientation figure for TM / TF suffix packing.



Block Diagram

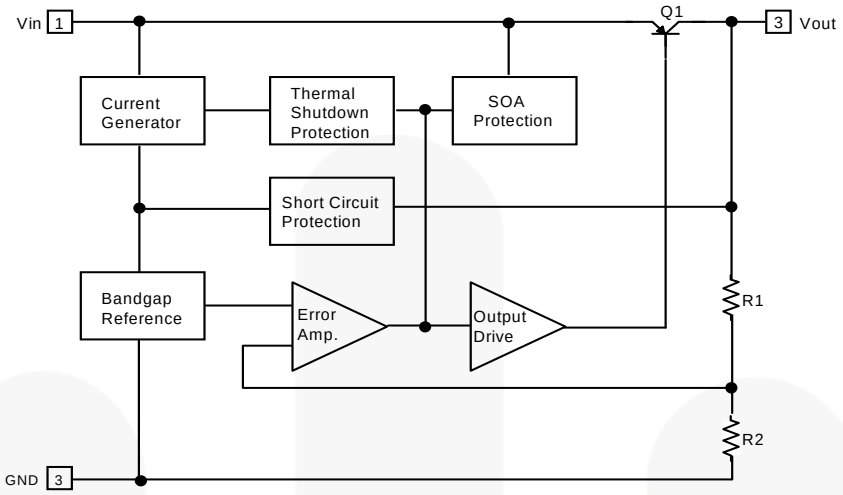


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Remark	Unit
V_{IN}	Input Voltage	20		V
I_O	Output Current	0.5		A
$R_{\theta JA}$	Thermal Resistance Junction-Air	110	No Heatsink	°C/W
P_D	Power Dissipation	Interally limited		
T_J	Junction Temperature	150		°C
T_{OPR}	Operating Temperature	-25 ~ +125		°C

Electrical Characteristics

Values are at T_A = 25°C, V_{IN} = 5 V, I_O = 0.25 A unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	I_O =10 mA	3.22	3.3	3.38	V
R_{LOAD}	Load Regulation	5 mA < I_O < 0.5 A		2	20	mV
R_{LINE}	Line Regulation	4.3 V < V_{IN} < 16 V		2	20	mV
R_R	Ripple Rejection Ratio	f = 120 Hz, V_{IN}=5 V ± 0.5 V_{RMS}	55			dB
V_{DROP}	Dropout Voltage	I_O = 0.5 A			0.6	V
I_Q	Quiescent Current	I_O = 0 A		5	10	mA
I_{PK}	Peak Current	V_{IN} = 5 V	0.5	1		A
V_N	Output Noise Voltage	10 Hz < f < 100 kHz		50		μV_{RMS}
$\Delta V_{OUT}/\Delta T$	Temperature Coefficient of Output Voltage	-25°C < T_J < 125°C, I_O = 100 mA		-0.2		mV /°C

Typical Application

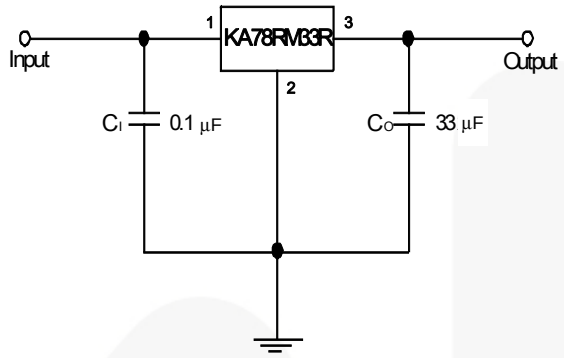


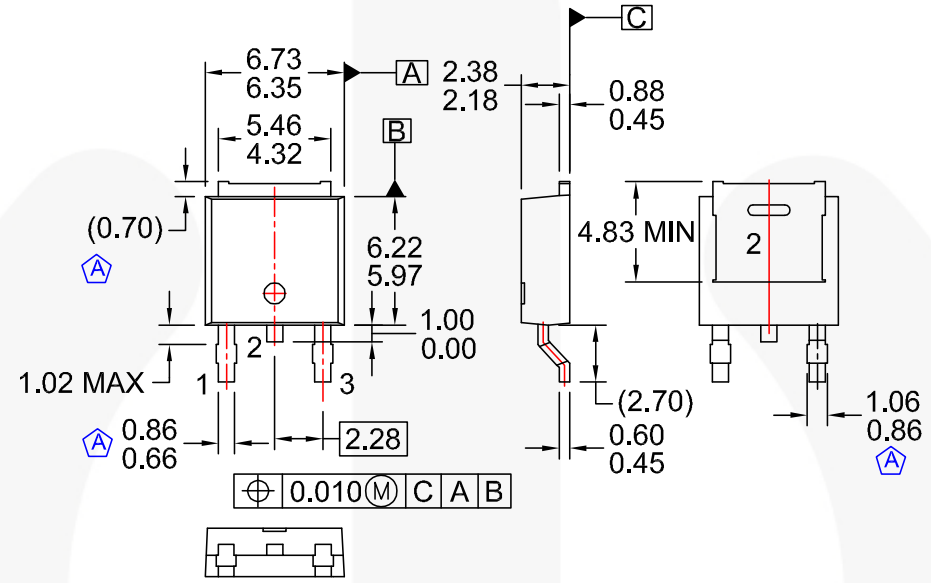
Figure 2. DC Parameters

Notes:

1. C_I is required if regulator is located an appreciable distance from power supply filter.
2. C_O improves stability and transient response.

Physical Dimensions

TO-252



NOTES: UNLESS OTHERWISE SPECIFIED

A) CONFORMS TO JEDEC TO-252 VARIATION AB EXCEPT WHERE NOTED

B) ALL DIMENSIONS ARE IN MILLIMETERS.

C) DRAWING CONFORMS TO ASME Y14.5M-1994

D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

E) FORMERLY NAMED BD1733

F) DRAWING FILE NAME: MKT-TO252D03REV1

Figure 3. 3Lead, TO252, Surface Mount Jedec TO-252 Variation AB, (D2PAK)

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Rev. I63