

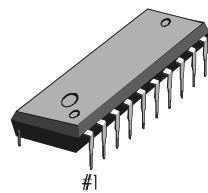
INTRODUCTION

The KA9270 is a monolithic integrated circuit designed for an audio filter. It is used in compact disc player, digital audio tape recorders, etc.

FEATURES

- Functions:
 - Buffer for impedance matching
 - Low pass filter
 - De - emphasis control
 - Mute control
 - Reference voltage circuit (1/2 V_{CC} AMP)
- Gain adjustable of audio output
- Minimum number of external parts required
- Recommand operation supply voltage range : 5.0 to 12.0 V

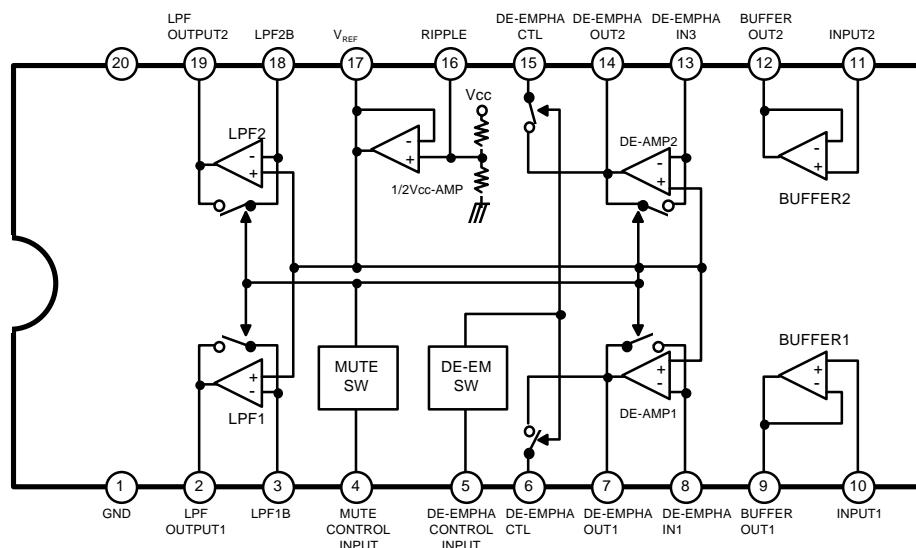
20-DIP-300A



ORDERING INFORMATION

Device	Package	Operating Temperature
KA9270	20-DIP-300A	20°C to + 75°C
KA9270D	20-SOP- 375	

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

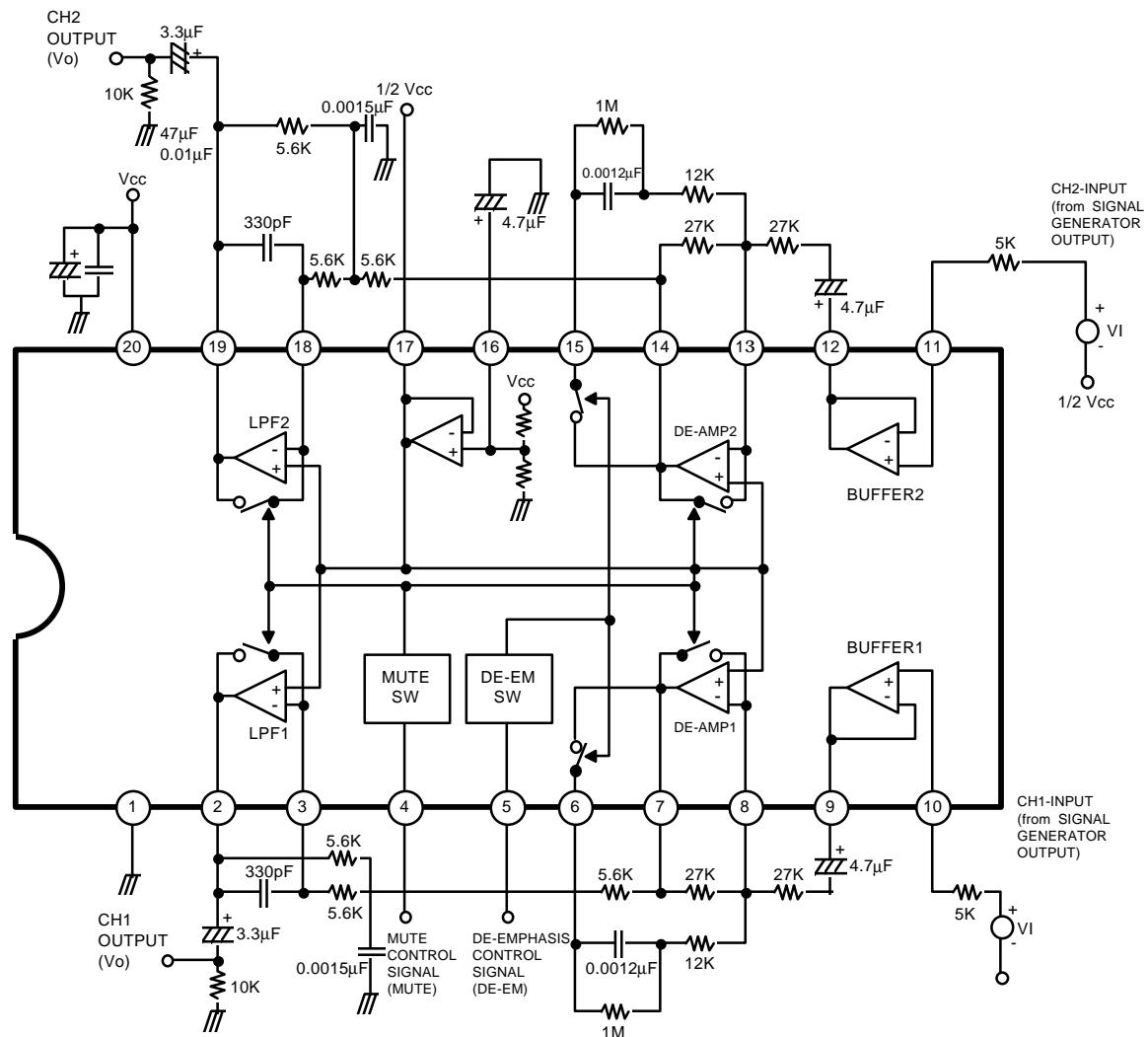
Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	16	V
Power Dissipation	P _D	550	mW
Operating Temperature	T _{OPR}	-20 ~ + 75	°C
Storage Temperature	T _{STG}	-45 ~ + 150	°C

ELECTRICAL CHARACTERISTICS(Ta = 25°C, V_{CC} = 8V, f = 1kHz, R_L = 10KΩ, De-emphasis; off, Mute; off, S1 & S2; off, unless otherwise specified)

Characteristic	Symbol	Test Conditions		Min	Typ	Max	Unit
Quiescent Circuit Current	I _{CC}	V _I = 0		1	4	6	mA
Maximum Output Voltage	V _{OM}	THD = 1%		1.8	2.1	—	Vrms
Total Harmonic Distortion	THD	V _O = 0 dBm	f = 100Hz	—	0.01	0.05	%
			f = 1kHz	—	0.01	0.05	
			f = 10kHz	—	0.05	0.1	
			f = 16kHz	—	0.1	0.2	
			f = 20kHz	—	0.1	0.2	
Frequency Characteristics	fv	V _O = 6 dBm	f = 100Hz	-0.1	0	0.1	dB
			f = 1kHz	0	0	0	
			f = 10kHz	-0.5	0	0.5	
			f = 16kHz	-1.0	0	1.0	
			f = 20kHz	-1.5	0	1.5	
Cross Talk	CT	V _O = 0dBm	f = 100Hz	70	80	—	dB
			f = 1kHz	65	75	—	
			f = 10kHz	60	65	—	
Signal to Noise Ratio	S/N	V _O = 0dBm, R _G = 600Ω 20kHz LPF		73	80	—	dB
Channel Balance	CB	V _O = 0dBm		-1.0	0	1.0	dB
Open Loop Gain	G _{VO}	V _I = 900mVrms		-2.6	-0.6	1.0	dB
Gain Adjusting Range	G _{VR}	V _I = 900mV, S1, S2; ON		4.5	6	—	dB
Mute Attenuation Ratio	ATT _{MUTE}	V _I = 900mV, Mute SW; ON		40	50	—	dB
De-emphasis	DE _{EMPH}	De-emphasis: ON	f = 1kHz	-0.87	-0.37	0.13	dB
			f = 5kHz	-6.03	-4.53	-3.03	
			f = 16kHz	-10.53	-9.03	-7.53	

NOTE: De-emphasis input conditions: V_O = 0dBm De-emphasis off position.

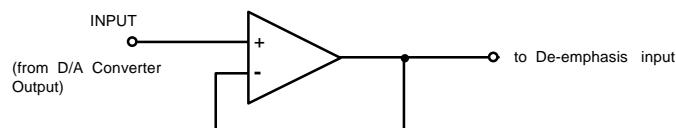
TEST CIRCUIT



APPLICATION INFORMATION

BUFFER2

It is used for impedance matching, between the D/A converter output and the de-emphasis input.



DE-EMPHASIS

De-emphasis operation condition

Control Input	De-emphasis Operation
High	ON
Low	OFF

De-emphasis characteristic when the de-emphasis is on.

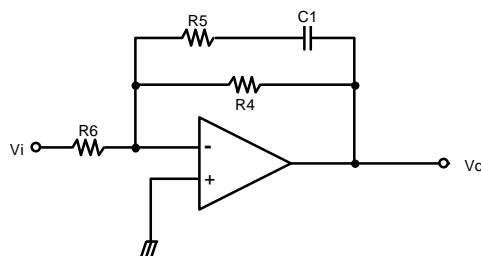


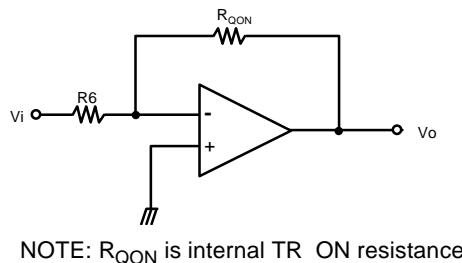
Figure 1. Equevalent Circuit of De-emphasis ON Mode

$$A_v \approx R_4 / R_6$$

$$T_1 = C_1 (R_4 + R_5)$$

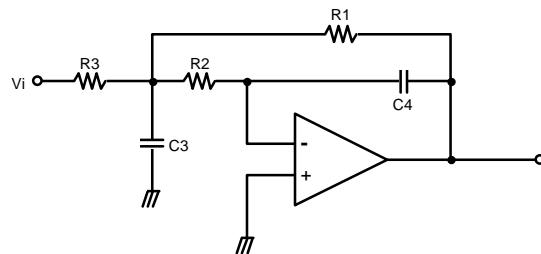
$$T_2 = C_1 \times R_5$$

The de-emphasis characteristics are dependent upon the external parts value.

MUTE**Figure 2. Equivalent Circuit of Mute Switch ON Mode**

Mute attenuation [M (att)] ratio is as follow:

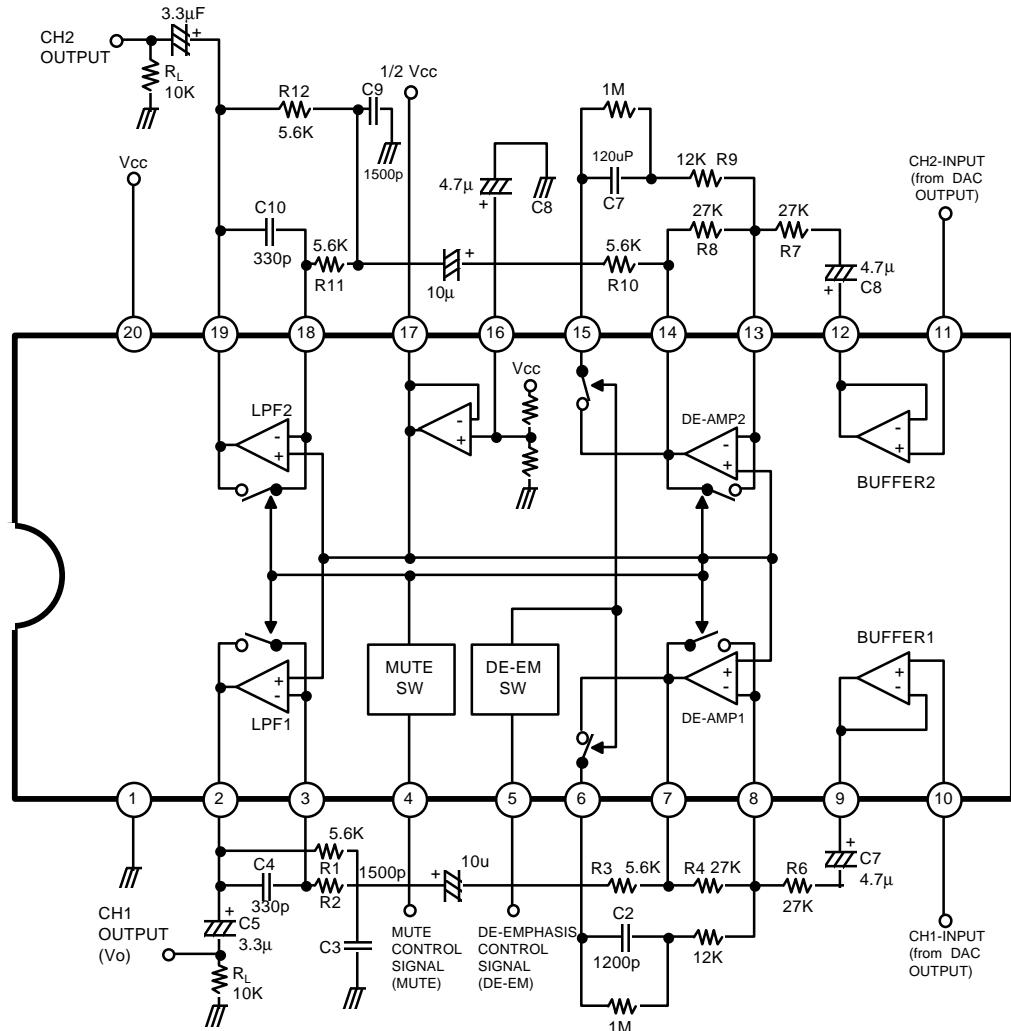
$$\begin{aligned} M (\text{att}) &= 20 \log \frac{V_o}{V_i} \\ &= 20 \log \frac{V_{QON}}{V_6} \text{ (dB)} \end{aligned}$$

LOW PASS FILTER**Figure 3. Equivalent Circuit of LPF**

Cut off frequency (F_C) is as follow:

$$f_C = \frac{1}{2\pi\sqrt{R_2R_1C_3C_4}} \text{ (Hz)}$$

APPLICATION CIRCUIT



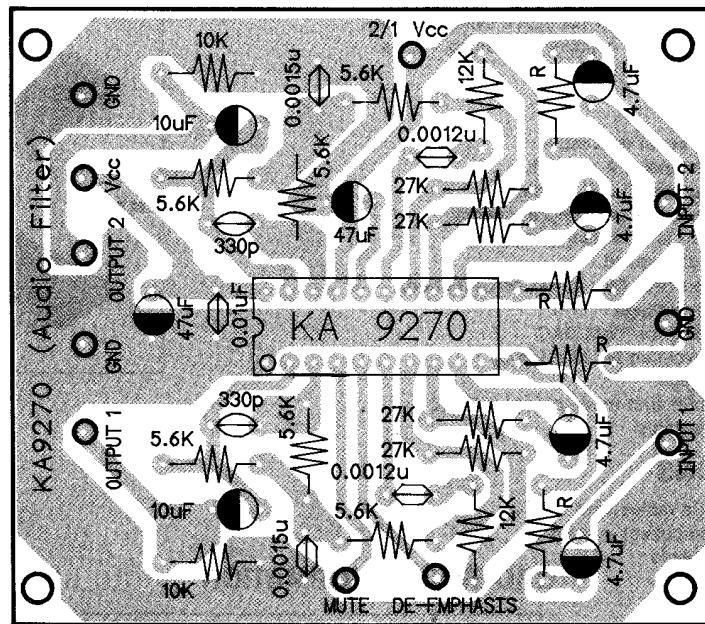
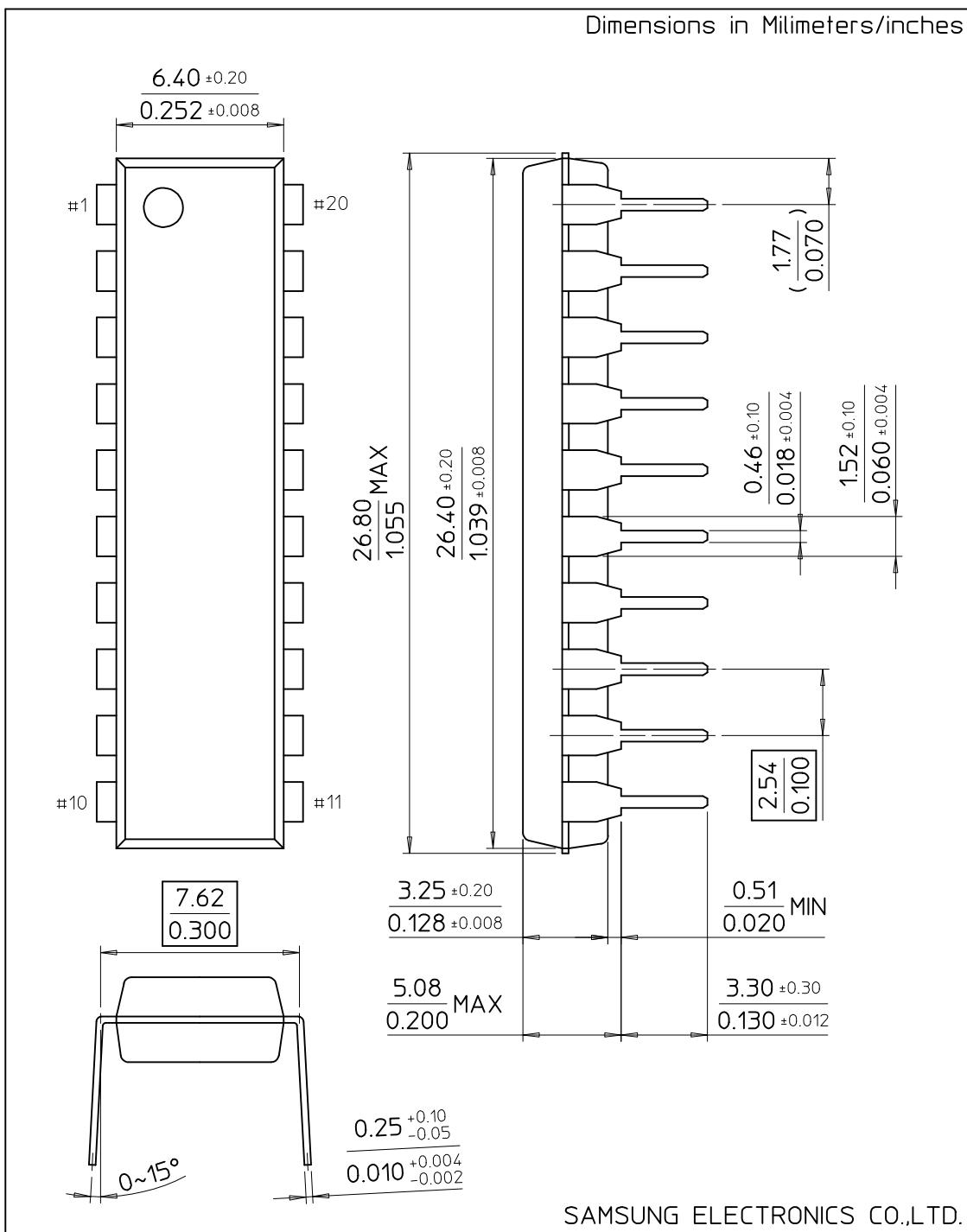


Figure 4. PCB PATTERN

20-DIP-300A

Dimensions in Millimeters/inches



20-SOP-375

