

LOW DROP DUAL POWER OPERATIONAL AMPLIFIERS

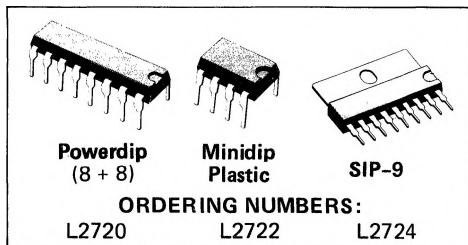
PRELIMINARY DATA

- OUTPUT CURRENT TO 1A
- OPERATES AT LOW VOLTAGES
- SINGLE OR SPLIT SUPPLY
- LARGE COMMON-MODE AND DIFFERENTIAL MODE RANGE
- LOW INPUT OFFSET VOLTAGE
- GROUND COMPATIBLE INPUTS
- LOW SATURATION VOLTAGE
- THERMAL SHUTDOWN
- CLAMP DIODE

The L2720, L2722 and L2724 are monolithic integrated circuits in powerdip, minidip and SIP-9 packages, intended for use as power operational amplifiers in a wide range of applications including servo amplifiers and power supplies.

They are particularly indicated for driving, inductive loads, as motor and finds applications in compact-disc VCR automotive, etc.

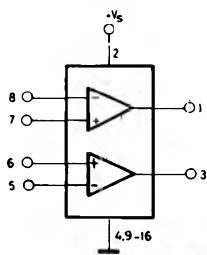
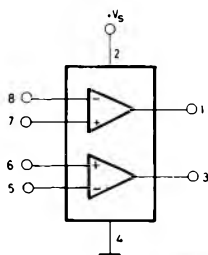
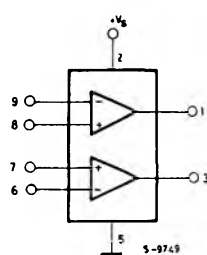
The high gain and high output power capability provide superior performance whatever an operational amplifier/power booster combination is required.



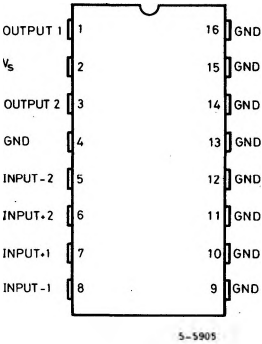
ABSOLUTE MAXIMUM RATINGS

V_s	Supply voltage	28	V
V_s	Peak supply voltage (50ms)	50	V
V_i	Input voltage	V_s	
V_i	Differential input voltage	$\pm V_s$	
I_o	DC output current	1	A
I_p	Peak output current (non repetitive)	1.5	A
P_{tot}	Power dissipation at $T_{amb} = 80^\circ\text{C}$ (L2720), $T_{amb} = 50^\circ\text{C}$ (L2722)	1	W
	$T_{case} = 75^\circ\text{C}$ (L2720)	5	W
	$T_{case} = 50^\circ\text{C}$ (L2724)	10	W
T_{stg}, T_j	Storage and junction temperature	-40 to 150	$^\circ\text{C}$

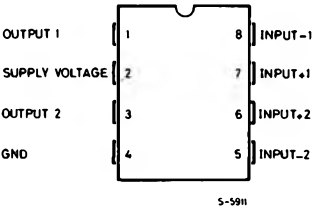
BLOCK DIAGRAMS


L2720

L2722

L2724

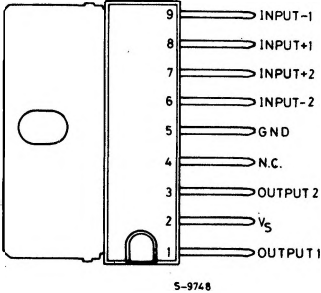
CONNECTION DIAGRAMS
(Top view)



L2720

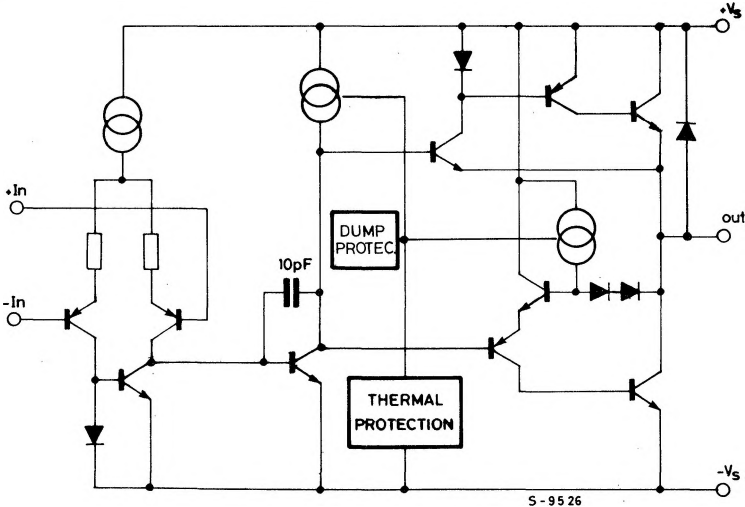


L2722



L2724

SCHEMATIC DIAGRAM (one section)



THERMAL DATA

			SIP-9	Powerdip	Minidip
$R_{th} \text{ j-case}$	Thermal resistance junction-pins	max	10°C/W	15°C/W	*70°C/W
$R_{th} \text{ j-amb}$	Thermal resistance junction-ambient	max	70°C/W	70°C/W	100°C/W

* Thermal resistance junction-pin 4.

ELECTRICAL CHARACTERISTICS ($V_s = 24V$, $T_{amb} = 25^\circ C$ unless otherwise specified)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
V _s	Single supply voltage			4		28	V
V _s	Split supply voltage			± 2		± 14	
I _s	Quiescent drain current	V _o = $\frac{V_s}{2}$	V _s = 24V		10	15	mA
			V _s = 8V		9	15	
I _b	Input bias current				0.2	1	μA
V _{os}	Input offset voltage					10	mV
I _{os}	Input offset current					100	nA
SR	Slew rate				2		V/μs
B	Gain-bandwidth product				1.2		MHz
R _i	Input resistance			500			KΩ
G _v	O.L. voltage gain	f = 100Hz		70	80		dB
		f = 1KHz			60		
e _N	Input noise voltage	B = 22Hz to 22KHz			10		μV
I _N	Input noise current				200		pA
CMR	Common Mode rejection	f = 1KHz		66	84		dB
SVR	Supply voltage rejection	f = 100Hz R _G = 10KΩ V _R = 0.5V	V _s = 24V V _s = ±12V V _s = ± 6V	60	70 75 80		dB dB dB
V _{DROP (HIGH)}		V _s = ±2.5V to ±12V	I _p = 100mA			0.7	
			I _p = 500mA		1.0	1.5	
V _{DROP (LOW)}			I _p = 100mA		0.3		V
			I _p = 500mA		0.5	1.0	
C _s	Channel separation	f = 1KHz R _L = 10Ω G _v = 30dB	V _s = 24V		60		dB
			V _s = 6V		60		
T _{sd}	Thermal shutdown junction temperature				145		°C

Fig. 1 - Quiescent current vs. supply voltage

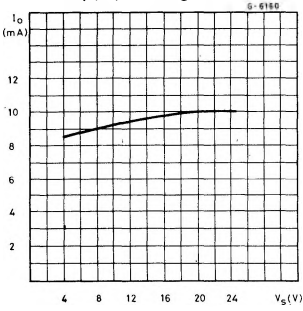


Fig. 2 - Open loop gain vs. frequency

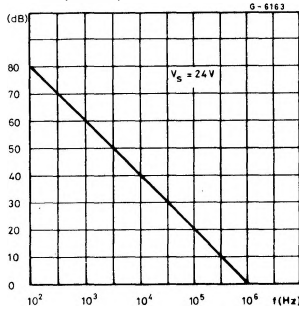


Fig. 3 - Common mode rejection vs. frequency

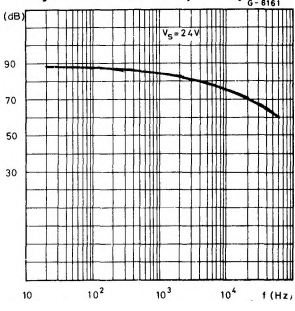


Fig. 4 - Output swing vs. load current ($V_S = \pm 5V$)

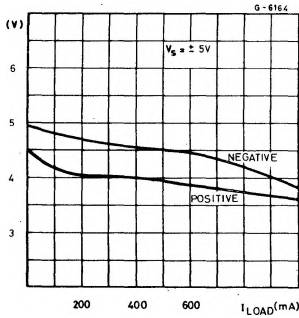


Fig. 5 - Output swing vs. load current ($V_S = \pm 12V$)

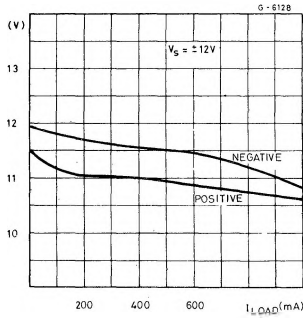


Fig. 6 - Supply voltage rejection vs. frequency

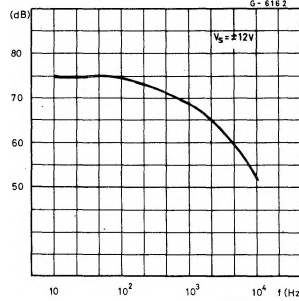
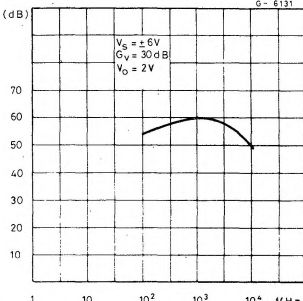


Fig. 7 - Channel separation vs. frequency



APPLICATION SUGGESTION

In order to avoid possible instability occurring into final stage the usual suggestions for the linear power stages are useful, as for instance:

- layout accuracy;
- A 100nF capacitor connected between supply pins and ground;

- boucherot cell (0.1 to $0.2 \mu\text{F} + 1\Omega$ series) between outputs and ground or across the load.
- With single supply operation, a resistor ($1\text{K}\Omega$) between the output and supply pin can be necessary for stability.

Fig. 8 - Bidirectional DC motor control with μP compatible inputs

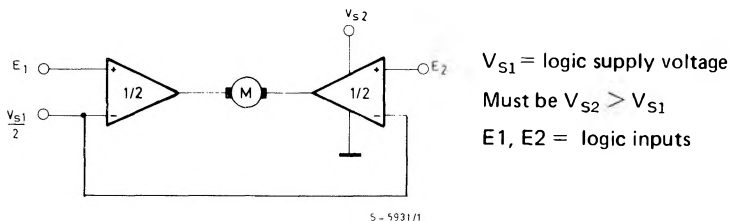


Fig. 9 - Servocontrol for compact-disc

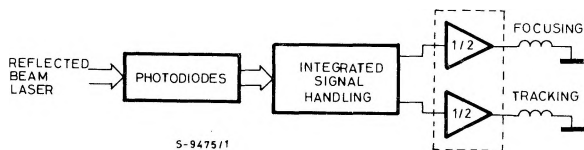


Fig. 10 - Capstan motor control in video recorders

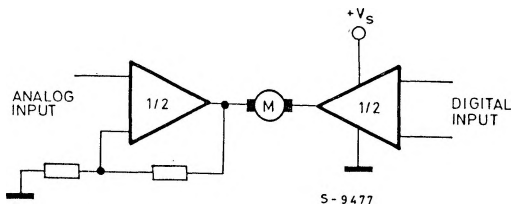
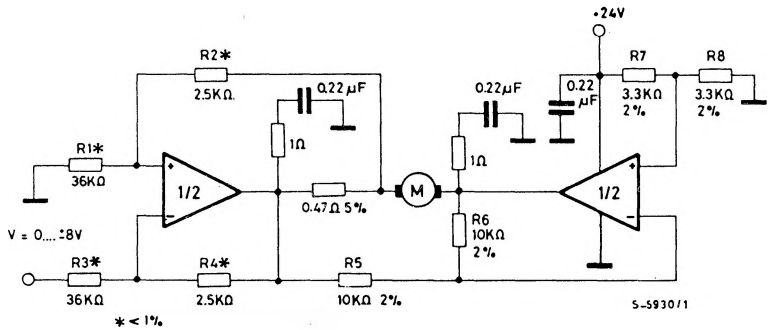


Fig. 11 - Motor current control circuit



Note: The input voltage level is compatible with L291 (5-BIT D/A converter)

Fig. 12 - Bidirectional speed control of DC motors.

For circuit stability ensure that $R_X > \frac{2R_3 \cdot R_1}{R_M}$ where R_M = internal resistance of motor. The voltage available at the terminals of the motor is $V_M = 2 \left(V_i - \frac{V_s}{2} \right) + |R_o| \cdot I_M$ where $|R_o| = \frac{2R \cdot R_1}{R_X}$ and I_M is the motor current.

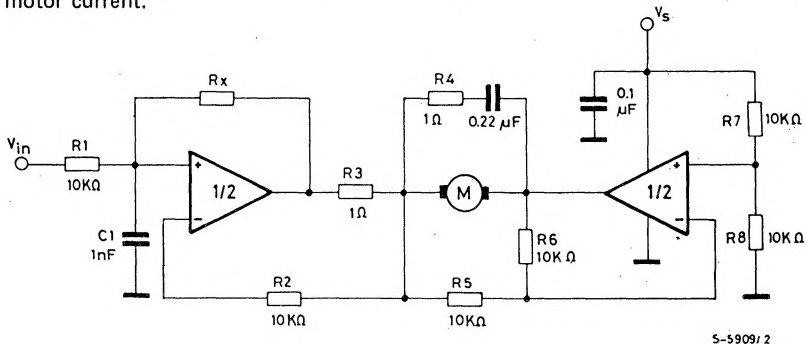


Fig. 13 - VHS-VCR Motor control circuit

