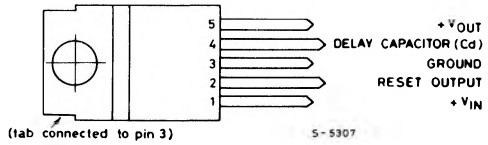
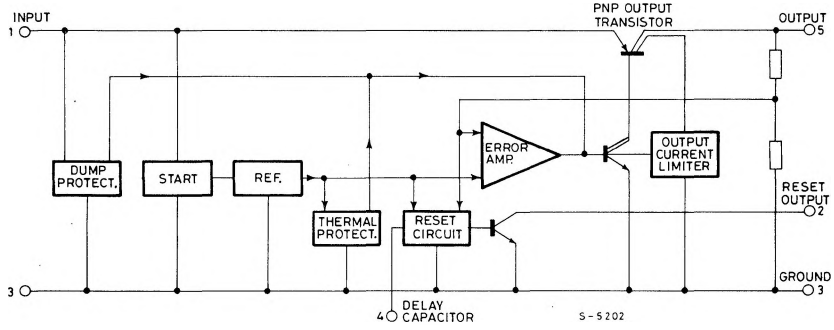
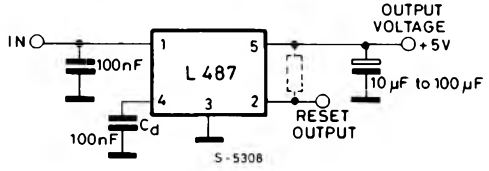
CONNECTION DIAGRAM (top view)



BLOCK DIAGRAM



TEST CIRCUIT



THERMAL DATA

$R_{th\ j\text{-}case}$	Thermal resistance junction-case	max 3	°C/W

ELECTRICAL CHARACTERISTICS (Refer to the test circuit, V_i = 14.4V, T_{amb} = 25°C, unless otherwise specified)

Parameter		Test conditions	Min.	Typ.	Max.	Unit
V_o	Output voltage	I_o = 5 mA to 500 mA	4.80	5	5.20	V
V_i	Operating input voltage				28	V
ΔV_o	Line regulation	V_i = 6 to 26V I_o = 5 mA		5		mV
ΔV_o	Load regulation	I_o = 5 to 500 mA		15		mV
V_i-V_o	Dropout voltage	I_o = 500 mA		0.6		V
I_d	Quiescent current	I_o = 0 mA I_o = 150 mA I_o = 500 mA		5 20 100		mA
$\frac{\Delta V_o}{\Delta T}$	Temperature output voltage drift			0.5		mV/°C
SVR	Supply voltage rejection	I_o = 350 mA f = 120 Hz C_o = 10 μF V_i = 12V ± 5 Vpp		60		dB
I_{sc}	Output short circuit current			0.8		A
V_R	Reset output voltage	I_R= 16 mA $V_o \leqslant$ 4.75V			0.8	V
I_R	Reset output leakage current	V_o in regulation			50	μA
t_d	Delay time for reset output	Cd = 100 nF		30		ms
V_{RT}	Reset threshold		4.75	V_o-0.15		V
V_{RTH}	Threshold hysteresis			10		mV

Fig. 1 - Timing diagram for reset function