LA5606N



BS/CS Tuner Regulator with On/Off Function

## **Overview**

The LA5606N is a low saturation regulator IC for BS/CS tuner applications, equipped with four regulators capable of ON/OFF control.

# Applications

- BS/CS tuner power supply system.
- Audio Video (AV) equipment with BS/CS receivers.
- Compact electronic equipment.

# **Functions**

- Four low saturation regulators (15.7 V/300 mA, 12 V/150 mA, 9 V/100 mA and 5 V/500 mA).
- Output on/off control ("L" active).
- On-chip protective circuitry (current limiter, thermal shutdown).

# **Features**

- Supports compact set design while incorporating four regulators needed by BS/CS tuners.
- Flexible system design by independent on/off control of  $V_01$ ,  $V_04$ , as well as  $V_02$  and  $V_03$  pair.
- Reduces internal loss by employment of low saturation regulators.
- Adapting three input pins contributes power dissipation reduction and heat sink design.

# **Package Dimensions**

unit: mm

## 3023A-SIP14H



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# **Specifications**

# Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum input voltage	V <sub>IN</sub> max	$V_{IN}1 \ge V_{IN}2 \ge V_{IN}3$ 35		V
Enable pin voltage	V <sub>EN</sub> max	EN1, EN2, EN3	V <sub>IN</sub> max	V
Allowable power dissipation	Pd max	With infinite heat sink	15	W
		With no heat sink	4.3	W
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +150	°C

# Operating Conditions at $Ta=25^{\circ}C$

Parameter	Symbol	Conditions Ratings		Unit
Output current 1	I <sub>O</sub> 1	Regulator 1 5 to 350		mA
Output current 2	I <sub>O</sub> 2	Regulator 2	1 to 200	mA
Output current 3	I <sub>O</sub> 3	Regulator 3	1 to 150	mA
Output current 4	I <sub>O</sub> 4	Regulator 4	5 to 500	mA

## Operating Characteristics at Ta = $25^{\circ}$ C and the specified Test Circuit

Oymbol	Symbol Conditions		Ratings		
			typ	max	- Unit
N, V <sub>IN</sub> 1 = 18.7	V and I <sub>O</sub> 1 = 300 mA)				
V <sub>O</sub> 1		14.9	15.7	16.5	V
V <sub>DROP1-1</sub>			0.3	0.5	V
V <sub>DROP1-2</sub>	I <sub>O</sub> 1 = 150 mA		0.15	0.3	V
$\Delta V_{OLN}$ 1	$17.5 \text{ V} \leq \text{V}_{IN} 1 \leq 23 \text{ V}$		20	100	mV
$\Delta V_{OLD}$ 1	$5 \text{ mA} \le I_0 1 \le 300 \text{ mA}$		40	200	mV
I <sub>OP</sub> 1		350	540		mA
I <sub>OSC</sub> 1			150		mA
V <sub>ENL</sub> 1	V <sub>O</sub> 1: On			1.0	V
V <sub>ENH</sub> 1	V <sub>O</sub> 1: Off	4.0		V <sub>IN</sub> 1	V
V <sub>O</sub> 1 OFF				0.2	V
V <sub>NO</sub> 1	10 Hz ≤ f ≤ 100 kHz		110		μVrms
Rrej1	f = 120 Hz, 18 V ≤ V <sub>IN</sub> 1 ≤ 23 V		50		dB
N, V <sub>IN</sub> 2 = 15.0	V, I <sub>O</sub> 2 = 150 mA)	L.			
V <sub>O</sub> 2	-	11.4	12.0	12.6	V
V <sub>DROP</sub> 2			0.3	0.5	V
ΔV <sub>OLN</sub> 2	$12.6 V \le V_{IN} 2 \le 23 V$		20	100	mV
ΔV <sub>OLD</sub> 2	$1 \text{ mA} \le I_0 2 \le 150 \text{ mA}$		20	70	mV
		200	270		mA
			70		mA
	V <sub>O</sub> 2: On			1.0	V
	-	4.0		V <sub>IN</sub> 2	V
				0.2	V
-	10 Hz ≤ f ≤ 100 kHz		110		μVrms
Rrej2	f = 120 Hz, 13 V ≤ V <sub>IN</sub> 2 ≤ 23 V		50		dB
N, V <sub>IN</sub> 2 = 12 V					
		8.55	9.0	9.45	V
			0.3	0.5	V
	$10.45 \text{ V} \le \text{V}_{IN}2 \le 23 \text{ V}$		20	100	mV
			20	50	mV
-	, ř	150	180		mA
			40		mA
	V <sub>O</sub> 3: On		-	1.0	V
		4.0			V
					V
	10 Hz ≤ f ≤ 100 kHz		70		µVrms
					dB
	$\begin{tabular}{ c c c c } \hline V_0 \\ \hline V_D ROP1-1 \\ \hline V_D ROP1-2 \\ \hline \Delta V_{OLN} \\ \hline \\ \hline \Delta V_{OLD} \\ \hline \\ $	$\begin{tabular}{ c c c c c } \hline V_{DROP1-1} & I_01 = 150 \text{ mA} \\ \hline \Delta V_{OLN}1 & 17.5 \ V \leq V_{IN}1 \leq 23 \ V \\ \hline \Delta V_{OLD}1 & 5 \ \text{mA} \leq I_01 \leq 300 \ \text{mA} \\ \hline I_{OP}1 & & & \\ \hline I_{OSC}1 & & & \\ \hline V_{ENL}1 & V_01: \ \text{On} & & & \\ \hline V_{ENH}1 & V_01: \ \text{Off} & & & \\ \hline V_{O1} \ OFF & & & \\ \hline V_{NO}1 & 10 \ \text{Hz} \leq f \leq 100 \ \text{kHz} & & \\ \hline \text{Rrej1} & f = 120 \ \text{Hz}, 18 \ V \leq V_{IN}1 \leq 23 \ V \\ \hline DN, \ V_{IN}2 = 15.0 \ V, \ I_02 = 150 \ \text{mA} \\ \hline V_{O2} & & & \\ \hline V_{DROP2} & & \\ \hline \Delta V_{OLD}2 & 1 \ \text{mA} \leq I_02 \leq 150 \ \text{mA} \\ \hline I_{OP}2 & & \\ \hline \Delta V_{OLD}2 & 1 \ \text{mA} \leq I_02 \leq 150 \ \text{mA} \\ \hline I_{OP}2 & & \\ \hline V_{ENL}2 & V_{O}2: \ \text{On} & & \\ \hline V_{ENH}2 & V_{O}2: \ \text{On} & & \\ \hline V_{ENH}2 & V_{O}2: \ \text{Off} \\ \hline V_{O2}OFF & & \\ \hline V_{NO}2 & 10 \ \text{Hz} \leq f \leq 100 \ \text{kHz} \\ \hline \text{Rrej2} & f = 120 \ \text{Hz}, 13 \ V \leq V_{IN}2 \leq 23 \ V \\ \hline DN, \ V_{IN}2 = 12 \ V, \ I_03 = 100 \ \text{mA} \\ \hline V_{O3} & & \\ \hline V_{OROP3} & & \\ \hline \Delta V_{OLD3} & 1 \ \text{mA} \leq I_03 \leq 100 \ \text{mA} \\ \hline I_{OP}3 & & \\ \hline \Delta V_{OLD3} & 1 \ \text{mA} \leq I_03 \leq 100 \ \text{mA} \\ \hline I_{OP}3 & & \\ \hline I_{OSC3} & & \\ \hline V_{ENL}2 & V_{O3}: \ \text{On} & \\ \hline V_{ENL}2 & V_{O3}: \ \text{Off} \\ \hline V_{O3} \ OFF & & \\ \hline V_{NO3} & 10 \ \text{Hz} \leq f \leq 100 \ \text{kHz} \\ \hline V_{NO3} & 10 \ \text{Hz} \leq f \leq 100 \ \text{kHz} \\ \hline \end{array}$	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	$\begin{tabular}{ c c c c c c } \hline V_01 & 14.9 & 15.7 \\ \hline V_{DROP1-1} & 0.3 \\ \hline V_{DROP1-2} & 10^1 = 150 \mbox{ mA} & 0.15 \\ \hline \Delta V_{OLN1} & 17.5 \ V \leq V_{IN1} \leq 23 \ V & 20 \\ \hline \Delta V_{OLD1} & 5 \ mA \leq 10^1 \leq 300 \ mA & 40 \\ \hline 10^{p1} & 350 & 540 \\ \hline 10sc 1 & 150 \\ \hline V_{ENL1} & V_01: On & 10 \\ \hline V_{ENH1} & V_01: Off & 4.0 \\ \hline V_01 \ OFF & 10 \ Hz \leq f \leq 100 \ Hz & 110 \\ \hline V_{01} \ F & 10 \ Hz \leq f \leq 100 \ Hz & 110 \\ \hline V_{02} & 11.4 & 12.0 \\ \hline V_{02} & 11.4 & 12.0 \\ \hline V_{02} \ V_{02} & 11.4 & 12.0 \\ \hline V_{01OP2} & 0.3 \\ \hline \Delta V_{0LP2} & 12.6 \ V \leq V_{IN2} \leq 23 \ V & 20 \\ \hline \Delta V_{0LP2} & 12.6 \ V \leq V_{IN2} \leq 23 \ V & 20 \\ \hline \Delta V_{0LP2} & 12.6 \ V \leq V_{IN2} \leq 23 \ V & 20 \\ \hline \Delta V_{0LP2} & 10 \ Hz \leq f \leq 100 \ Hz & 10 \ Hz \leq 100 \ Hz & 100 \ Hz = 100$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$

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Parameter	Symbol	Conditions	Ratings			- Unit
			min	typ	max	
Regulator 4 ( $V_{EN}3 = low, V_O4$ : ON	I, V <sub>IN</sub> 3 = 8.0 V	/, I <sub>O</sub> 4 = 500 mA)				
Output voltage 4	V <sub>O</sub> 4		4.75	5.0	5.25	V
Dropout voltage	V <sub>DROP4-1</sub>			0.3	0.5	V
Diopour voltage	V <sub>DROP4-2</sub>	I <sub>O</sub> 4 = 250 mA		0.2	0.4	V
Line regulation	$\Delta V_{OLN} 4$	$6.25 \text{ V} \leq \text{V}_{\text{IN}} 3 \leq 23 \text{ V}$		20	100	mV
Load regulation	$\Delta V_{OLD} 4$	$5 \text{ mA} \le 104 \le 500 \text{ mA}$		30	150	mV
Peak output current	I <sub>OP</sub> 4		500	900		mA
Output short current	I <sub>OSC</sub> 4			250		mA
Output on control voltage	V <sub>ENL</sub> 3	V <sub>O</sub> 4: On			1.0	V
Output off control voltage	V <sub>ENH</sub> 3	V <sub>O</sub> 4: Off	4.0		V <sub>IN</sub> 3	V
Output low level voltage	V <sub>O</sub> 4 OFF				0.2	V
Output noise voltage	V <sub>NO</sub> 4	10 Hz $\leq$ f $\leq$ 100 kHz		70		μVrms
Ripple rejection	Rrej4	f = 120 Hz, 7 V $\leq$ V <sub>IN</sub> $3 \leq$ 23 V		60		dB
Current dissipation 1	l <sub>Q</sub> 1	$I_0 1, I_0 2, I_0 3, I_0 4 = 0$		11		mA
Current dissipation 2	I <sub>Q</sub> 2	I <sub>O</sub> 1 = 300mA, I <sub>O</sub> 2 = 150 mA, I <sub>O</sub> 3 = 100mA, I <sub>O</sub> 4 = 500 mA		53		mA

Continued from preceding page.

## **Pin Assignment**



Note: The NC pins should not be used (No. 1 and No. 14 in the pin layout).



## **Block Diagram**



#### LA5606N

## **Test Circuit**



Unit (capacitance: F)

#### **Function Table**

The following table indicates conditions for operation with  $V_{IN}1 \ge V_{IN}2 \ge V_{IN}3$  ( $V_{IN}1 \ge 11$  V,  $V_{IN}2 \ge 6$  V and  $V_{IN}3 \ge 4$  V).

EN1, EN2, EN3	V <sub>O</sub> 1, V <sub>O</sub> 2/V <sub>O</sub> 3, V <sub>O</sub> 4		
Н	L		
L	Н		

- 1. Within the table of EN "H" indicates an H level and "L" indicates an L level.
- 2. In the table of V<sub>O</sub> "H" indicates an output on voltage while "L" indicates an output off voltage.
- 3. All output voltages corresponding to all EN locations are controlled independently. (EN1  $\rightarrow$ V<sub>0</sub>1, EN2  $\rightarrow$  V<sub>0</sub>2 and V<sub>0</sub>3, EN3  $\rightarrow$  V<sub>0</sub>4)
- 4. When EN is open,  $V_{O}$  is at the H level.

## EN (On/Off Control) Input Equivalent Block Diagram

$$V_01(V_04)$$

$$V_0^2$$
 and  $V_0^3$ 



#### **Notes for Above Applications**

- 1. GND1 and GND2 should be at the same electric potential; since these are connected to the substrate of the LA5606N, the lowest possible electric potential should be used. (If the electric potential of GND1 and GND2 differ, performance characteristics of the LA5606N can not be guaranteed.)
- 2. Rise and fall times for  $V_{IN}1$ ,  $V_{IN}2$  and  $V_{IN}3$  should be unified and concerning these pins operating in an opencircuit state or connected to the ground state is forbidden.
- 3. When  $V_{IN}1$  and  $V_{IN}2$  are open or lower than the required value,  $V_O1$  to  $V_O4$  are forced off for the IC's protection.
- 4. Use output capacitors  $C_{OUT}$ 1 and  $C_{OUT}$ 4 rated at 100  $\mu$ F or more and  $C_{OUT}$ 2 and  $C_{OUT}$ 3 rated at 47  $\mu$ F or more. To prevent oscillation at low temperature, be sure to use less temperature sensitive capacitors.
- 5. In order to provide stable operation, C<sub>IN</sub>1 to C<sub>IN</sub>3 and C<sub>OUT</sub>1 to C<sub>OUT</sub>4 should be mounted as close to the LA5606N as possible.
- 6. The NC pins should not be used (No. 1 and No. 14 in the pin layout).
- 7. The output voltage of each voltage regulator is affected by a change in the load on the other voltage regulators.

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