


**SANYO Semiconductors**

# DATA SHEET

An ON Semiconductor Company

## LA5771MP — Monolithic Linear IC Separately-excited Step-down Switching Regulator (3.3V)

### Overview

The LA5771MP is a separately-excited step-down switching regulator (3.3V).

### Features

- High efficiency
- Four external parts
- Time-base generator (160kHz) incorporated
- Current limiter incorporated
- Thermal shutdown circuit incorporated
- Soft start circuit incorporated

### Specifications

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V <sub>IN</sub> max		30	V
Output current	I <sub>O</sub> max		3	A
SW pin application reverse voltage	V <sub>sw</sub>		-1	V
Allowable power dissipation	P <sub>d</sub> max	Mounted on the specified board *	3.9	W
Operating temperature	T <sub>opr</sub>		-30 to +125	°C
Storage temperature	T <sub>stg</sub>		-40 to +150	°C

\* Mounted on a specified board: 114.3mm×76.1mm×1.6mm, Copper foil ratio 60% FR4

#### Recommended Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage range	V <sub>IN</sub>		5.5 to 28	V

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# LA5771MP

## Electrical Characteristics at Ta = 25°C

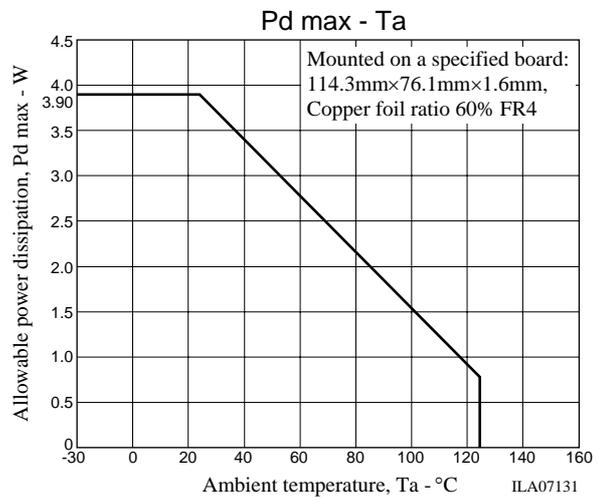
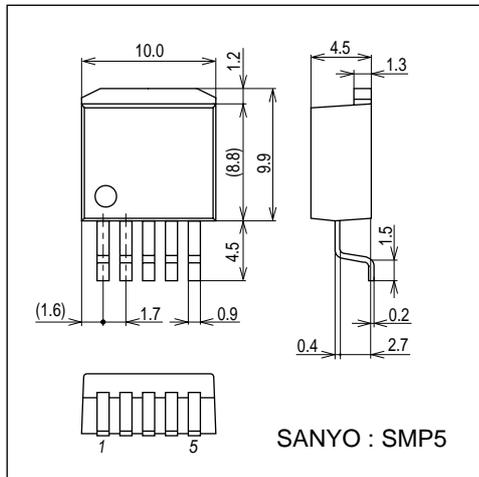
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Output voltage	$V_O$	$V_{IN}=15V, I_O=1.0A$	3.17	3.30	3.43	V
Efficiency	$\eta$	$V_{IN}=15V, I_O=1.0A$		79		%
Switching frequency	f	$V_{IN}=15V, I_O=1.0A$	128	160	192	kHz
Line regulation	$\Delta V_{OLINE}$	$V_{IN}=8 \text{ to } 20V, I_O=1.0A$		25	80	mV
Load regulation	$\Delta V_{OLOAD}$	$V_{IN}=15V, I_O=0.5 \text{ to } 1.5A$		10	30	mV
Output voltage temperature coefficient	$\Delta V_O/\Delta T_a$	Designed target value*		$\pm 0.5$		mV/°C
Ripple attenuation factor	RREJ	F=100 to 120Hz		45		dB
Current limiter operating voltage	IS	$V_{IN}=15V$	3.1			A
Thermal shutdown operating temperature	TSD	Designed target value*		165		°C
Thermal shutdown hysteresis width	$\Delta TSD$	Designed target value*		15		°C

\* Designed target value: No measurement made.

## Package Dimensions

unit : mm (typ)

3275

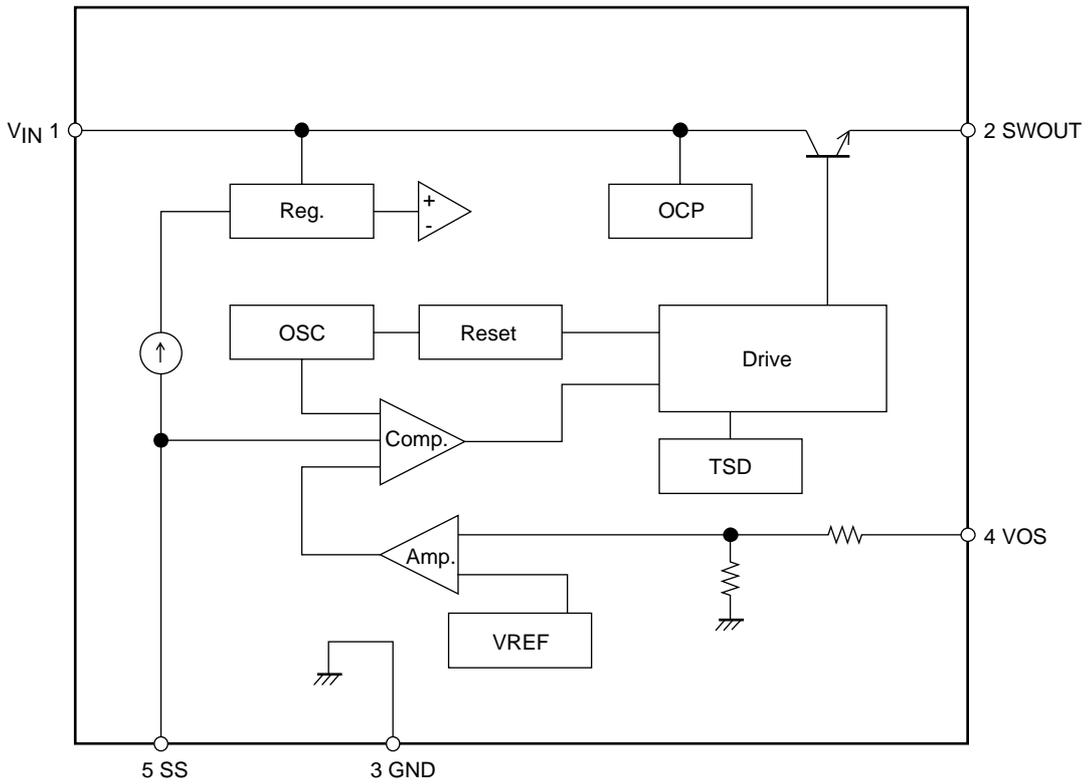


## Pin Assignment

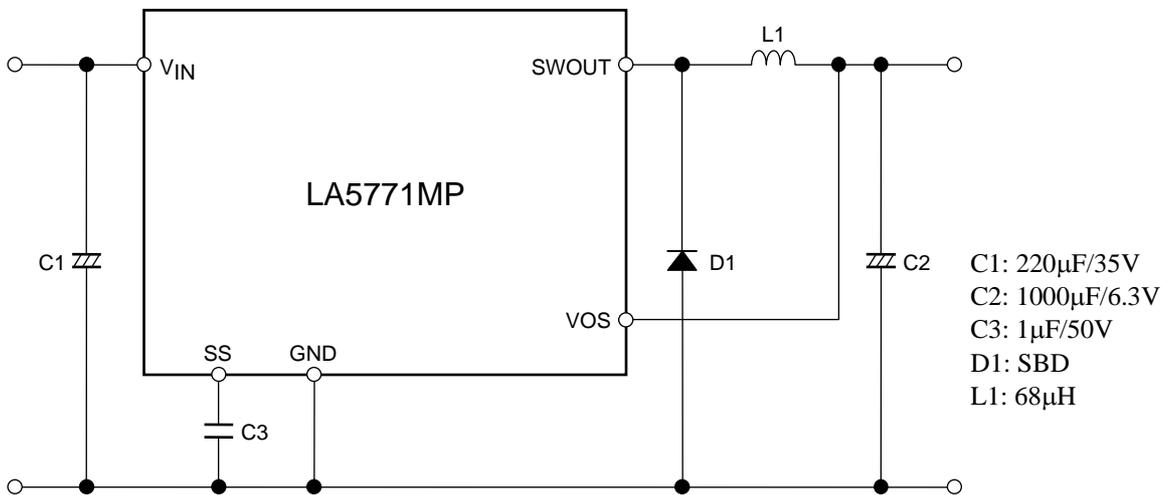
①VIN ②SWOUT ③GND ④VOS ⑤SS

# LA5771MP

## Block Diagram



## Application Circuit Example



Notes: C3 is for the soft start function. Delete C3 and keep the SS pin open when the soft function is not necessary.

## Description of Functional Settings

### 1. Start delay function

The SS pin has the internally-connected 22μA (typ) constant-current supply. When the voltage of SS pin exceeds the threshold voltage, the regulator starts operation. As the threshold is 0.62V(typ), the start delay time can be calculated as follows:

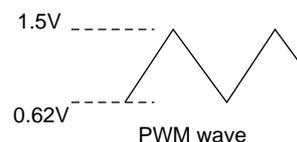
ex. For setting at 1μF

$$T_d = \frac{C \times V}{i} = \frac{1\mu\text{A} \times 0.62\text{V}}{22\mu\text{A}} = 28.2\text{ms}$$

### 2. Soft start function

The internal PWM waveform has the voltage value as shown in the right.

If down-conversion from the voltage of  $V_{IN}=15\text{V}$  to 3.3V output is to be made, for example, the PWM-ON duty has the value as shown below.



$$\text{PWMduty} = \frac{V_{\text{OUT}}}{V_{\text{IN}} - V_{\text{sat}} + V_{\text{F}}} = 23\%$$

(Note that calculation is made with  $V_{\text{sat}}=1\text{V}$  and  $V_{\text{F}}=0.2\text{V}$ )

The output voltage of error amplifier, which is 3.3V, is the value with PWM=23%, as calculated in the above equation, so that this voltage is determined as follows:

$$V_{\text{er}} = (\Delta V_{\text{PWM}}) \times \text{PWMduty} + V_{\text{PWML}} = 0.88\text{V} \times 0.23 + 0.62\text{V} = 0.82\text{V}$$

( $\Delta V_{\text{PWM}}$  is the PWM amplitude value or 0.88V(typ) while  $V_{\text{PWML}}$  is the lower limit voltage of PWM waveform or 0.62V(typ))

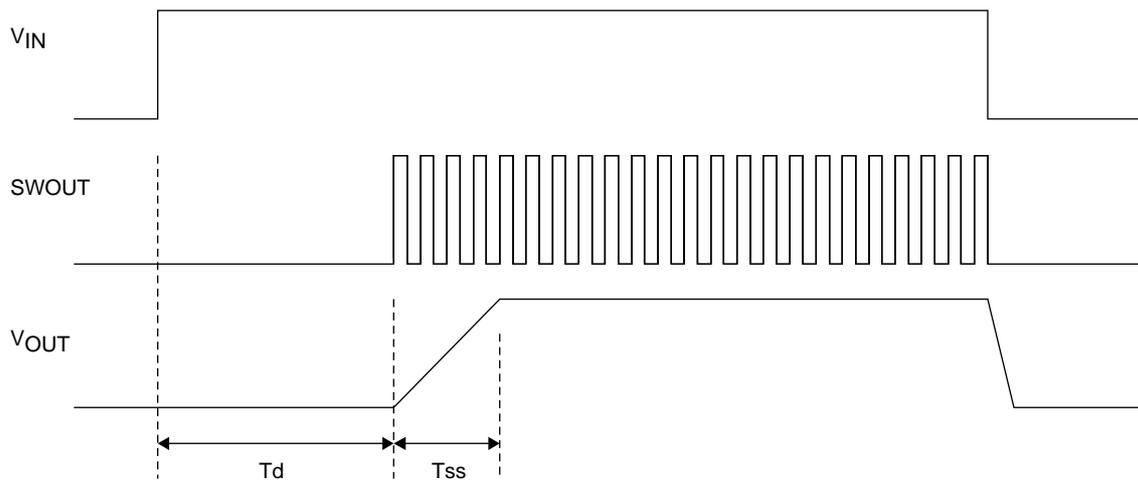
SS pin and error amplifier output voltages are designed to prefer the lower voltages, so that  $V_{\text{OUT}}$  will reach the designed regulation voltage in timing when the SS pin voltage exceeds the error amplifier output. Therefore, the soft start time is calculated as follows:

$$T_{\text{ss}} = \frac{C \times \Delta V_{\text{PWM}} \times \text{PWMduty}}{i} = \frac{C \times 0.88\text{V} \times \text{PWMduty}}{22\mu\text{A}}$$

For the set conditions of  $C=1\mu\text{F}$  and  $\text{PWMduty}=23\%$ :

$$T_{\text{ss}} = \frac{1\mu\text{A} \times 0.88\text{V} \times 0.23}{22\mu\text{A}} = 9.2\text{ms}$$

## Timing Chart



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