

Ordering number: EN 994D

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Test Circuits

1. Input offset voltage 2. Input offset current VCC RZ QVcc QVCC RZ QVcc ₩₽ R NUL NUL ₹R2 ₽R2 ovee 0 $J_{10} = \frac{V_{F2} - V_{F1}}{R(1 + R_2/R_1)}$ VF1 1 + R2/R1 ۳ю –



4. Current dissipation



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5. Voltage gain



6. Response time





7. Output sink current





9. Output leak current





51 k

51 k

Square wave generator



No.994-3/4

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