

Overview

The LA6358NM is an IC integrating two high-performance operational amplifiers in a single package. This operational amplifier contains an internal phase compensator and is designed to operate from a single power supply over a wide range of voltages. As with conventional general-purpose operational amplifiers, operation from dual power supplies is also possible and power dissipation is very low. This IC can be used widely in commercial and industrial applications including various transducer amplifiers and DC amplifiers.

Features

- · Eliminates need for phase compensation
- Wide range of operating supply voltage :

3.0 to 30.0V (single power supply) ± 1.5 to $\pm 15.0V$ (dual power supply)

 \cdot Input voltage swingable down to nearly ground level and output voltage range V_{OUT} of 0 to V_{CC}-1.5V

· Low current dissipation : $I_{CC} = 0.5 \text{mA typ}/V_{CC} = +5 \text{V}, \text{R}_{L} = \infty$

· Miniflat package permitting the LA6358NM-applied sets to be made small

Maximum Ratings at Ta = 25°C			unit
Maximum Supply Voltage	V _{CC}	32	v
Differential Input Voltage	V _{ID}	32	v
Maximum Input Voltage	V _{IN} max	-0.3 to $+32$	v
Allowable Power Dissipation	Pd max	300	mW
Operating Temperature	Topr	-30 to $+85$	°C
Storage Temperature	Tstg	-55 to $+125$	°C
Operating Characteristics at Ta	$a = 25^{\circ}C, V_{CC} = +5V$	Test	

			Circuit	min	typ	max	unit
Input Offset Voltage	VIO		1		±2	±7	mV
Input Offset Current	IIO	$I_{IN(+)}/I_{IN(-)}$	2		±5	± 50	nA
Input Bias Current	IB	$I_{IN(+)}/I_{IN(-)}$	3		45	250	nA
Common-mode	V _{ICM}	• • • • •	` 4	0	V _{CC}	-1.5	v
Input Voltage Range							

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			Test				
			Circuit	min	typ	max	unit
Common-mode	CMR		4	65	80		dB
Rejection Ratio							
Large Signal Voltage Gain	VG	$V_{CC} = 15 V, R_L \ge 2 k \Omega$	5	25	100	•	V/mV
Output Voltage Range	V _{OUT}			0	VCC	-1.5	v
Power Supply Rejection Ratio	SVR		6	65	10 0		dB
Channel Separation		f = 1k to 20kHz	7		120		dB
Current Dissipation	I _{CC}		8		0.5	1.2	mA
Output Current (Source)	IO source	$V_{IN+} = 1V, V_{IN-} = 0V$	9	20	40		mA
Output Current (Sink)	I _{O sink}	$V_{IN+} = 0V, V_{IN-} = 1V$	10	10	20		mA

Test Circuits



2. Input Offset Current IIO

5. Voltage Gain VG



3. Input Bias Current IB



$$I_{\rm B} = \frac{VF4 - VF3}{2R(1 + R2/R1)}$$

4. Common-mode Rejection Ratio CMR Common-mode Input Voltage Range V_{ICM}



6. Supply Voltage Rejection SVR



7. Channel Separation CS



SW : a $CS (A \rightarrow B) + 20 \log \frac{R_2 V_{OA}}{R_1 V_{OB}}$ SW : b $CS (B \rightarrow A) + 20 \log \frac{R_2 V_{OB}}{R_1 V_{OA}}$

8. Current Dissipation I_{CC}

















Sample Application Circuits

• ¥V_{јN}

Noninverting DC amplifier

RŹ



CIN RI

R2 100k 0-₩' Vcc

10 ju

Rectangular wave oscillator



Unit (resistance: Ω , capacitance: F)

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