Ordering number: EN 3264



The LA6530, 6531 are 2-channel BTL-use drivers designed for compact disc pickup actuation.

Functions and Features

• High output current ($I_0 \max = 0.7A$)

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- · Wide operating voltage range (4 to 15V)
- Low input bias current

• LA6530 Output of amps 1 to 4 at muting-ON mode : OFF

· LA6531 Output of amps 1 to 4 and buffer amp at muting-ON mode : OFF

	Maximum Ratings at Ta=25°C						unit			
	Maximum Supply Voltage	V _{CC} m	ax			16	v			
	Allowable Power Dissipation	Pd ma				1.9	W			
	Differential Input Voltage	V_{ID}		Amp 2, amp 3		15	v			
	Common-Mode Input Voltage	V _{ICM}		Amp 2, amp 3		15	v			
	Maximum Input Voltage	VINBI	nax	Buffer amp		15	v			
	Maximum Flow-in Current	I _M ma		F		1.0	mÅ			
	at Muting Pin	- 147				2.10				
	Maximum Output Current	I _O ma:	x			0.7	А			
	Operating Temperature	Topr			- 20 to +	-75	°C			
	Storage Temperature	\mathbf{Tstg}			-55 to +	150	°C			
	Operating Conditions at Ta = 25	-					unit			
	Maximum Supply Voltage	V _{CC} m	ax			5	V			
	Load Resistance	R_L				8	Ω			
Operating Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5.0V$ min typ max unit										
					5	typ	max			
	No-Loaded Current Dissipation		cc^1	Mute OFF, pins 8,9,16 GND	-	10	20	mA		
	No-Loaded Current Dissipation		cc^2	Mute OFF,pins 8,9,16 GND	3	7	15	mA		
	No-Loaded Current Dissipation	n.3 I	cc_3	Mute OFF,pins 8,9,16 1/2 V_{CC}	10	20	30	mA		
	No-Loaded Current Dissipation	n.4 I	cc^4	Mute OFF,pins 8,9,16 1/2 V_{CC}	4	8	16	mA		
					Conti	nued	on next	t page.		





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Continued from preceding page.

			min	typ max	unit				
Output Offset Voltage 1	V _{OF} 1	Out 1 - Out 2	-50	50	mV				
Output Offset Voltage 2	V _{OF} 2	Out 4 - Out 3	-50	50	mV				
Buffer Input-Output	V _{BIO}	Buffer amp	- 30	30	mV				
Voltage Difference									
Buffer Input Voltage Range	VBICM	Buffer amp	1.5	$V_{CC} - 1.5$	v				
Common-Mode Input Voltage Range	V_{ICM}	Amp 2, amp 3	1.0	$V_{\rm CC} = 1.5$	V				
Input Bias Current	IB			50 300	nA				
Output Voltage	Vo	$R_L = 8.0\Omega$	2.8	3.3	v				
Bridge Output Voltage Difference	V _{OD}	Pins 3-6,11-14 8 Ω load	1.8	2.2	V				
Closed-Circuit Voltage Gain	V_{G}		30	38	dB				
Muting Pin ON-State Voltage	VM			0.7	v				
Muting Pin Flow-in Current	I _M			3	μA				
Note) With thermal shutdown function									

X : The LA6530 is so designed that the outputs at OUT1 to OUT4 are turned OFF and the output at VBOUT is not turned OFF at the muting-ON mode.

% : The LA6531 is so designed that the outputs at OUT1 to OUT4 and the output at VBOUT are turned OFF.

* : Be carefull in handling the LA6530, 6531, because dielectric breakdown is liable to occur.

Equivalent Circuit Block Diagram

- W

6.4





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