

LB1997



Three-Phase Brushless Motor Driver for CD-ROM Spindle Drive

Overview

The LB1997 is a three-phase brushless motor driver especially suited for CD-ROM spindle motor drives.

Functions

- Current linear drive
- Control V type amplifier
- Separate power supply for output upper side bias circuit allows low output saturation by boosting this power supply only (useful for 5V power supply types).
- Upper side current detection technique reduces loss voltage of current detection resistor. Voltage effect of this resistor reduces internal current drain of IC.
- Built-in short braking circuit
- Built-in reverse blocking circuit
- Hall FG output
- Built-in S/S function
- Built-in current limiter circuit
- Built-in Hall power supply
- Built-in thermal shutdown circuit
- V type control/single-side control switching pin allows CLV and CAV operation.

Package Dimensions

unit: mm **3222-HSOP28**





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Specifications

Maximum Ratings at Ta = $25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|-----------------------|--|-------------------|------|
| Power supply voltage | V _{CC} 1 max | | 7.0 | V |
| | V _{CC} 2 max | | 14.4 | V |
| | V _{CC} 3 max | | 14.4 | V |
| Applied output voltage | V _O max | | 14.4 | V |
| Applied intput voltage | V _{IN} max | | V _{CC} 1 | V |
| Output current | I _O max | | 1.3 | А |
| Allowable power dissipation | Pd max | IC only | 0.6 | W |
| | | with substrate (114.3 \times 76.1 \times 1.6 mm ³ , glass exposy) | 1.5 | W |
| Operating temperature | Topr | | –20 to +75 | °C |
| Storage temperature | Tstg | | -55 to +150 | °C |

Operating Conditions at $Ta = 25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|----------------------|-------------------|---------------------|-----------|------|
| Power supply voltage | V _{CC} 1 | | 4 to 6 | V |
| | V _{CC} 2 | ≥ V _{CC} 1 | 4 to 13.6 | V |
| | V _{CC} 3 | | 4 to 13.6 | V |

Sample Application at Ta = $25^{\circ}C$

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------|---------------------|--|-----------|------|
| 12V type | V _{CC} 1 | Regulated voltage | | V |
| | $V_{CC}2 = V_{CC}3$ | Unregulated voltage | 4 to 13.6 | V |
| 5V type | $V_{CC}1 = V_{CC}3$ | Regulated voltage | 4 to 6 | V |
| | V _{CC} 2 | Boost-up voltage or regulated voltage (Note) | 4 to 13.6 | V |

Note: When boost-up voltage is used at V_{CC}^2 , output can be set to low-saturation.

Electrical Characteristics at Ta = 25°C, $V_{CC}1 = 5V$, $V_{CC}2 = V_{CC}3 = 12V$

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|----------------------------------|---------------------|--|---------|------|----------------------|-------------------|
| T urumotor | Cymbol | | min | typ | max | Unit |
| [Power supply current] | _ | | | | | |
| Power supply current | I _{CC} 1 | V _C = V _{CREF} | | 8 | | mA |
| | I _{CC} 2 | $V_{C} = V_{CREF}$ | | 0 | | mA |
| | I _{CC} 3 | $V_{C} = V_{CREF}$ | | 150 | 250 | μA |
| Output idle current | I _{CC} 10Q | $V_{S/S} = 0V$ | | | 200 | μA |
| | I _{CC} 2OQ | $V_{S/S} = 0V$ | | | 30 | μA |
| | I _{CC} 3OQ | $V_{S/S} = 0V$ | | | 30 | μA |
| [Output] | | | | | | |
| Saturation voltage, upper side 1 | V _{OU} 1 | $I_{O} = -0.5A, V_{CC}1 = 5V, V_{CC}2 = V_{CC}3 = 12V$ | | 1.0 | | V |
| lower side 1 | V _{OD} 1 | $I_0 = 0.5A, V_{CC}1 = 5V, V_{CC}2 = V_{CC}3 = 12V$ | | 0.3 | | V |
| Saturation voltage, upper side 2 | V _{OU} 2 | $I_{O} = -0.5A, V_{CC}1 = V_{CC}3 = 5V, V_{CC}2 = 12V$ | | 0.3 | | V |
| lower side 2 | V _{OD} 2 | $I_0 = 0.5A, V_{CC}1 = V_{CC}3 = 5V, V_{CC}2 = 12V$ | | 0.3 | | V |
| Current limiter setting voltage | V _{CL} | $R_{RF} = 0.43\Omega$ | | 0.37 | | V |
| [Hall amplifier] | - | | | | - | |
| Common mode input voltage range | V _{HCOM} | | 1.2 | | V _{CC} -1.0 | V |
| Input bias current | I _{HIB} | | | 1 | | μA |
| Minimum Hall input level | V _{HIN} | | 60 | | | mV _{P-P} |
| [S/S pin] | | | | | | |
| High level voltage | V _{S/SH} | | 2.0 | | V _{CC} 1 | V |
| Low level voltage | V _{S/SL} | | | | 0.7 | V |
| Input current | I _{S/SI} | $V_{S/S} = 5V$ | | | 200 | μA |
| Leak current | I _{S/SL} | $V_{S/S} = 0V$ | -30 | | | μA |
| [Control] | | | | | | |
| | I _{VC} | $V_{C} = V_{CREF} = 2.5V$ | | 1 | | μA |
| | IVCREF | $V_{C} = V_{CREF} = 2.5V$ | | 1 | | μA |
| Voltage gain | GV _{CO} | $\Delta V_{RF} / \Delta V_{C}$ | | 0.25 | | times |
| Startup voltage | V _{CTH} | $V_{CREF} = 2.5V$ | 2.35 | | 2.65 | V |
| Startup voltage width | ΔV_{CTH} | $V_{CREF} = 2.5V$ | 50 | | 150 | mV |
| [Hall power supply] | | | | | | |
| Hall power supply voltage | V _H | I _H = 5mA | | 0.8 | | V |
| Allowable current | Ι _Η | | 20 | | | mA |
| [Thermal shutdown] | | | | | | |
| Operating temperature | T _{TSD} | Design target value | 150 | 180 | 210 | °C |
| Hysterisis | ΔT_{TSD} | Design target value | | 15 | | ĉ |
| [Short braking] | | • | | | | |
| Brake pin at High level | V _{BRH} | | 4 | | 5 | V |
| Brake pin at Low level | V _{BRL} | | 0 | | 1 | V |
| [Control switching] | | • | · | | | |
| CC pin at High level | V _{CCH} | | 4 | | 5 | V |
| CC pin at Low level | V _{CCL} | | 0 | | 1 | V |

Note:

• During S/S OFF (standby), the Hall comparator is at High.

• Design target values are not measured.

Truth Table

| \smallsetminus | | | Control | | |
|------------------|--------------------|---|---------|----|----------------|
| | Source -> Sink | U | V | W | V _C |
| | Phase W -> Phase V | н | н | 1 | Н |
| I | Phase V -> Phase W | | | L | L |
| 2 | Phase W -> Phase U | н | L | 1 | н |
| 2 | Phase U -> Phase W | | | L | L |
| 3 | Phase V -> Phase W | | L | Н | Н |
| 5 | Phase W -> Phase V | | | | L |
| 4 | Phase U -> Phase V | L | н | 1 | н |
| 4 | Phase V -> Phase U | | | L | L |
| 5 | Phase V -> Phase U | н | L | н | н |
| 5 | Phase U -> Phase V | | | | L |
| 6 | Phase U -> Phase W | 1 | н | н | Н |
| Ø | Phase W -> Phase U | | - 11 | 11 | L |

Input:

H: Input 1 is higher in potential than input 2 by at least 0.2V.

L: Input 1 is lower in potential than input 2 by at least 0.2V.

Pin Assignment



Block Diagram



Sample Application Circuit



Pin Descriptions

| Pin number | Pin name | Pin voltage | Equivalent circuit | Pin function |
|------------|--|---------------------------------|--|---|
| 17 | V _{CC} 2 | 4V to 13.6V | | Source side predrive voltage supply pin |
| 18 | V _{CC} 3 | 4V to 13.6V | | Constant current control amplifier voltage supply pin |
| 21 | V _{CC} 1 | 4V to 6V | | Power supply pin for all circuits except output transistors, source predriver, and low current control amplifier |
| 1 | RS | | 100μA VCC1 100μA (14)(15) | Reverse detector pin Forward rotation: High Reverse rotation: Low |
| 2 | FG | | A11184 | 1 Hall element waveform Schmitt comparator composite output |
| 23 22 | U _{IN} 1 U _{IN} 2 | | ν _{cc1} | U phase Hall element input and reverse detector U phase Schmitt comparator input pin Logic High indicates U _{IN} 1 > U _{IN} 2. |
| 25 24 | V _{IN} 1 V _{IN} 2 | 1.2V to V _{CC} 1–1V | | V phase Hall element input and reverse detector V phase Schmitt comparator input pin Logic High indicates $V_{IN}1 > V_{IN}2$. |
| 27 26 | W _{IN} 1 W _{IN} 2 | | 25µА (↓) (↓) 25µА 777 777 777 777 777 A11185 | W phase Hall element input and reverse detector W phase Schmitt comparator input pin Logic High indicates W _{IN} 1 > W _{IN} 2. |
| 28 | V _H | | 75μA VCC1 75μA (3) 30 kΩ ≥ 2 kΩ ≥ 1 A11186 | Hall element lower side bias voltage supply pin |
| 4 | S/S | 0V to V _{CC} 1 | V _{CC} 1 17 50 kΩ 50 kΩ 75 kΩ 411187 | When this pin is at 0.7V or lower, or when it is open, all circuits are inactive. When driving motor, set this pin to 2V or higher. |
| 7 | SIG GND | | | GND pin for all circuits except output |
| 9 | FC | | | Control loop frequency compensa- tor pin. Connecting a capacitor between this pin and GND prevents closed loop oscillation in current limiting circuitry. |

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| Pin number | Pin name | Pin voltage | Equivalent circuit | Pin function |
|------------|-------------------|-------------------------|---|--|
| 10 | V _{CREF} | 2V to 3V | $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | Control reference voltage supply pin. Determines control start voltage. |
| 11 | V _C | 0V to V _{CC} 1 | 30 kΩ 100μA 100μA 100μA 100μA 100μA 100μA 100μA 100μA 100μA 100μA 100μA 100μA | Speed control voltage supply pin V type control technique $V_C > V_{CREF}$: Forward $V_C < V_{CREF}$: Slowdown (Reverse-blocking circuit built in to prevent reverse rotation.) |
| 12 | W _{OUT} | | | W phase output |
| 13 | PWR GND | | | Output transistor GND |
| 15 | V _{OUT} | | | V phase output |
| 16 | U _{OUT} | | | U phase output |
| 20 | RF | | 3.9Ω 3.9Ω 3.9Ω (23)(1)(2) (24) A11190 | Upper side output PNP transistor collector pin (common for all 3 phases). For current detection, connect resistor between V_{CC} 3 pin and RF pin. Constant current control and current limiter works by detecting this voltage. |
| 8 | сс | | Vcc1 75 kΩ 19 50 kΩ 411191 411191 | V type control/single-side control switching pin CC: High -> Single-side control Low/Open -> V type control |
| 3 | BRAKE | | 100µA ↓ VCC1 75 kΩ ↓ (16) 50 kΩ ↓ A11192 | Short brake pin BRAKE: High> Short brake operation Low/Open> Motor drive operation |

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