

## LH0062/LH0062C High Speed FET Operational Amplifier

#### **General Description**

The LH0062/LH0062C is a precision, high speed FET input operational amplifier with more than an order of magnitude improvement in slew rate and bandwidth over conventional FET IC op amps. In addition it features very closely matched input characteristics, very high input impedance, and ultra low input currents with no compromise in noise, common mode rejection ratio or open loop gain. The device has internal unity gain frequency compensation, thus assuring stability in all normal applications. This considerably simplifies its application, since no external components are necessary for operation. However, unlike most internally compensated amplifiers, external frequency compensation may be added for optimum performance. For inverting applications, feedforward compensation will boost the slew rate to over 120 V/μs and almost double the bandwidth. (See LB-2, LB-14, and LB-17 for discussions of the application of feed-forward techniques). Over-compensation can be used with the amplifier for greater stability when maximum bandwidth is not needed. Further, a single capacitor can be added to reduce the 0.1% settling time to under 1 µs. In addition it is free of latch-up and may be simply offset nulled with negligible effect on offset drift or CMRR.

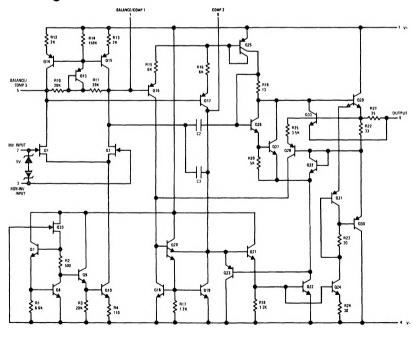
The LH0062 is designed for applications requiring wide bandwidth, high slew rate and fast settling time while at the same time demanding the high input impedance and low input currents characteristic of FET inputs. Thus it is particularly suited for such applications as video amplifiers, sample/hold circuits, high speed integrators, and buffers for A/D conversion and multiplex system. The LH0062 is specified for the full military temperature range of  $-55^{\circ}$  to  $+125^{\circ}{\rm C}$  while the LH0062C is specified to operate over a  $-25^{\circ}{\rm C}$  to  $+85^{\circ}{\rm C}$  temperature range.

#### **Features**

■ High slew rate	70 V/μs
■ Wide bandwidth	15 MHz
■ Settling time (0.1%)	1 μs
■ Low input offset voltage	2 mV
■ Low input offset current	1 pA
■ Wide supply range	+5V to +20V

- Internal 6 dB/octave frequency compensation
- Pin compatible with std IC op amps (TO-5 pkg)

#### **Schematic Diagram**



\*Pin Numbers Shown for TO-5 Package

#### **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 5)

Supply Voltage  $\pm 20V$ Power Dissipation (see graph) 500 mW

Input Voltage (Note 1) ± 5V
Differential Input Voltage (Note 2) ± 30V

Short Circuit Duration

Operating Temperature LH0062 LH0062C

Storage Temperature Range

ESD rating to be determined.

-55°C to +125°C -25°C to +85°C

-65°C to +150°C

Lead Temperature (Soldering, 10 sec.)

260°C

Continuous

DC Electrical Characteristics (Note 3)

Parameter	Conditions	Limits						
		LH0062			LH0062C			Units
		Min	Тур	Max	Min	Тур	Max	1
Input Offset Voltage	$R_S \le 100 \text{ k}\Omega$ , $T_A = 25^{\circ}\text{C}$ , $R_S \le 100 \text{ k}\Omega$		2	5 7		10	15 20	mV mV
Temperature Coefficient of Input Offset Voltage	R <sub>S</sub> ≤ 100 kΩ		25		1	25		μV/°C
Offset Voltage Drift with Time			4			5		μV/week
Input Offset Current	T <sub>A</sub> = 25°C		0.2	2 2		1	5 0.2	pA nA
Temperature Coefficient of Input Offset Current		Doubles every 10°C			Doubles every 10°C			
Offset Current Drift with Time			0.1			0.1		pA/week
Input Bias Current	T <sub>A</sub> = 25°C (Note 4)		5	10 10		10	65 2	pA nA
Temperature Coefficient of Input Bias Current		Doubles every 10°C		Doubles every 10°C				
Differential Input Resistance			1012			1012		Ω
Common Mode Input Resistance			1012			1012		Ω
Input Capacitance	-3		4			4		pF
Input Voltage Range	V <sub>S</sub> = ±15V	±10	± 12		±10	± 12		V
Common Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega, V_{IN} = \pm 10V$	80	90		70	90		dB
Supply Voltage Rejection Ratio	$R_S \le 10 \text{ k}\Omega, \pm 5V \le V_S \le \pm 15V$	80	90		70	90		₫B
Large Signal Voltage Gain	$\begin{aligned} & R_L = 2  k\Omega,  V_{OUT} =  \pm  10V, \\ & T_A = 25^{\circ}C,  V_S =  \pm  15V \\ & R_L = 2  k\Omega,  V_{OUT} =  \pm  10V, \\ & V_S =  \pm  15V \end{aligned}$	50 25	200		25 25	160		V/mV V/mV
Output Voltage Swing	$R_L = 2 k\Omega, T_A = 25^{\circ}C,$ $V_S = \pm 15V$	±12	± 13		± 12	13		V
	$R_L = 2 k\Omega, V_S = \pm 15V$	± 10			±10			V
Output Current Swing	$V_{OUT} = \pm 10V, T_A = 25^{\circ}C$	±10	±15		±10	±15		mA
Output Resistance			75			75		Ω
Output Short Circuit Current	T <sub>A</sub> = 25°C		25			25		mA .
Supply Current	$V_S = \pm 15V$		5	8		7	12	mA
Power Consumption	$V_S = \pm 15V$			240	L		360	mW

## AC Electrical Characteristics ( $T_A = 25^{\circ}C$ , $V_S = \pm 15V$ )

	Conditions	Limits						
Parameter		LH0062			LH0062C			Units
		Min	Тур	Max	Min	Тур	Max	
Slew Rate	Voltage Follower	50	70		50	70		V/µs
Large Signal Bandwidth	Voltage Follower		2			2		MHz
Small Signal Bandwidth			15			15		MHz
Rise Time			25			25		ns
Overshoot			10			15		%
Settling Time (0.1%)	$\Delta V_{IN} = 10V$		1			1_		μs
Overload Recovery			0.9			0.9		μs
Input Noise Voltage	$R_S = 10 \text{ k}\Omega$ , $f_0 = 10 \text{ Hz}$		150			150		nV/√Hz
Input Noise Voltage	$R_S = 10 \text{ k}\Omega, f_0 = 100 \text{ Hz}$		55			55		nV/√Hz
Input Noise Voltage	$R_S = 10 \text{ k}\Omega, f_0 = 1 \text{ kHz}$		35			35		nV/1∕Hz
Input Noise Voltage	$R_S = 10 \text{ k}\Omega, f_0 = 10 \text{ kHz}$		30			30		nV/1∕Hz
Input Noise Voltage	BW = 10 Hz to 10 kHz, $R_S = 10 \text{ k}\Omega$		12			12		μVrms
Input Noise Current	BW = 10 Hz to 10 kHz		<0.1			<0.1		pArms

Note 1: For supply voltages less than ± 15V, the absolute maximum input voltage is equal to the supply voltage.

Note 2: Inputs are protected from excessive voltages by back-to-back diodes. Input currents should be limited to 1 mA.

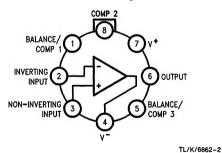
Note 3: Unless otherwise specified, these specifications apply for  $\pm 5\text{V} \leq \text{V}_S \leq \pm 20\text{V}$  and  $-55^{\circ}\text{C} \leq \text{T}_A \leq +125^{\circ}\text{C}$  for the LH0062 and  $-25^{\circ}\text{C} \leq \text{T}_A \leq +85^{\circ}\text{C}$  for the LH0062C. Typical values are given for  $\text{T}_A = 25^{\circ}\text{C}$ . Power supplies should be bypassed with 0.1  $\mu\text{F}$  ceramic capacitors.

Note 4: Input currents are a strong function of temperature. Due to high speed testing they are specified at a junction temperature T = 25°C, self heating will cause an increase in current in manual tests. 25°C spec is guaranteed by testing at 125°C.

Note 5: Refer to RETS0062X for LH0062D and LH0062H military specifications.

### **Connection Diagrams**

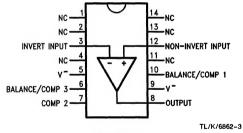
#### **Metal Can Package**



**Top View** 

Order Number LH0062H or LH0062CH See NS Package Number H08D

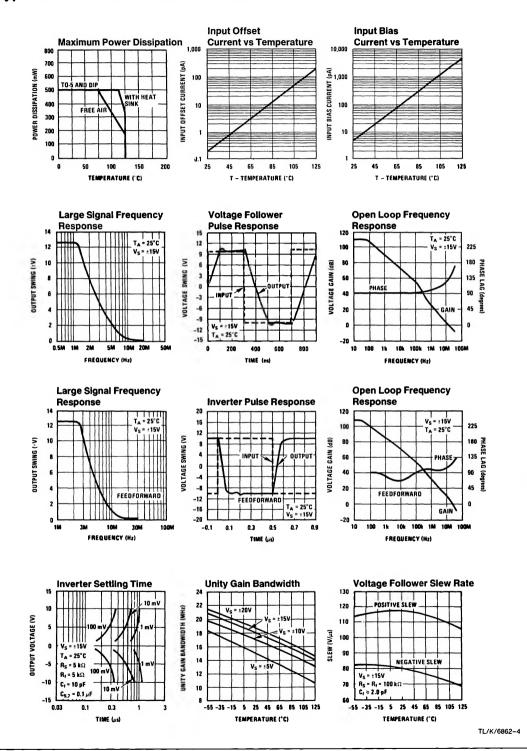
#### **Dual-In-Line Package**



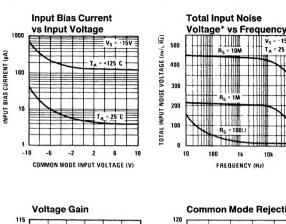
Top View

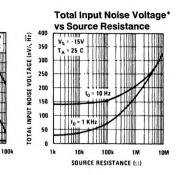
Order Number LH0062D or LH0062CD See NS Package Number D14E

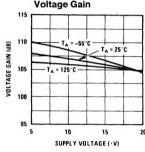
### **Typical Performance Characteristics**

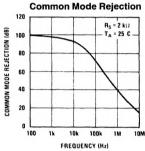


## **Typical Performance Characteristics (Continued)**

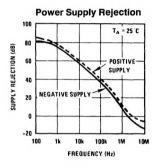


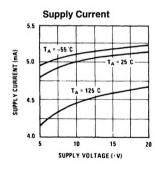


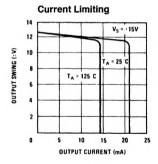


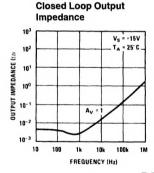


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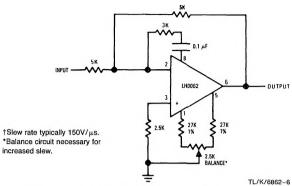




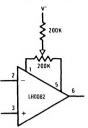
TL/K/6862-5 \*Noise Voltage Includes Contribution from Source Resistance

## **Auxiliary Circuits**

#### **Feedforward Compensation for Greater** Inverting Slew Rate†

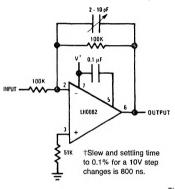


#### Offset Balancing

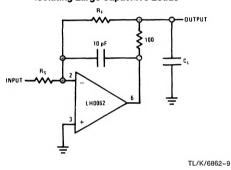


TL/K/6862-7

Compensation for Minimum Settling† Time

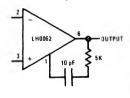


#### **Isolating Large Capacitive Loads**



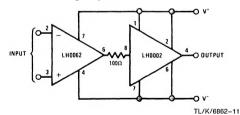
TL/K/6862-8

#### Overcompensation



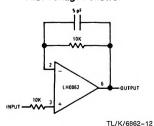
TL/K/6862-10

#### Boosting Output Drive to $\pm$ 100 mA

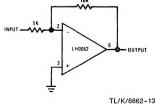


## **Typical Applications\***

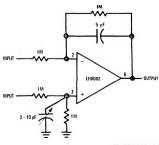
**Fast Voltage Follower** 



## **Fast Summing Amplifier**

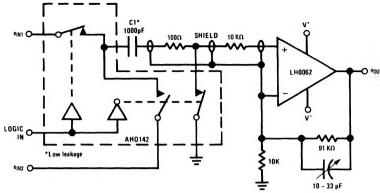


#### **Differential Amplifier**



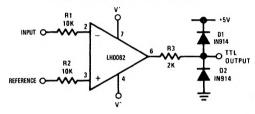
# Typical Applications\* (Continued)

## High Speed Subtractor



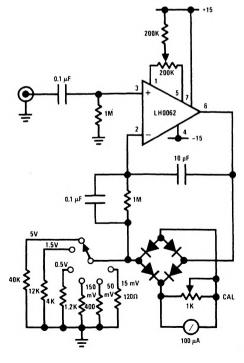
TL/K/6862-15

#### **Fast Precision Voltage Comparator**



TL/K/6862-16

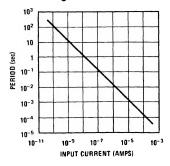
#### Wide Range AC Voltmeter



## Typical Applications\* (Continued) **Video DC Restoring Amplifier** VIDEO IN O-OBUTPUT LH0062 R3 1K LOGIC CONTROL O \*Pin numbers shown for TO-5 package TL/K/6862-18 **High Speed Positive Peak Detector** LM118 LH0062 1000 pF -TL/K/6862-19 **Precision Integrator** AH0133 TTL INTEGRATE/RESET CONTROL C1 0.1 μF R2 10K R1 109Ω LH0062 R3 10K $V_{OUT} = \frac{1}{C1R1} \int -V_1 dt + V_2$ INITIAL CONDITION V, TL/K/6862-20 \*Pin numbers shown for TO-5 package

## Typical Applications\* (Continued)

#### Precision Wide Range Current to Period Converter



TL/K/6862-21

