

## **LH4001 Wideband Current Buffer**

### **General Description**

The LH4001 is a high speed unity gain buffer designed to provide high current drive capability at frequencies from DC to over 25 MHz. It is capable of providing a continuous output current of  $\pm$ 100 mA and a peak of  $\pm$ 200 mA.

The LH4001 is designed to fulfill a wide range of applications such as impedance transformation, high impedance input buffers for A/D converters and comparators, as well as high speed line drivers. It is also suitable for use in current booster applications within an op amp loop. This allows the output current capability of existing op amps to be increased to ± 100 mA.

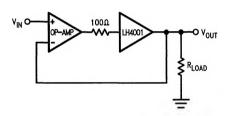
#### **Features**

- DC to 25 MHz bandwidth
- 125 V/µs slew rate
- Drives ±10V into 50Ω
- Operates from ±5 to ±20V supplies
- Output swing approaches supply voltage

## **Applications**

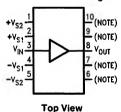
- Boost op amp output
- Buffer amplifiers
- Isolate capacitive loads
- Drive long cables

## **Typical Applications and Connection Diagram**



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#### **Dual-In-Line Package**



\*Note: Electrically connected internally. No connection should be made to these pins.

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Order Number LH4001CN See NS Package Number N10A

## **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage, V<sub>S</sub> ± 22V
Continuous Output Current, I<sub>O</sub> ± 100 mA

Peak Output Current, I<sub>O(peak)</sub> (50 ms On/1 Sec Off)

± 200 mA

Input Voltage Range, V<sub>IN</sub>
Power Dissipation Ambient
Storage Temperature Range, T<sub>STG</sub>
Operating Temperature Range, T<sub>A</sub>
Maximum Junction Temperature, T<sub>J</sub>
Lead Temp. (Soldering, <10 seconds)
ESD rating is to be determined.

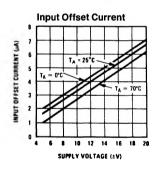
±V<sub>S</sub> 500 mW -65°C to +150°C 0°C to +70°C 150°C 260°C

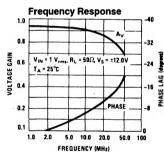
#### **Electrical Characteristics (Note 1)**

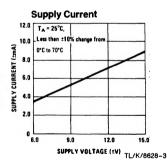
Symbol	Parameter	Conditions	Min	Тур	Max	Units
A <sub>V</sub>	Voltage Gain	$R_S = 10 \text{ k}\Omega, R_L = 1 \text{ k}\Omega$ $V_{\text{IN}} = \pm 10 \text{V}$	0.95	0.97	1	V/V
R <sub>IN</sub>	Input Impedance	$R_S = 200 \text{ k}\Omega, R_L = 1 \text{ k}\Omega$ $V_{IN} = \pm 1.0 \text{V}$	180	400		. , kΩ
ROUT	Output Impedance	$R_{S} = 10 \text{ k}\Omega, R_{L} = 50\Omega$ $V_{IN} = \pm 1.0V$		6	10	Ω
V <sub>O</sub>	Output Swing	$V_S = \pm 15V, R_S = 50\Omega$ $R_L = 1 k\Omega, V_{IN} = \pm 12V$	±10	±11		v
IB	Input Bias Current	$R_S = 10 \text{ k}\Omega, R_L = 1 \text{ k}\Omega$		± 10	±50	μA
t <sub>r</sub>	Rise Time	$R_L = 100\Omega$ , $\Delta V_{IN} = 100 \text{ mV}$		7		ns
SR	Slew Rate	$V_{IN} = \pm 5V, R_L = 100\Omega$		125		V/µs
Is	Supply Current	$R_S = 10 \text{ k}\Omega$		±6	±10	mA
Vos	DC Output Offset Voltage	$R_S = 300\Omega$ , $R_L = 1 k\Omega$		±10	±50	m∨

Note 1: Specification applies for TA = 25°C with +12V on Pins 1 & 2; -12V on Pins 4 & 5 unless otherwise specified.

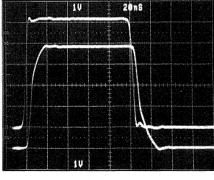
# **Typical Performance Characteristics**











TOP TRACE = INPUT

BOTTOM TRACE = OUTPUT

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 $V_{IN} = \pm 2.5V$ ,  $R_S = R_L = 50\Omega$ 

### **Applications Information**

Figure 1 shows a simple implementation of a non-inverting buffer amplifier of unity gain. Popular industry standard operational amplifiers such as LF156, LF351, LF411, LF441, LM11, LM741, etc. can be used in this configuration. Due to the high bandwidth of the LH4001, it is suitable for use with most monolithic op amps.

Figure 2 shows an implementation of an inverting amplifier with output current capability in excess of  $\pm 100$  mA. The gain of this amplifier is determined by the values of R<sub>F</sub> and R<sub>IN</sub>. The resistor between the non-inverting input and ground is used to minimize the output offset voltage resulting from the input bias current.

Because of its high current drive capability, the LH4001 buffer amplifier is suitable for driving terminated or unterminated co-axial cables, and high current or reactive loads.

Figure 3 shows a co-axial cable drive circuit. The  $43\Omega$  resistor matches the driving source to the cable, however, its inclusion rarely will result in substantial improvement in pulse response into a terminated cable. If the  $43\Omega$  resistor is included, the output voltage to the load is about half what it would be without the near end termination.

Figure 4 shows a non-inverting amplifier with gain and output current capability in excess of  $\pm\,100$  mA. It is capable of providing  $\pm\,10$  mA into a 1 kΩ load or  $\pm\,100$  mA into a 100Ω load ( $\pm\,10V$  swing). Figures 5 and 6 show two different methods of providing current limit or short circuit protection for the LH4001. In Figure 6, the 10Ω resistor limits the output current to approximately 70 mA. This circuit is highly recommended if there is a potential for a short circuit to occur.

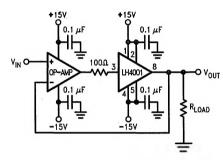


FIGURE 1. Non-Inverting Buffer Amplifier

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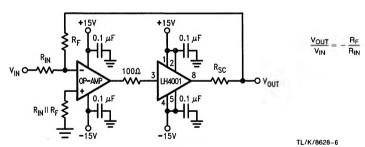


FIGURE 2. Inverting Buffer Amplifier with Current Limit

# **Applications Information (Continued)**

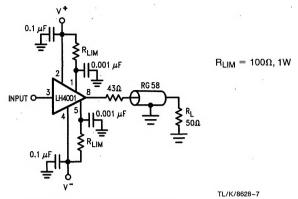


FIGURE 3. Coaxial Cable Drive Circuit

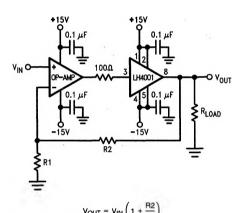
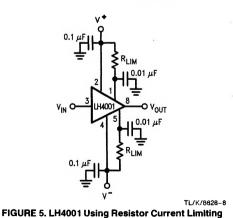
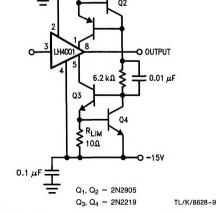


FIGURE 4. Non-Inverting Buffer Amplifier with Gain





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FIGURE 6. Current Limit Using Current Sources