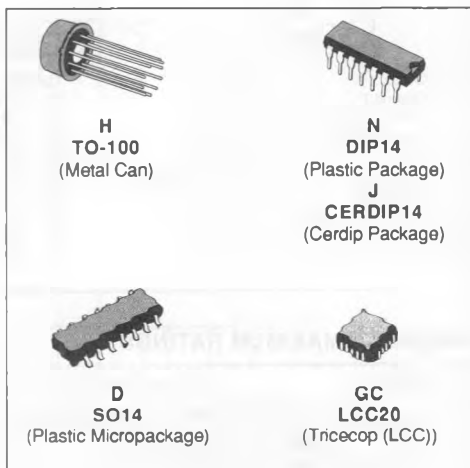


HIGH SPEED DUAL COMPARATORS

- TWO INDEPENDENT COMPARATORS
- OPERATION FROM A SINGLE + 5 V SUPPLY
- TYPICALLY 80 ns RESPONSE TIME AT ± 15 V
- MINIMUM FAN-OUT OF 2 EACH SIDE
- MAXIMUM INPUT CURRENT OF 1 μ A OVER OPERATING TEMPERATURE RANGE
- INPUTS AND OUTPUTS CAN BE ISOLATED FROM SYSTEM GROUND
- HIGH COMMON-MODE SLEW RATE



DESCRIPTION

These products are precision high speed dual comparators designed to operate over a wide range of supply voltages down to a single 5 V logic supply and ground and have low input currents and high gains.

The open collector of the output stage makes compatible with TTL as well as capable of driving lamps and relays at currents up to 25 mA.

Although designed primarily for applications requiring operation from digital logic supplies, are fully specified for power supplies up to 15 V.

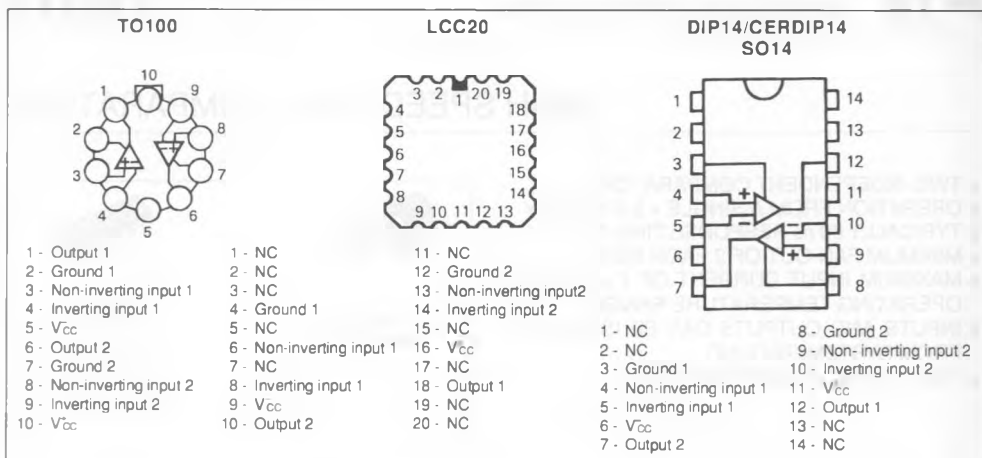
They feature faster response than the LM111 at the expense of higher power dissipation. However, the high speed, wide operating voltage range and low package count make the much more versatile.

ORDER CODES

Part Number	Temperature Range	Package				
		H	N	J	D	GC
LM119	- 55 to + 125 °C	•		•		•
LM219	- 40 to + 105 °C	•	•	•	•	
LM319	0 to + 70 °C		•	•	•	

Note : Hi-Rel Versions Available.
Examples : LM119H, LM219N.

PIN CONNECTIONS (top views)

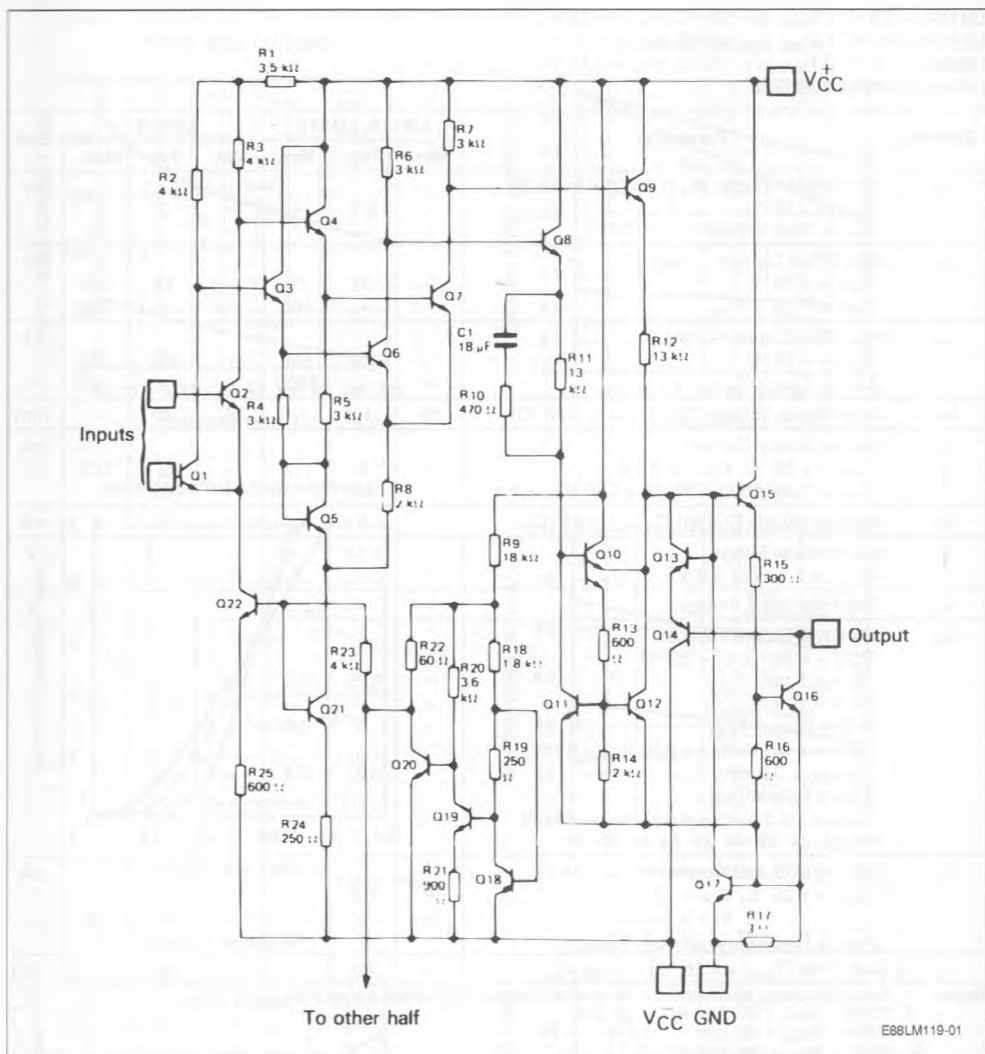


ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	LM119	LM219	LM319	Unit
$V_O - V_{CC}$	Output to Negative Supply Voltage	36	36	36	V
V_{CC}	Negative Supply Voltage	25	25	25	V
V_{CC}	Positive Supply Voltage	18	18	18	V
V_{ID}	Differential Input Voltage	± 5	± 5	± 5	V
V_I	Input Voltage – (note 1)	± 15	± 15	± 15	V
P_{tot}	Power Dissipation – (note 2)	500	500	500	mW
T_{oper}	Operating Free-air Temperature Range	-55 to $+125$	-40 to $+105$	0 to $+70$	$^{\circ}C$
T_{stg}	Storage Temperature Range	-65 to $+150$	-65 to $+150$	-65 to $+150$	$^{\circ}C$

* All potentials referenced to ground unless otherwise specified.

SCHEMATIC DIAGRAM



CASE	Outputs	Inverting Inputs	Non-inverting Inputs	GND	V _{CC}	V _{CC}	N.C.
TO100	1, 6	4, 9	3, 8	2, 7	10	5	—
DIP14/ CERDIP14/ SO14	7, 12	5, 10	4, 9	3, 8	11	6	1, 2, 13, 14
LCC20	10, 18	8, 14	6, 13	4, 12	16	9	*

* LCC20 : Other pins are not connected

ELECTRICAL CHARACTERISTICS

LM119 : $-55^{\circ}\text{C} \leq T_{\text{amb}} \leq +125^{\circ}\text{C}$, $V_{\text{CC}} = \pm 15\text{ V}$ LM219 : $-40^{\circ}\text{C} \leq T_{\text{amb}} \leq +105^{\circ}\text{C}$, $V_{\text{CC}} = \pm 15\text{ V}$ LM319 : $0^{\circ}\text{C} \leq T_{\text{amb}} \leq +70^{\circ}\text{C}$, $V_{\text{CC}} = \pm 15\text{ V}$

(unless otherwise specified)

Symbol	Parameter	LM119, LM219			LM319			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
V_{IO}	Input Offset Voltage ($R_{\text{S}} \leq 5\text{ k}\Omega$) – (note 3) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$	–	0.7	4	–	2	8	mV
		–	–	7	–	–	10	
I_{IO}	Input Offset Current – (note 3) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$	–	30	75	–	80	200	nA
		–	–	100	–	–	300	
I_{IB}	Input Bias Current – (note 3) $T_{\text{amb}} = +25^{\circ}\text{C}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$	–	150	500	–	250	1000	nA
		–	–	1000	–	–	1200	
A_{VD}	Large Signal Voltage Gain ($T_{\text{amb}} = +25^{\circ}\text{C}$)	10	40	–	8	40	–	V/mV
I_{CC}	Positive Supply Current $T_{\text{amb}} = +25^{\circ}\text{C}$, $V_{\text{CC}} = \pm 15\text{ V}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$, $V_{\text{CC}} = +5\text{ V}$, $V_{\text{CC}} = 0\text{ V}$	–	8	11.5	–	8	12.5	mA
		–	4.3	–	–	4.3	–	
I_{CC}	Negative Supply Current ($T_{\text{amb}} = +25^{\circ}\text{C}$)	–	3	4.5	–	3	5	mA
V_{I}	Input Voltage Range ($V_{\text{CC}} = +5\text{ V}$, $V_{\text{CC}} = 0\text{ V}$)	–	± 13	–	–	± 13	–	V
		1	–	3	1	–	3	
V_{ID}	Differential Input Voltage	–	–	± 5	–	–	± 5	V
V_{OL}	Low Level Output Voltage $T_{\text{amb}} = +25^{\circ}\text{C}$, $I_{\text{O}} = 25\text{ mA}$ $V_{\text{I}} < -5\text{ mV}$ $V_{\text{I}} < -10\text{ mV}$ $0^{\circ}\text{C} \leq T_{\text{amb}} \leq T_{\text{max}}$ $V_{\text{CC}} > +4.5\text{ V}$, $V_{\text{CC}} = 0\text{ V}$, $V_{\text{I}} < -6\text{ mV}$, $I_{\text{O(sink)}} < 3.2\text{ mA}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$ $V_{\text{CC}} > +4.5\text{ V}$, $V_{\text{CC}} = 0\text{ V}$, $V_{\text{I}} < -10\text{ mV}$, $I_{\text{O(sink)}} \leq 3.2\text{ mA}$	–	0.75	1.5	–	–	–	V
		–	–	–	–	0.75	1.5	
		–	–	0.6	–	–	–	
		–	0.23	0.4	–	–	–	
		–	–	–	–	0.3	0.4	
I_{OH}	High Level Output Current ($V_{\text{O}} = +35\text{ V}$) $T_{\text{amb}} = +25^{\circ}\text{C}$, $V_{\text{I}} > +5\text{ mV}$ $V_{\text{I}} > +10\text{ mV}$ $T_{\text{min}} \leq T_{\text{amb}} \leq T_{\text{max}}$, $V_{\text{I}} > 5\text{ mV}$	–	0.2	2	–	–	–	μA
		–	–	–	–	0.2	10	
		–	1	10	–	–	–	
t_{r}	Rise Time ($T_{\text{amb}} = +25^{\circ}\text{C}$) – (note 4)	–	80	–	–	80	–	ns

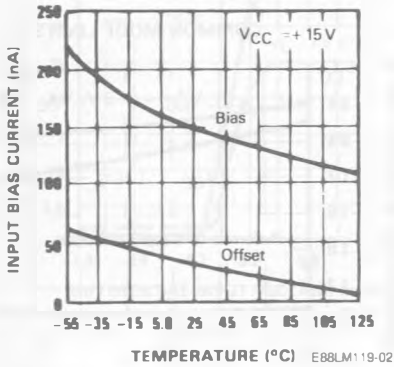
Notes : 1. For supply voltages less than $\pm 15\text{ V}$, the absolute maximum input voltage is equal to the supply voltage.2. TO100 : $R_{\text{th(j-a)}} = 160^{\circ}\text{C/W}$, $R_{\text{th(j-c)}} = 45^{\circ}\text{C/W}$ DIP14 : $R_{\text{th(j-a)}} = 150^{\circ}\text{C/W}$ SO14 : $R_{\text{th(j-a)}} = 250^{\circ}\text{C/W}$ 3. These specifications apply for $V_{\text{CC}} = \pm 15\text{ V}$, unless otherwise stated. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single $\pm 5\text{ V}$ supply up to $\pm 15\text{ V}$ supplies. The offset voltages and offset current given are the maximum values required to drive the output down to 1 V or up to $+14\text{ V}$ with a 1 mA load current.

Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

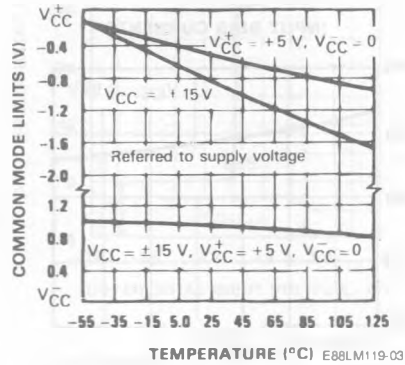
4. The response time specified is for a 100 mV input step with 5 mV overdrive.

LM119-LM219

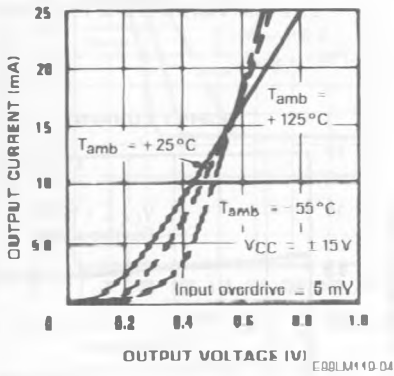
INPUT BIAS CURRENTS



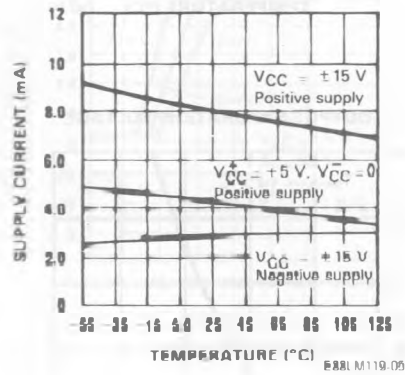
COMMON MODE LIMITS



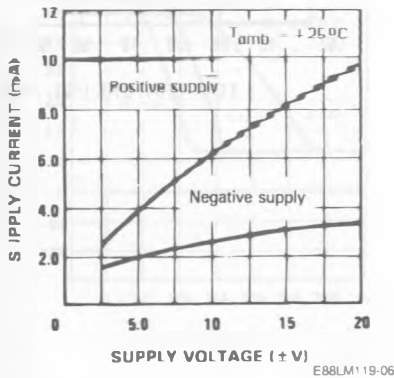
OUTPUT SATURATION VOLTAGE



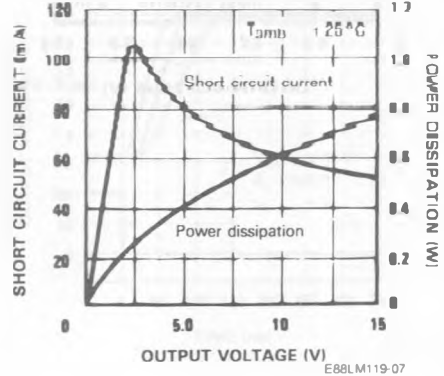
SUPPLY CURRENT



SUPPLY CURRENT

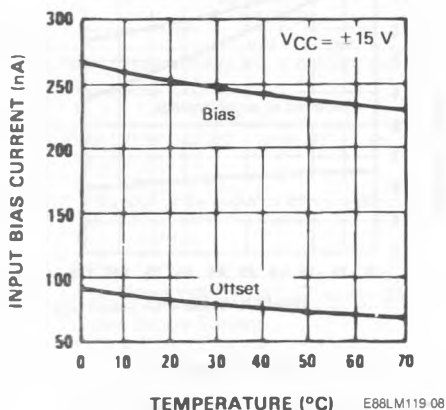


OUTPUT LIMITING CHARACTERISTICS

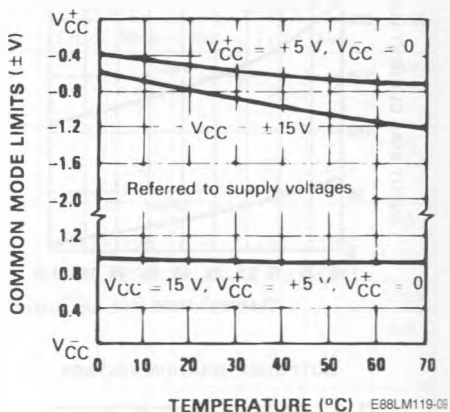


LM319

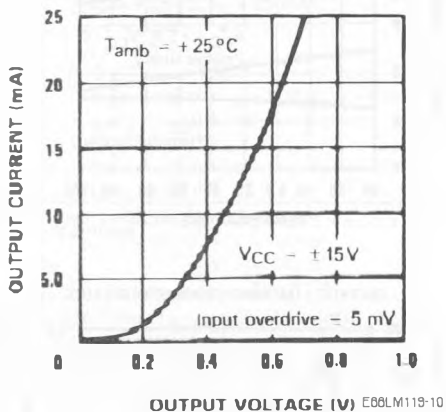
INPUT BIAS CURRENTS



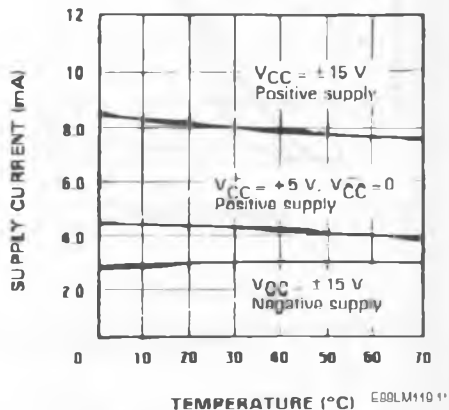
COMMON MODE LIMITS



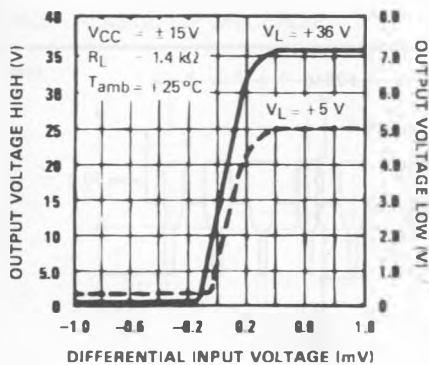
OUTPUT SATURATION VOLTAGE



SUPPLY CURRENT

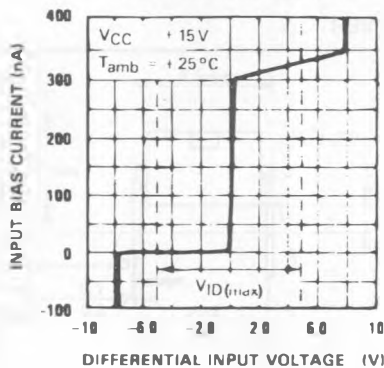


TRANSFER FUNCTION



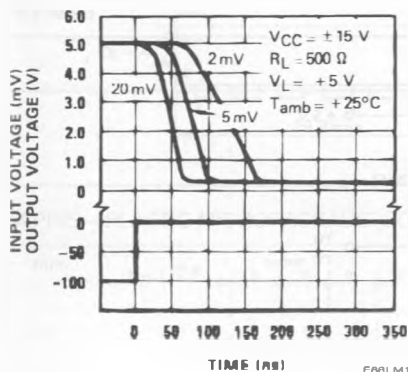
E88LM119-12

INPUT CHARACTERISTICS



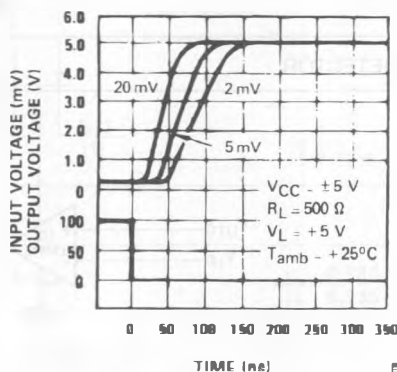
E88LM119-13

RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES



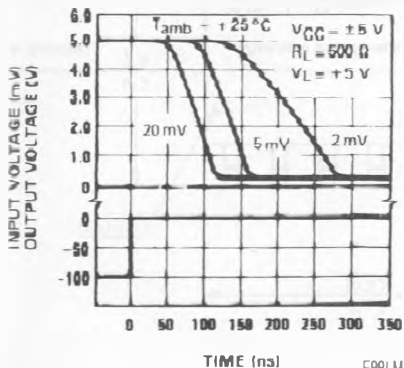
E88LM119-15

RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES



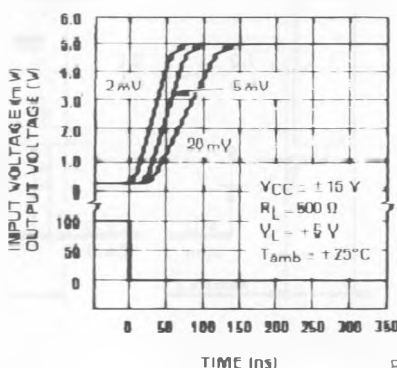
E88LM119-16

RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES



E88LM119-16

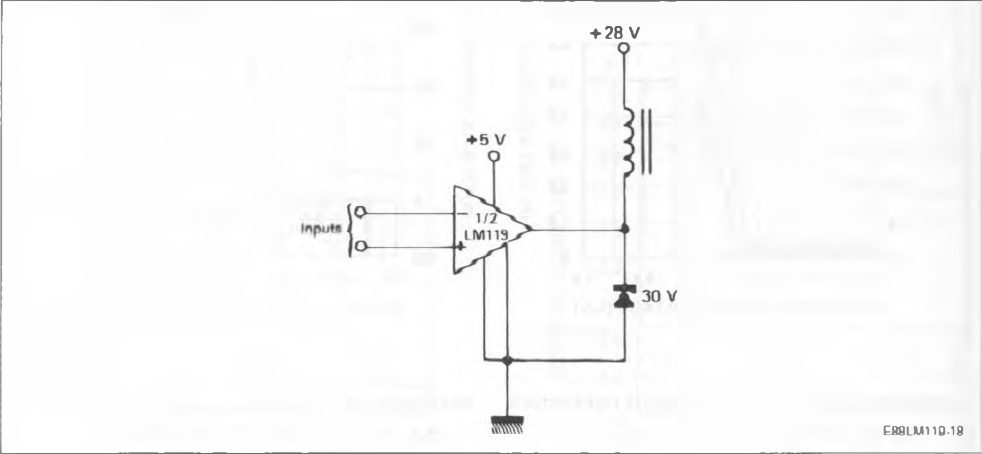
RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES



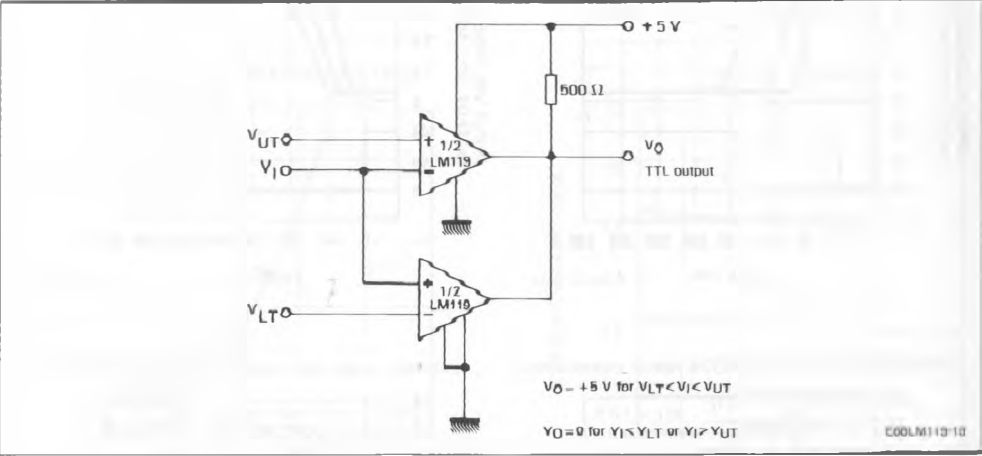
E88LM119-17

TYPICAL APPLICATION DIAGRAMS

RELAY DRIVER

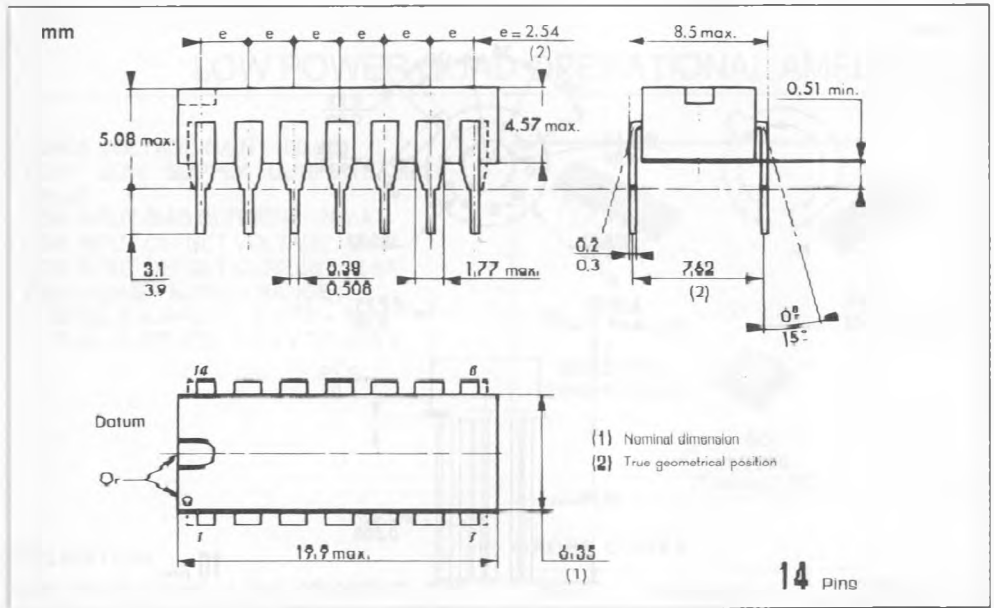


WINDOW DETECTOR

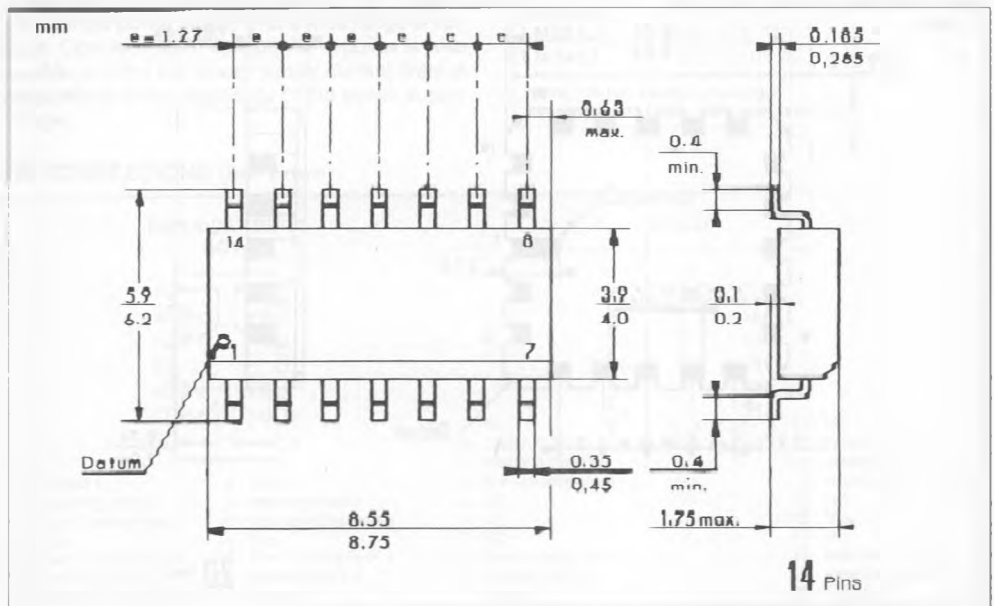


PACKAGE MECHANICAL DATA

14 PINS – PLASTIC DIP OR CerdIP

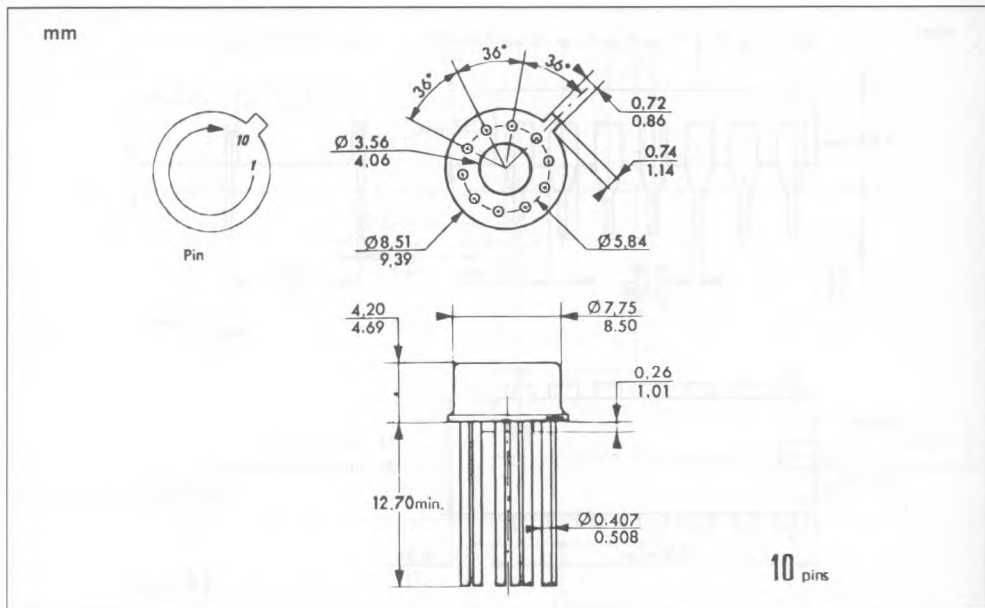


14 PINS – PLASTIC MICROPACKAGE (SO)



PACKAGE MECHANICAL DATA (continued)

10 PINS – METAL CAN TO100



20 PINS – TRICECOP (LCC)

