

# LM2877 Dual 4-Watt Power Audio Amplifier

# **General Description**

The LM2877 is a monolithic dual power amplifier designed to deliver 4W/channel continuous into  $8\Omega$  loads. The LM2877 is designed to operate with a low number of external components, and still provide flexibility for use in stereo phonographs, tape recorders and AM-FM stereo receivers, etc. Each power amplifier is biased from a common internal regulator to provide high power supply rejection and output Q point centreing. The LM2877 is internally compensated for all gains greater than 10, and comes in an 11-lead single-in-line package.

### **Features**

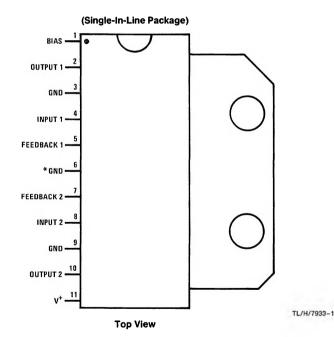
- 4W/channel
- -68 dB ripple rejection, output referred
- -70 dB channel separation, output referred

- Wide supply range, 6-24V
- Very low cross-over distortion
- Low audio band noise
- AC short circuit protected
- Internal thermal shutdown

# **Applications**

- Multi-channel audio systems
- Stereo phonographs
- Tape recorders and players
- AM-FM radio receivers
- Servo amplifiers
- Intercom systems
- Automotive products

# **Connection Diagram**



Order Number LM2877P See NS Package Number P11A

"Pin 6 can be connected to pin 3 or pin 9, if not, pin 6 must be left with NO connection.

# **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage

Input Voltage ±0.7V Operating Temperature Storage Temperature

Junction Temperature Lead Temperature (Soldering, 10 sec.)

0°C to +70°C -65°C to +150°C

150°C

260°C

# **Electrical Characteristics** $V_S = 20V$ , $T_{TAB} = 25^{\circ}C$ , $R_L = 8\Omega$ , $A_V = 50$ (34 dB) unless otherwise specified.

Parameter	Conditions	Min	Тур	Max	Units
Total Supply Current	$P_0 = 0W$		25	50	mA
Operating Supply Voltage		6		24	V
Output Power/Channel		4.0 1.5	4.5 3.6 1.9		w w w
Distortion, THD	$\begin{array}{l} f=1\text{ kHz, }V_S=20V\\ P_O=50\text{ mW/Channel}\\ P_O=1\text{W/Channel}\\ P_O=2\text{W/Channel}\\ f=1\text{ kHz, }V_S=12V, R_L=4\Omega\\ P_O=50\text{ mW/Channel}\\ P_O=500\text{ mW/Channel}\\ P_O=500\text{ mW/Channel}\\ P_O=1\text{W/Channel} \end{array}$		0.1 0.07 0.07 0.25 0.20 0.15	1	% % % %
Output Swing	$R_L = 8\Omega$		V <sub>S</sub> -4		V <sub>p-p</sub>
Channel Separation	$C_F=50~\mu F,~C_{IN}=0.1~\mu F,~f=1~kHz,$ Output Referred $V_S=20V,~V_O=4~Vrms$ $V_S=7V,~V_O=0.5~Vrms$	-50	-70 -60		dB dB
PSRR Power Supply	$C_F = 50 \mu F$ , $C_{IN} = 0.1 \mu F$ , $f = 120 \text{ Hz}$				
Rejection Ratio	Output Referred  V <sub>S</sub> = 20V, V <sub>RIPPLE</sub> = 1 Vrms  V <sub>S</sub> = 7V, V <sub>RIPPLE</sub> = 0.5 Vrms	-50	-68 -40		dB dB
Noise	Equivalent Input Noise  R <sub>S</sub> = 0, C <sub>IN</sub> = 0.1 μF, BW = 20 Hz-20 kHz  Output Noise Wideband		2.5		μV mV
Open Loop Gain	$R_S = 0$ , $C_{IN} = 0.1 \mu F$ , $A_V = 200$ $R_S = 0$ , $f = 1 \text{ kHz}$ , $R_L = 8\Omega$	-	70		dB
Input Offset Voltage	113 0,1 11112,111 032	-	15		mV
Input Bias Current		_	50		nA
Input Impedance	Open Loop		4		MΩ
DC Output Level	V <sub>S</sub> = 20V	9	10	11	V
Slew Rate			2.0		V/μ
Power Bandwidth		_	65		kHz
Current Limit			1.0		A

Note 1: For operation at ambient temperature greater than 25°C, the LM2877 must be derated based on a maximum 150°C junction temperature using a thermal resistance which depends upon device mounting techniques.

# **Equivalent Schematic Diagram** es o

#### **Typical Performance Characteristics Power Supply Rejection Ratio Power Supply Rejection Ratio Device Dissipation vs** (Referred to the Output) vs (Referred to the Output) vs **Ambient Temperature** Frequency Frequency 70 10 VRIPPLE = 1 Vrm ALUMINUM THICKNESS - 1/16 INCH (48) CIN = 0.0047 µF OWER SUPPLY REJECTION (dB) Av = 50 3 POWER SUPPLY REJECTION 50 15° C/W 50 IN 46.8° C/W 7 DEVICE DISSIPATION 40 30 30 20 20 VRIPPLE = 1 2 1 Ay = 50 | n 10 20 30 40 50 60 10 100 10 11 FREQUENCY (Ha) TA-AMBIENT TEMPERATURE (°C) FREQUENCY (Hz) **Power Supply Rejection Ratio** (Referred to the Output) vs **Channel Separation (Referred) Channel Separation (Referred)** Supply Voltage to the Output) vs Frequency to the Output) vs Frequency CBYPASS = 50 µF NOISE -VCC = 7V CHANNEL SEPARATION (4B) CIN = 0.1 µF (48) Vg = 500 mVrms POWER SUPPLY REJECTION CHANNEL SEPARATION Ay = 50 VRIPPLE \* 0.3 Vrms 60 CRYPASS VRIPPLE = 0.5 Vrms VCC = 20V CBYPASS = 50 Vout = 4 V CIN . 0.1 .F V = 50 20 50 VRIPPLE . 1 VI f = 120 Hz Av = 50 40 6 12 10 1k 10 16 SUPPLY VOLTAGE (V) FREQUENCY (Hz) FREQUENCY (Hz) **Total Harmonic Distortion Average Supply Current vs Total Harmonic Distortion Power Output** vs Frequency vs Frequency 800 V<sub>CC</sub> = 14V # **FOTAL HARMONIC DISTORTION (%)** TOTAL HARMONIC DISTORTION (%) (mA) = 0.5W = **8**Ω **AVERAGE SUPPLY CURRENT** 600 200 0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 100 14 1k POWER OUTPUT (W/CHANNEL) FREQUENCY (Hz) FREQUENCY (Hz) **Power Dissipation vs** Open Loop Gain vs **Output Swing vs Supply Power Output** Frequency Voltage 100 R<sub>L</sub> = 8Ω RL = 8Ω VS = 20V DEVICE DISSIPATION (W) BOTH CHANNELS OPERATING 80 **OUTPUT SWING (Vp-p)** VOLTAGE GAIN (4B) THD = 39 20V 40 20

FREQUENCY (Hz)

8 10 12 14 16

SUPPLY VOLTAGE (V)

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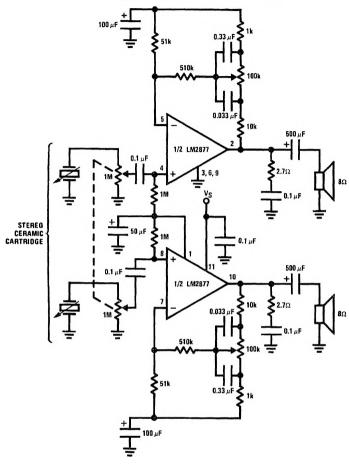
0

POWER OUTPUT (W/CHANNEL)

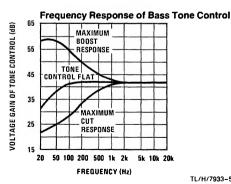
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# **Typical Applications**

Stereo Phonograph Amplifier with Bass Tone Control



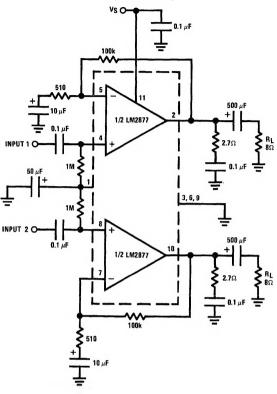
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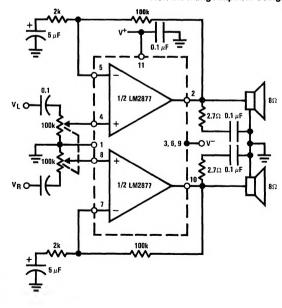
# Typical Applications (Continued)

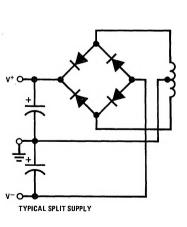
## Stereo Amplifier with $A_V=200\,$



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## **Non-Inverting Amplifier Using Split Supply**

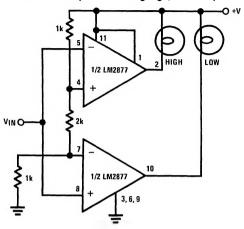




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# Typical Applications (Continued)

## Window Comparator Driving High, Low Lamps



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**Truth Table** 

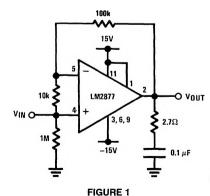
V <sub>IN</sub>	High	Low
<1/4 V+	Off	On
1/4 V + to 3/4 V +	Off	Off
>3/4 V+	On	Off

# **Application Hints**

The LM2877 is an improved LM377 in typical audio applications. In the LM2877, the internal voltage regulator for the input stage is generated from the voltage on pin 1. Normally, the input common-mode range is within  $\pm 0.7 V$  of this pin 1 voltage. Nevertheless, the common-mode range can be increased by externally forcing the voltage on pin 1. One way to do this is to short pin 1 to the positive supply, pin 11.

The only special care required with the LM2877 is to limit the maximum input differential voltage to  $\pm 7 \text{V}.$  If this differential voltage is exceeded, the input characteristics may change.

Figure 1 shows a power op amp application with  $A_V=1$ . The 100k and 10k resistors set a noise gain of 10 and are dictated by amplifier stability. The 10k resistor is bootstrapped by the feedback so the input resistance is dominated by the 1 M $\Omega$  resistor.



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