# LM2930 3-Terminal Positive Regulator

## **General Description**

The LM2930 3-terminal positive regulator features an ability to source 150 mA of output current with an input-output differential of 0.6V or less. Efficient use of low input voltages obtained, for example, from an automotive battery during cold crank conditions, allows 5V circuitry to be properly powered with supply voltages as low as 5.6V. Familiar regulator features such as current limit and thermal overload protection are also provided.

Designed originally for automotive applications, the LM2930 and all regulated circuitry are protected from reverse battery installations or 2 battery jumps. During line transients, such as a load dump (40V) when the input voltage to the regulator can momentarily exceed the specified maximum operating voltage, the regulator will automatically shut down to protect both internal circuits and the load. The LM2930 cannot be harmed by temporary mirror-image insertion.

Fixed outputs of 5V and 8V are available in the plastic TO-220 power package.

#### **Features**

- Input-output differential less than 0.6V
- Output current in excess of 150 mA
- Reverse battery protection
- 40V load dump protection
- Internal short circuit current limit
- Internal thermal overload protection
- Mirror-image insertion protection
- 100% electrical burn-in in thermal limit

## Voltage Range

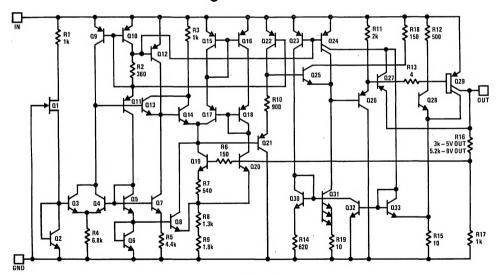
LM2930T-5.0

5V

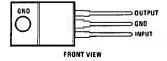
LM2930T-8.0

8V

## **Schematic and Connection Diagrams**



(TO-220) Plastic Package



TL/H/5539-1

Order Number LM2930T-5.0 or LM2930T-8.0 See NS Package T03B

## **Absolute Maximum Ratings**

if Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage

Operating Range Overvoltage Protection Reverse Voltage (100 ms) Reverse Voltage (DC) 26V 40V -12V

-6V

Internal Power Dissipation (Note 1)
Operating Temperature Range
Maximum Junction Temperature

Storage Temperature Range Lead Temp. (Soldering, 10 seconds) Internally Limited -40°C to +85°C 125°C -65°C to +150°C

230°C

## **Electrical Characteristics** (Note 2)

LM2930T-5.0  $V_{IN}$  = 14V,  $I_{O}$  = 150 mA,  $T_{I}$  = 25°C (Note 5), C2 = 10  $\mu$ F, unless otherwise specified

Parameter	Conditions	Тур	Tested Limit (Note 3)	Design Limit (Note 4)	Unit
Output Voltage		5	5.3 4.7		VMAX VMIN
	$6V \le V_{IN} \le 26V$ , 5 mA $\le I_O \le 150$ mA -40°C $\le T_J \le 125$ °C			5.5 4.5	V <sub>MAX</sub> V <sub>MIN</sub>
Line Regulation	$9V \le V_{IN} \le 16V$ , $I_O = 5$ mA $6V \le V_{IN} \le 26V$ , $I_O = 5$ mA	7 30	25 80	1	mV <sub>MAX</sub> mV <sub>MAX</sub>
Load Regulation	5 mA≤I <sub>O</sub> ≤150 mA	14	50		mV <sub>MAX</sub>
Output Impedance	100 mA <sub>DC</sub> & 10 mA <sub>rms</sub> , 100 Hz – 10 kHz	200			mΩ
Quiescent Current	I <sub>O</sub> = 10 mA I <sub>O</sub> = 150 mA	4 18	7 40	8	mA <sub>MAX</sub> mA <sub>MAX</sub>
Output Noise Voltage	10 Hz – 100 kHz	140			μV <sub>rms</sub>
Long Term Stability		20		100	mV/1000 hr
Ripple Rejection	f <sub>O</sub> =120 Hz	56			dB
Current Limit		400	700 150		mA <sub>MAX</sub> mA <sub>MIN</sub>
Dropout Voltage	I <sub>O</sub> =150 mA	0.32	0.6		V <sub>MAX</sub>
Output Voltage Under Transient Conditions	$-12V \le V_{ N} \le 40V, R_L = 100\Omega$		5.5 -0.3		V <sub>MAX</sub> V <sub>MIN</sub>

### **Electrical Characteristics** (Note 2)

LM2930T-8.0 ( $V_{IN}$ = 14V,  $I_O$ = 150 mA,  $T_i$ = 25°C (Note 5), C2=10  $\mu$ F, unless otherwise specified)

Parameter	Conditions	Тур	Tested Limit (Note 3)	Design Limit (Note 4)	Unit
Output Voltage		8	8.5 7.5		V <sub>MAX</sub> V <sub>MIN</sub>
	$9.4V \le V_{IN} \le 26V$ , 5 mA $\le I_O \le 150$ mA, -40°C $\le T_J \le 125$ °C			8.8 7.2	V <sub>MAX</sub> V <sub>MIN</sub>
Line Regulation	$9.4V \le V_{IN} \le 16V$ , $I_O = 5$ mA $9.4V \le V_{IN} \le 26V$ , $I_O = 5$ mA	12 50	50 100		mV <sub>MAX</sub> mV <sub>MAX</sub>
Load Regulation	5 mA≤I <sub>O</sub> ≤150 mA	25	50		mV <sub>MAX</sub>
Output Impedance	100 mA <sub>DC</sub> & 10 mA <sub>rms</sub> , 100 Hz – 10 kHz	300			$m\Omega$
Quiescent Current	I <sub>O</sub> = 10 mA I <sub>O</sub> = 150 mA	4 18	7 40-		mA <sub>MAX</sub> mA <sub>MAX</sub>
Output Noise Voltage	10 Hz-100 kHz	170			$\mu V_{rms}$
Long Term Stability		30			mV/1000 h
Ripple Rejection	f <sub>O</sub> = 120 Hz	52			dB
Current Limit	,	400	700 150		mA <sub>MAX</sub> mA <sub>MIN</sub>
Dropout Voltage	I <sub>O</sub> = 150 mA	0.32	0.6		V <sub>MAX</sub>
Output Voltage Under Transient Conditions	$-12V \le V_{\text{IN}} \le 40V$ , $R_{\text{L}} = 100\Omega$		8.8 -0.3		V <sub>MAX</sub> V <sub>MiN</sub>

Note 1: Thermal resistance without a heat sink for junction to case temperature is 3°C/W and for case to ambient temperature is 50°C/W.

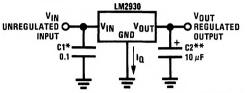
Note 2: All characteristics are measured with a capacitor across the input of 0.1 μF and a capacitor across the output of 10 μF. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques (t<sub>W</sub>≤ 10 ms, duty cycle≤5%). Output voltage changes due to changes in internal temperature must be taken into account separately.

Note 3: Guaranteed and 100% production tested.

Note 4: Guaranteed (but not 100% production tested) over the operating temperature and input current ranges. These limits are not used to calculate outgoing quality levels.

Note 5: To ensure constant junction temperature, low duty cycle pulse testing is used.

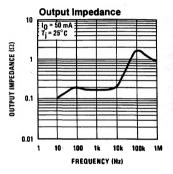
## **Typical Application**

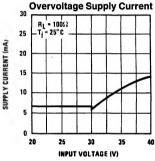


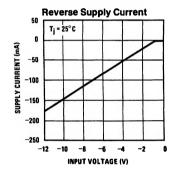
TL/H/5539-5

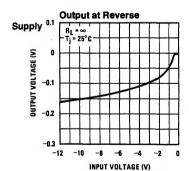
- \*Required if regulator is located far from power supply filter.
- \*\*Cour must be at least 10 μF to maintain stability. May be increased without bound to maintain regulation during transients. Locate as close as possible to the regulator. This capacitor must be rated over the same operating temperature range as the regulator. The equivalent series resistance (ESR) of this capacitor should be less than 1Ω over the expected operating temperature range.

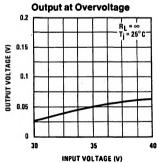
## **Typical Performance Characteristics**

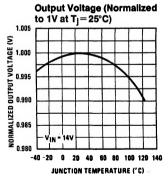












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#### Typical Performance Characteristics (Continued) **Dropout Voltage Dropout Voltage** Low Voltage Behavior 0.6 Tj = 25°C LM2930-5 INPUT-DUTPUT DIFFERENTIAL (V) INPUT-DUTPUT DIFFERENTIAL (V) 0.5 5.0 **OUTPUT VOLTAGE (V)** 0.4 lo = 150 mA 0.4 0.3 0.3 10 = 50 mA 0.2 0.2 0.1 lo = 10 mA 1.0 50 100 200 3.0 4.0 5.0 0 100 150 50 150 2.0 JUNCTION TEMPERATURE (°C) OUTPUT CURRENT (mA) INPUT VOLTAGE (V) **High Voltage Behavior Line Transient Response Load Transient Response** LM2930-5 LOAD CURRENT (mA) OUTPUT VOLTAGE DEVIATION (mV) OUTPUT VOLTAGE DEVIATION (mV) **OUTPUT VOLTAGE (V)** INPUT VOLTAGE CHANGE (V) 3 2 0 10 20 30 40 O 15 30 45 30 45 INPUT VOLTAGE (V) TIME (us) TIME (µs) **Peak Output Current Quiescent Current Quiescent Current** 600 35 22 20 30 500 QUIESCENT CURRENT (mA) IUIESCENT CURRENT (mA) 10 = 150 mA OUTPUT CURRENT (mA) 25 18 400 20 16 300 15 10 = 50 mA 200 10 6 100 3 10 = 0 0 0 15 20 25 0 10 60 90 D INPUT VOLTAGE (V) OUTPUT CURRENT (mA) JUNCTION TEMPERATURE (°C) **Quiescent Current Ripple Rejection** Ripple Rejection 10 = 50 mA VOUT **QUIESCENT CURRENT (mA)** RIPPLE REJECTION (4B) RIPPLE REJECTION (dB) 60 60 50 40 40 30 20 20 20 10 VIN-VOUT 0 50 100 150 1k 10k OUTPUT CURRENT (mA) INPUT VOLTAGE (V) FREQUENCY (Hz) TL/H/5539-2

#### **Definition of Terms**

**Dropout Voltage:** The input-output voltage differential at which the circuit ceases to regulate against further reduction in Input voltage. Measured when the output voltage has dropped 100 mV from the nominal value obtained at 14V input, dropout voltage is dependent upon load current and junction temperature.

**Input Voltage:** The DC voltage applied to the input terminals with respect to ground.

**Input-Output Differential:** The voltage difference between the unregulated input voltage and the regulated output voltage for which the regulator will operate.

Line Regulation: The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation: The change in output voltage for a change in load current at constant chip temperature.

Long Term Stability: Output voltage stability under accelerated life-test conditions after 1000 hours with maximum rated voltage and junction temperature.

Output Noise Voltage: The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Quiescent Current: That part of the positive input current that does not contribute to the positive load current. The regulator ground lead current.

Ripple Rejection: The ratio of the peak-to-peak input ripple voltage to the peak-to-peak output ripple voltage.

Temperature Stability of V<sub>0</sub>: The percentage change in output voltage for a thermal variation from room temperature to either temperature extreme.

#### **Maximum Power** Dissipation (TO-220) 22 INFINITE HEAT SINK 20 18 OWER DISSIPATION (W) 16 14 12 10 8 10°C/W HEAT SINK 6 NO HEAT SINK 2 0 10 20 30 40 50 60 70 80 90 100 AMBIENT TEMPERATURE (°C)

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