National Semiconductor

LM2940/LM2940C 1A Low Dropout Regulator

General Description

The LM2940/LM2940C positive voltage regulator features the ability to source 1A of output current with a dropout voltage of typically 0.5V and a maximum of 1V over the entire temperature range. Furthermore, a quiescent current reduction circuit has been included which reduces the ground current when the differential between the input voltage and the output voltage exceeds approximately 3V. The quiescent current with 1A of output current and an input-output differential of 5V is therefore only 30 mA. Higher quiescent currents only exist when the regulator is in the dropout mode ($V_{IN} - V_{OUT} \leq 3V$).

Designed also for vehicular applications, the LM2940/LM2940C and all regulated circuitry are protected from reverse battery installations or 2-battery jumps. During line transients, such as load dump when the input voltage can momentarily exceed the specified maximum operating voltage, the regulator will automatically shut down to protect both the internal circuits and the load. The LM2940/LM2940C cannot be harmed by temporary mirror-image insertion. Familiar regulator features such as short circuit and thermal overload protection are also provided.

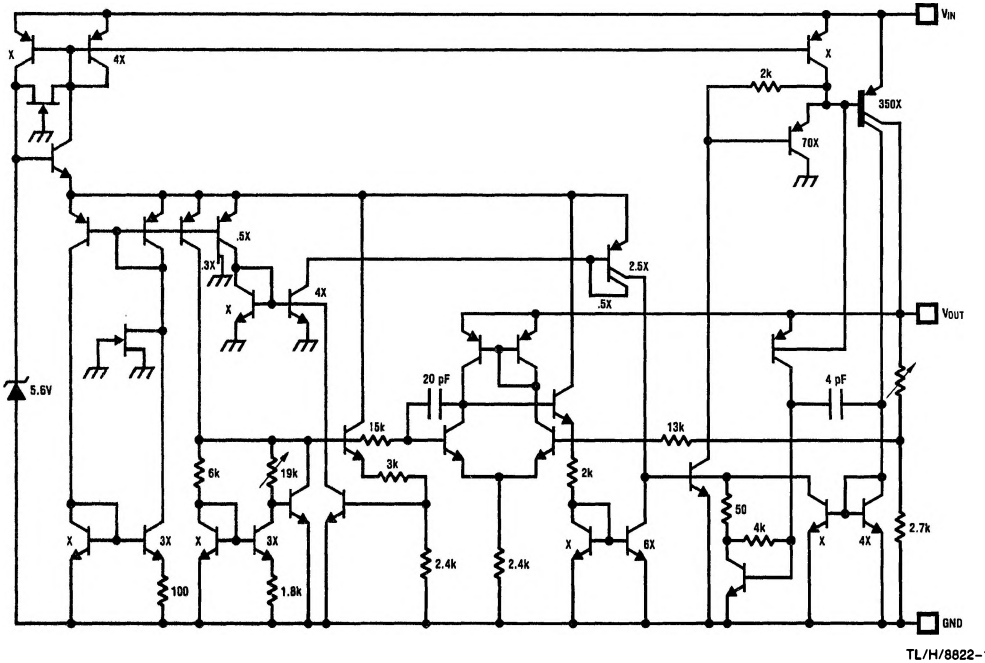
Features

- Dropout voltage typically 0.5V @I_O = 1A
- Output current in excess of 1A
- Output voltage trimmed before assembly
- Reverse battery protection
- Internal short circuit current limit
- Mirror image insertion protection
- P+ Product Enhancement tested

Device	Output Voltages	Package
LM2940CT	5, 12, 15	TO-220
LM2940T	5, 8, 9, 10, 12	TO-220
LM2940K/883*	5, 8, 12, 15	TO-3

*Available only as a military specified device.

Equivalent Schematic Diagram



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Order Number LM2940T-5.0, LM2940T-8.0, LM2940T-9.0, LM2940T-10, LM2940T-12, LM2940CT-5.0, LM2940CT-12, LM2940CT-15, LM2940K-5.0/883, LM2940K-8.0/883, LM2940K-12/883 or LM2940K-15/883
See NS Package Number KO2A or TO3B

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

(Note 2)

Input Voltage (Survival Voltage)

LM2940T, T ≤ 100 ms	60V
LM2940K/883, T ≤ 20 ms	40V
LM2940CT, T ≤ 1 ms	45V
Internal Power Dissipation (Note 3)	Internally Limited
Maximum Junction Temperature	150°C
Storage Temperature Range	$-65°C \le T_J \le +150°C$
Lead Temperature (Soldering, 10 seconds)	
TO-3 (K) Package	300°C
TO-220 (T) Package	260°C
ESD Susceptibility (Note 4)	2 kV

Operating Conditions (Note 1)

Input Voltage	26V
Temperature Range	
LM2940K/883	$-55°C \le T_A \le 125°C$
LM2940T	$-40°C \le T_A \le 125°C$
LM2940CT	$0°C \le T_A \le 125°C$

Electrical Characteristics

$V_{IN} = V_O + 5V$, $I_O = 1A$, $C_O = 22\ \mu F$, unless otherwise specified. **Boldface limits apply over the entire operating temperature range of the indicated device.** All other specifications apply for $T_A = T_J = 25°C$.

Output Voltage (V_O)		5V			8V			
Parameter	**Conditions**	**Typ**	**LM2940T-5.0 LM2940CT-5.0 Limit (Note 5)**	**LM2940K-5.0/883 Limit (Note 6)**	**Typ**	**LM2940T-8.0 Limit (Note 5)**	**LM2940K-8.0/883 Limit (Note 6)**	**Units**
		$6.25V \le V_{IN} \le 26V$			$9.4V \le V_{IN} \le 26V$			
Output Voltage	$5\ mA \le I_O \le 1A$	5.00	4.85/**4.75** 5.15/**5.25**	4.85/**4.75** 5.15/**5.25**	8.00	7.76/**7.60** 8.24/**8.40**	7.76/**7.60** 8.24/**8.40**	V_{MIN} V_{MAX}
Line Regulation	$V_O + 2V \le V_{IN} \le 26V$, $I_O = 5\ mA$	20	50	40/**50**	20	80	50/**80**	mV_{MAX}
Load Regulation	$50\ mA \le I_O \le 1A$ LM2940, LM2940/883 LM2940C	 35 35	 50/**80** 50	 50/**100**	 55	 80/**130**	 80/**130**	 mV_{MAX} mV_{MAX}
Output Impedance	100 mADC and 20 mArms, $f_O = 120\ Hz$	35		1000/**1000**	55		1000/**1000**	mΩ
Quiescent Current	$V_O + 2V \le V_{IN} \le 26V$, $I_O = 5\ mA$ LM2940, LM2940/883 LM2940C	 10 10	 15/**20** 15	 15/**20**	 10	 15/**20**	 15/**20**	 mA_{MAX} mA_{MAX}
	$V_{IN} = V_O + 5V$, $I_O = 1A$	30	45/**60**	50/**60**	30	45/**60**	50/**60**	mA_{MAX}
Output Noise Voltage	10 Hz – 100 kHz, $I_O = 5\ mA$	150		700/**700**	240		1000/**1000**	μV_{rms}
Ripple Rejection	$f_O = 120\ Hz$, $1\ V_{rms}$, $I_O = 100\ mA$ LM2940 LM2940C	 72 72	 60/**54** 60		 66	 54/**48**		 dB_{MIN} dB_{MIN}
	$f_O = 1\ kHz$, $1\ V_{rms}$, $I_O = 5\ mA$			60/**50**			54/**48**	dB_{MIN}
Long Term Stability		20			32			mV/ 1000 Hr
Dropout Voltage	$I_O = 1A$	0.5	0.8/**1.0**	0.7/**1.0**	0.5	0.8/**1.0**	0.7/**1.0**	V_{MAX}
	$I_O = 100\ mA$	110	150/**200**	150/**200**	110	150/**200**	150/**200**	mV_{MAX}

Electrical Characteristics (Continued)

$V_{IN} = V_O + 5V$, $I_O = 1A$, $C_O = 22\ \mu F$, unless otherwise specified. **Boldface limits apply over the entire operating temperature range of the indicated device.** All other specifications apply for $T_A = T_J = 25°C$.

Output Voltage (V_O)		5V			8V			Units
Parameter	Conditions	Typ	LM2940T-5.0 LM2940CT-5.0 Limit (Note 5)	LM2940K-5.0/883 Limit (Note 6)	Typ	LM2940T-8.0 Limit (Note 5)	LM2940K-8.0/883 Limit (Note 6)	
		6.25V ≤ V_{IN} ≤ 26V			9.4V ≤ V_{IN} ≤ 26V			
Short Circuit Current	(Note 7)	1.9	1.6	1.5/**1.3**	1.9	1.6	1.6/**1.3**	A_{MIN}
Maximum Line Transient	$R_O = 100\Omega$							
	LM2940, T ≤ 100 ms	75	60/**60**		75	60/**60**		V_{MIN}
	LM2940/883, T ≤ 20 ms			40/**40**			40/**40**	V_{MIN}
	LM2940C, T ≤ 1 ms	55	45					V_{MIN}
Reverse Polarity DC Input Voltage	$R_O = 100\Omega$							
	LM2940, LM2940/883	−30	−15/−**15**	−15/−**15**	−30	−15/−**15**	−15/−**15**	V_{MIN}
	LM2940C	−30	−15					V_{MIN}
Reverse Polarity Transient Input Voltage	$R_O = 100\Omega$							
	LM2940, T ≤ 100 ms	−75	−50/−**50**		−75	−50/−**50**		V_{MIN}
	LM2940/883, T ≤ 20 ms			−45/−**45**			−45/−**45**	V_{MIN}
	LM2940C, T ≤ 1 ms	−55	−45/−**45**					V_{MIN}

Electrical Characteristics (Continued)

$V_{IN} = V_O + 5V$, $I_O = 1A$, $C_O = 22\ \mu F$, unless otherwise specified. **Boldface limits apply over the entire operating temperature range of the indicated device.** All other specifications apply for $T_A = T_J = 25°C$.

Output Voltage (V_O)		9V		10V		
Parameter	Conditions	Typ	LM2940T-9.0 Limit (Note 5)	Typ	LM2940T-10 Limit (Note 5)	Units
		$10.5V \le V_{IN} \le 26V$		$11.5V \le V_{IN} \le 26V$		
Output Voltage	$5\ mA \le I_O \le 1A$	9.00	8.73/**8.55** 9.27/**9.45**	10.00	9.70/**9.50** 10.30/**10.50**	V_{MIN} V_{MAX}
Line Regulation	$V_O + 2V \le V_{IN} \le 26V$, $I_O = 5\ mA$	20	90	20	100	mV_{MAX}
Load Regulation	$50\ mA \le I_O \le 1A$	60	90/**150**	65	100/**165**	mV_{MAX}
Output Impedance	100 mADC and 20 mArms, $f_O = 120\ Hz$	60		65		mΩ
Quiescent Current	$V_O + 2V \le V_{IN} < 26V$, $I_O = 5\ mA$	10	15/**20**	10	15/**20**	mA_{MAX}
	$V_{IN} = V_O + 5V$, $I_O = 1A$	30	45/**60**	30	45/**60**	mA_{MAX}
Output Noise Voltage	10 Hz – 100 kHz, $I_O = 5\ mA$	270		300		μV_{rms}
Ripple Rejection	$f_O = 120\ Hz$, $1\ V_{rms}$, $I_O = 100\ mA$	64	52/**46**	63	51/**45**	dB_{MIN}
Long Term Stability		34		36		mV/ 1000 Hr
Dropout Voltage	$I_O = 1A$	0.5	0.8/**1.0**	0.5	0.8/**1.0**	V_{MAX}
	$I_O = 100\ mA$	110	150/**200**	110	150/**200**	mV_{MAX}
Short Circuit Current	(Note 7)	1.9	1.6	1.9	1.6	A_{MIN}
Maximum Line Transient	$R_O = 100\Omega$ $T \le 100\ ms$	75	60/**60**	75	60/**60**	V_{MIN}
Reverse Polarity DC Input Voltage	$R_O = 100\Omega$	−30	−15/−**15**	−30	−15/−**15**	V_{MIN}
Reverse Polarity Transient Input Voltage	$R_O = 100\Omega$ $T \le 100\ ms$	−75	−50/−**50**	−75	−50/−**50**	V_{MIN}

Electrical Characteristics (Continued)

$V_{IN} = V_O + 5V$, $I_O = 1A$, $C_O = 22\ \mu F$, unless otherwise specified. **Boldface limits apply over the entire operating temperature range of the indicated device.** All other specifications apply for $T_A = T_J = 25°C$.

Output Voltage (V_O)		12V			15V			
Parameter	Conditions	Typ	LM2940T-12 LM2940CT-12.0 Limit (Note 5)	LM2940K-12/883 Limit (Note 6)	Typ	LM2940CT-15 Limit (Note 5)	LM2940K-15/883 Limit (Note 6)	Units
			13.6V ≤ V_{IN} ≤ 26V			16.75V ≤ V_{IN} ≤ 26V		
Output Voltage	5 mA ≤ I_O ≤ 1A	12.00	11.64/**11.40** 12.36/**12.60**	11.64/**11.40** 12.36/**12.60**	15.00	14.55/**14.25** 15.45/**15.75**	14.55/**14.25** 15.45/**15.75**	V_{MIN} V_{MAX}
Line Regulation	V_O + 2V ≤ V_{IN} ≤ 26V, I_O = 5 mA	20	120	75/**120**	20	150	95/**150**	mV_{MAX}
Load Regulation	50 mA ≤ I_O ≤ 1A LM2940, LM2940/883 LM2940C	 55 55	 120/**200** 120	 120/**190**		 70	 150/**240**	 mV_{MAX} mV_{MAX}
Output Impedance	100 mADC and 20 mArms, f_O = 120 Hz	80		1000/**1000**	100		1000/**1000**	mΩ
Quiescent Current	V_O + 2V ≤ V_{IN} ≤ 26V, I_O = 5 mA LM2940, LM2940/883 LM2940C	 10 10	 15/**20** 15	 15/**20**	 10	 15	 15/**20**	 mA_{MAX} mA_{MAX}
	V_{IN} = V_O + 5V, I_O = 1A	30	45/**60**	50/**60**	30	45/**60**	50/**60**	mA_{MAX}
Output Noise Voltage	10 Hz − 100 kHz, I_O = 5 mA	360		1000/**1000**	450		1000/**1000**	μV_{rms}
Ripple Rejection	f_O = 120 Hz, 1 V_{rms}, I_O = 100 mA LM2940 LM2940C	 66 66	 54/**48** 54	 	 64	 52	 	 dB_{MIN} dB_{MIN}
	f_O = 1 kHz, 1 V_{rms}, I_O = 5 mA			52/**46**			48/**42**	dB_{MIN}
Long Term Stability		48			60			mV/ 1000 Hr
Dropout Voltage	I_O = 1A	0.5	0.8/**1.0**	0.7/**1.0**	0.5	0.8/**1.0**	0.7/**1.0**	V_{MAX}
	I_O = 100 mA	110	150/**200**	150/**200**	110	150/**200**	150/**200**	mV_{MAX}
Short Circuit Current	(Note 7)	1.9	1.6	1.6/**1.3**	1.9	1.6	1.6/**1.3**	A_{MIN}
Maximum Line Transient	R_O = 100Ω LM2940, T ≤ 100 ms LM2940/883, T ≤ 20 ms LM2940C, T ≤ 1 ms	 75 55	 60/**60** 45	 40/**40**	 55	 45	 40/**40**	 V_{MIN} V_{MIN} V_{MIN}
Reverse Polarity DC Input Voltage	R_O = 100Ω LM2940, LM2940/883 LM2940C	 −30 −30	 −15/−**15** −15	 −15/−**15**	 −30	 −15	 −15/−**15**	 V_{MIN} V_{MIN}
Reverse Polarity Transient Input Voltage	R_O = 100Ω LM2940, T ≤ 100 ms LM2940/883, T ≤ 20 ms LM2940C, T ≤ 1 ms	 −75 −55	 −50/−**50** −45/−**45**	 −45/−**45**	 −55	 −45/−**45**	 −45/−**45**	 V_{MIN} V_{MIN} V_{MIN}

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specifications might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note 2: Military specifications complied with RETS/SMD at the time of printing. For current specifications refer to RETS LM2940K-5.0, LM2940K-8.0, LM2940K-12, and LM2940K-15. SMD numbers are 5962-8958701YA(5V), 5962-9083301YA(8V), 5962-9088401YA(12V), and 5962-9088501YA(15V).

Note 3: The maximum power dissipation is a function of the maximum junction temperature, $T_J = 150°C$, the junction-to-ambient thermal resistance, θ_{JA}, and the ambient temperature, T_A. The maximum allowable power dissipation at any ambient temperature is $P_{DMAX} = (150 - T_A)/\theta_{JA}$. If this dissipation is exceeded, the die temperature will rise above 150°C and the LM2940 will go into thermal shutdown. For the LM2940T and LM2940CT, the junction-to-ambient thermal resistance (θ_{JA}) is 53°C/W. When using a heatsink, θ_{JA} is the sum of the 3°C/W junction-to-case thermal resistance (θ_{JC}) of the LM2940T or LM2940CT and the case-to-ambient thermal resistance of the heatsink. For the LM2940K, θ_{JA} is 39°C/W and θ_{JC} is 4°C/W.

Note 4: ESD rating is based on the human body model, 100 pF discharged through 1.5 kΩ.

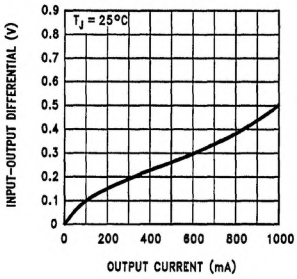
Note 5: All limits are guaranteed at $T_A = T_J = 25°C$ only (standard typeface) or over the entire operating temperature range of the indicated device (boldface type). All limits at $T_A = T_J = 25°C$ are 100% production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control methods.

Note 6: All limits are guaranteed at $T_A = T_J = 25°C$ only (standard typeface) or over the entire operating temperature range of the indicated device (boldface type). All limits are 100% production tested and are used to calculate Outgoing Quality Levels.

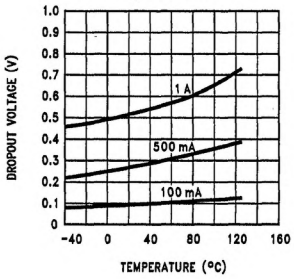
Note 7: Output current will decrease with increasing temperature but will not drop below 1A at the maximum specified temperature.

Typical Performance Characteristics

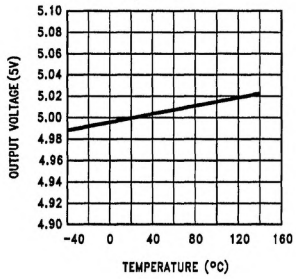
Dropout Voltage



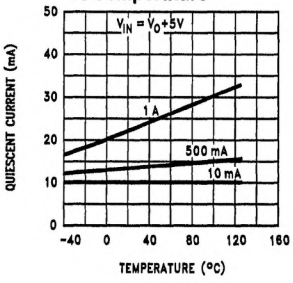
Dropout Voltage vs Temperature



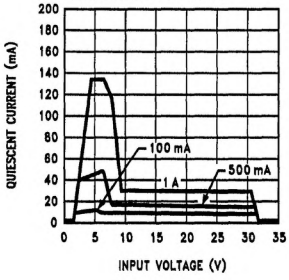
Output Voltage vs Temperature



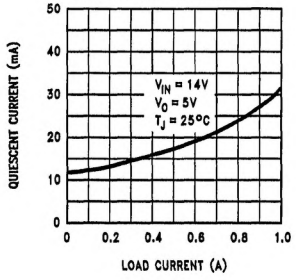
Quiescent Current vs Temperature



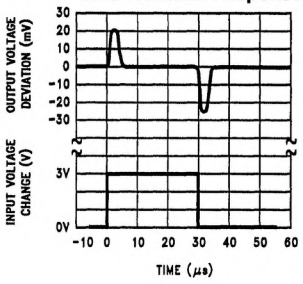
Quiescent Current



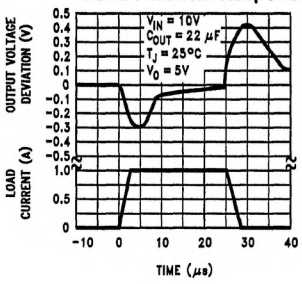
Quiescent Current



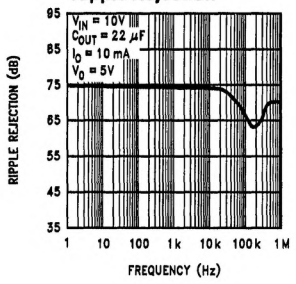
Line Transient Response



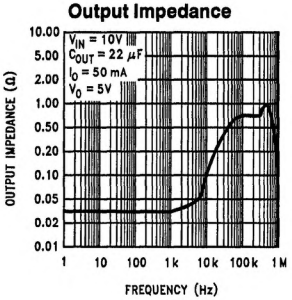
Load Transient Response



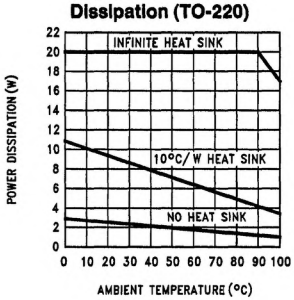
Ripple Rejection



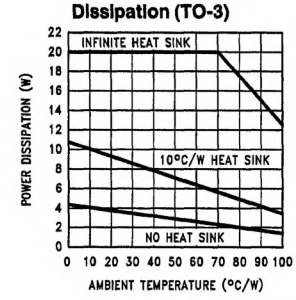
Output Impedance



Maximum Power Dissipation (TO-220)



Maximum Power Dissipation (TO-3)



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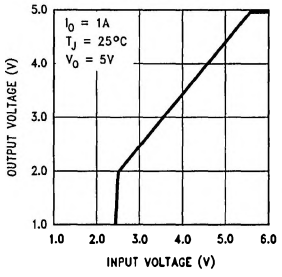
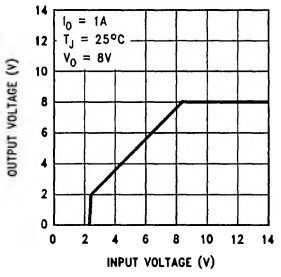
Typical Performance Characteristics (Continued)

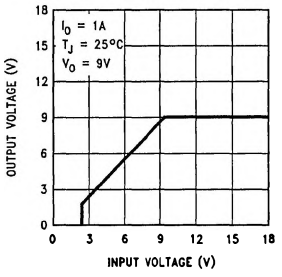
Low Voltage Behavior



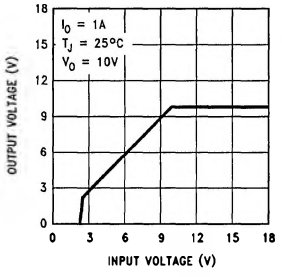
Low Voltage Behavior



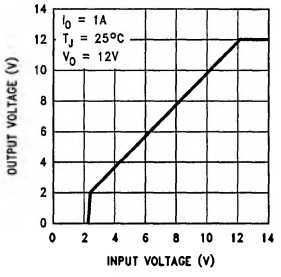
Low Voltage Behavior



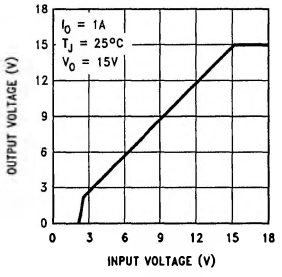
Low Voltage Behavior



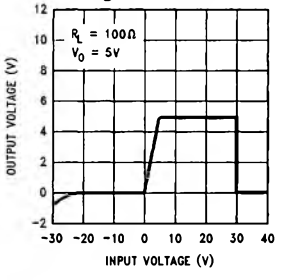
Low Voltage Behavior



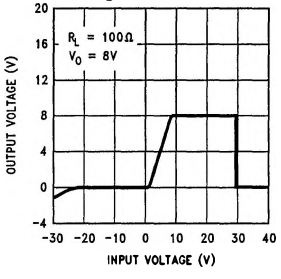
Low Voltage Behavior



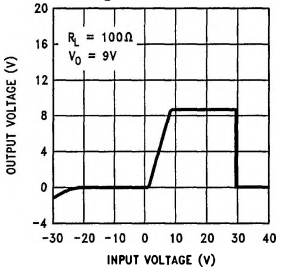
Output at Voltage Extremes



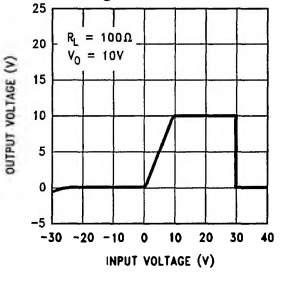
Output at Voltage Extremes



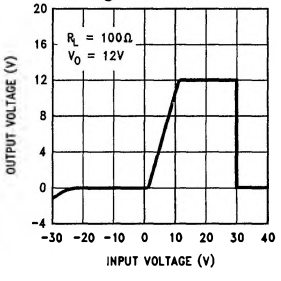
Output at Voltage Extremes



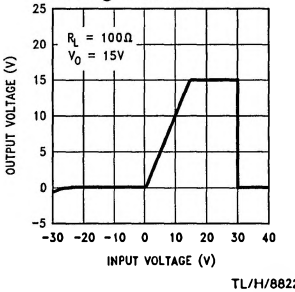
Output at Voltage Extremes



Output at Voltage Extremes



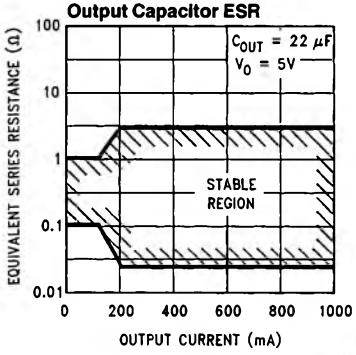
Output at Voltage Extremes



TL/H/8822–5

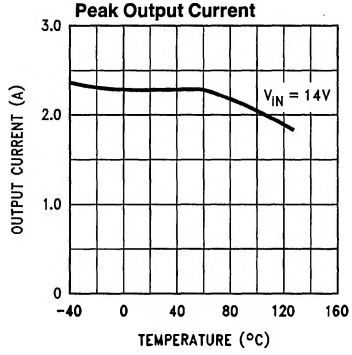
Typical Performance Characteristics (Continued)

Output Capacitor ESR



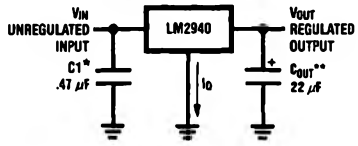
TL/H/8822-6

Peak Output Current



TL/H/8822-8

Typical Application



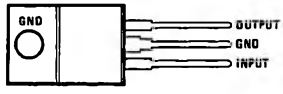
TL/H/8822-3

*Required if regulator is located far from power supply filter.

**C_{OUT} must be at least 22 μF to maintain stability. May be increased without bound to maintain regulation during transients. Locate as close as possible to the regulator. This capacitor must be rated over the same operating temperature range as the regulator and the ESR is critical; see curve.

Connection Diagram

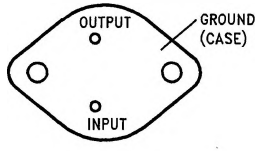
(TO-220) Plastic Package



TL/H/8822-2

Front View
Order Number LM2940T-5.0, LM2940T-8.0, LM2940T-9.0, LM2940T-10, LM2940T-12, LM2940CT-5.0, LM2940CT-12 or LM2940CT-15
See NS Package Number T03B

TO-3 Metal Can Package (K)



TL/H/8822-7

Bottom View
Order Number LM2940K-5.0/883, LM2940K-8.0/883, LM2940K-12/883, LM2940K-15/883
See NS Package Number K02A