



# Voltage Comparators/Buffers

## LM710C voltage comparator

### general description

The LM710C is a high-speed voltage comparator intended for use as an accurate, low-level digital level sensor or as a replacement for operational amplifiers in comparator applications where speed is of prime importance. The circuit has a differential input and a single-ended output, with saturated output levels compatible with practically all types of integrated logic.

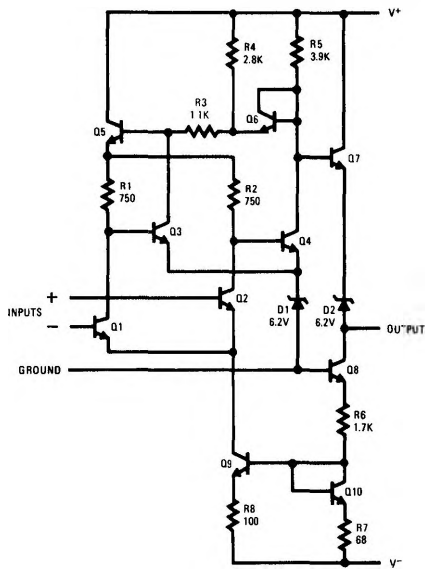
The device is built on a single silicon chip which insures low offset and thermal drift. The use of a minimum number of stages along with minority-carrier lifetime control (gold doping) makes the circuit much faster than operational amplifiers in saturating comparator applications. In fact, the low stray and wiring capacitances that can be realized

with monolithic construction make the device difficult to duplicate with discrete components operating at equivalent power levels.

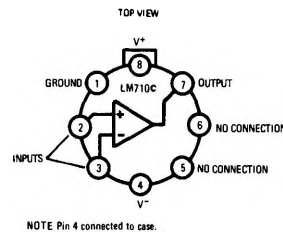
The LM710C is useful as a pulse height discriminator, a voltage comparator in high-speed A/D converters or a go, no-go detector in automatic test equipment. It also has applications in digital systems as an adjustable-threshold line receiver or an interface between logic types. In addition, the low cost of the unit suggests it for applications replacing relatively simple discrete component circuitry.

The LM710C is the commercial/industrial version of the LM710A. It is identical to the LM710A except that operation is specified over a 0°C to 70°C temperature range.

### schematic and connection diagrams

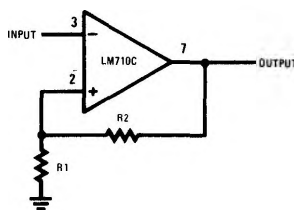


Metal Can

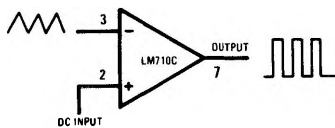


### typical applications

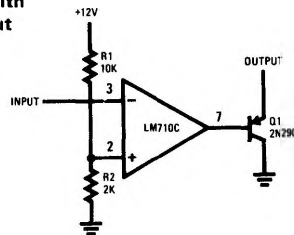
Schmidt Trigger



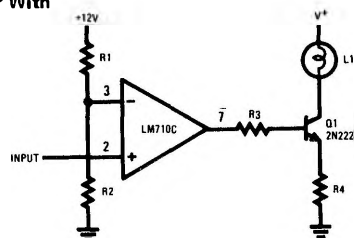
Pulse Width Modulator



Line Receiver With Increased Output Sink Current



Level Detector With Lamp Driver



**absolute maximum ratings**

Positive Supply Voltage	14.0V
Negative Supply Voltage	-7.0V
Differential Input Voltage	±5.0V
Input Voltage	±7.0V
Power Dissipation (Note 1)	300 mW
Output Short Circuit Duration	10 sec
Operating Temperature Range	0°C to 70°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering, 60 sec)	300°C

**electrical characteristics** (Note 2)

PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Offset Voltage	$T_A = 25^\circ\text{C}$ , $R_S < 200\Omega$ $V_{OUT} = 1.4\text{V}$		1.6	5.0	mV
Input Offset Current	$T_A = 25^\circ\text{C}$ , $V_{OUT} = 1.4\text{V}$		1.8	5.0	$\mu\text{A}$
Input Bias Current	$T_A = 25^\circ\text{C}$		16	25	$\mu\text{A}$
Voltage Gain	$T_A = 25^\circ\text{C}$	1000	1500		
Output Resistance	$T_A = 25^\circ\text{C}$		200		$\Omega$
Output Sink Current	$T_A = 25^\circ\text{C}$ , $\Delta V_{IN} \geq 5\text{ mV}$ $V_{OUT} = 0$	1.7	2.5		mA
Response Time (Note 3)			40		ns
Input Offset Voltage	$R_S \leq 200\Omega$			6.5	mV
Average Temperature Coefficient of Input Offset Voltage	$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $R_S \leq 50\Omega$		5.0	20	$\mu\text{V}/^\circ\text{C}$
Input Offset Current				7.5	$\mu\text{A}$
Average Temperature Coefficient of Input Offset Current	$25^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $0^\circ\text{C} \leq T_A \leq 25^\circ\text{C}$		15 24	50 100	$\text{nA}/^\circ\text{C}$ $\text{nA}/^\circ\text{C}$
Input Bias Current	$T_A = 0^\circ\text{C}$		25	40	$\mu\text{A}$
Input Voltage Range	$V^- = -7.0\text{V}$	±5.0			V
Differential Input Voltage Range		±5.0			V
Voltage Gain		800			
Positive Output Level	$\Delta V_{IN} \geq 5\text{ mV}$ , $0 \leq I_{OUT} \leq 5\text{ mA}$	2.5	3.2	4.0	V
Negative Output Level	$\Delta V_{IN} \geq 5\text{ mV}$	-1.0	-0.5	0	V
Output Sink Current	$\Delta V_{IN} \geq 5\text{ mV}$ , $V_{OUT} = 0.2\text{V}$	1.6			mA
Positive Supply Current	$-5\text{V} \leq \Delta V_{IN} \leq 5\text{V}$ , $I_{OUT} \leq 0$			11	mA
Negative Supply Current			4.6	7.0	mA
Power Consumption	$T_A = 70^\circ\text{C}$ , $I_{OUT} \leq 0$ $-5\text{V} \leq \Delta V_{IN} \leq 5\text{V}$			170	mW

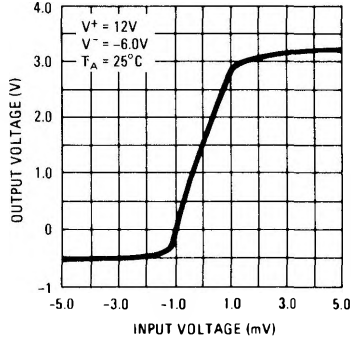
Note 1: Ratings apply for ambient temperatures to 70°C.

Note 2: These specifications apply for  $V^+ = 12.0\text{V}$ ,  $V^- = 6.0\text{V}$ ,  $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$  and for a logic threshold voltage of 1.5V at 0°C, 1.4V at 25°C and 1.2V at 70°C unless otherwise specified.

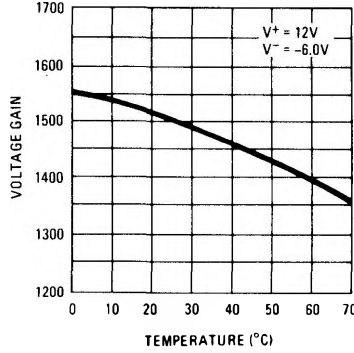
Note 3: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive.

typical performance characteristics

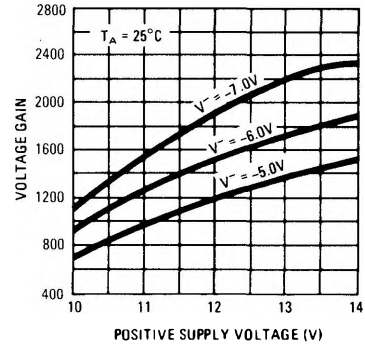
Transfer Function



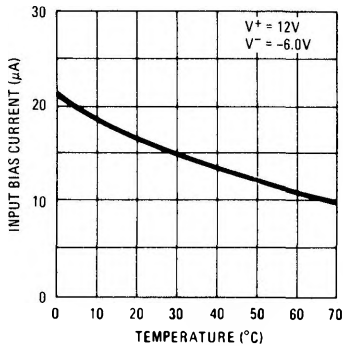
Voltage Gain



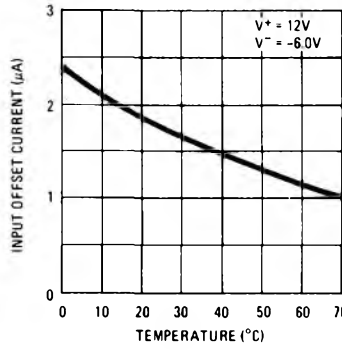
Voltage Gain



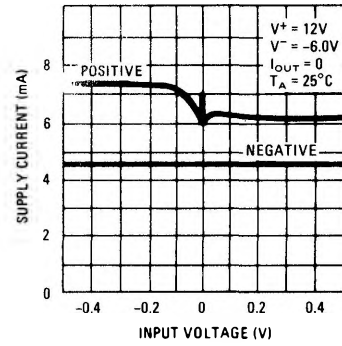
Input Bias Current



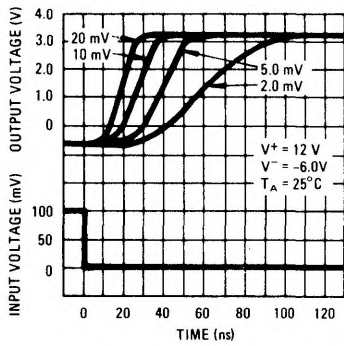
Input Offset Current



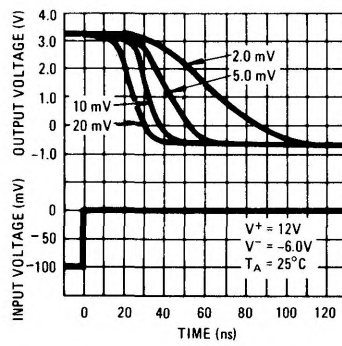
Supply Current



Response Time For Various Input Overdrives



Response Time For Various Input Overdrives



Common Mode Pulse Response

