

LM78XX Series Voltage Regulators

General Description

The LM78XX series of three terminal regulators is available with several fixed output voltages making them useful in a wide range of applications. One of these is local on card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow these regulators to be used in logic systems, instrumentation, HiFi, and other solid state electronic equipment. Although designed primarily as fixed voltage regulators these devices can be used with external components to obtain adjustable voltages and currents.

The LM78XX series is available in an aluminum TO-3 package which will allow over 1.0A load current if adequate heat sinking is provided. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistor is provided to limit internal power dissipation. If internal power dissipation becomes too high for the heat sinking provided, the thermal shutdown circuit takes over preventing the IC from overheating.

Considerable effort was expended to make the LM78XX series of regulators easy to use and minimize the number

of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor or the power supply.

For output voltage other than 5V, 12V and 15V the LM117 series provides an output voltage range from 1.2V to 57V.

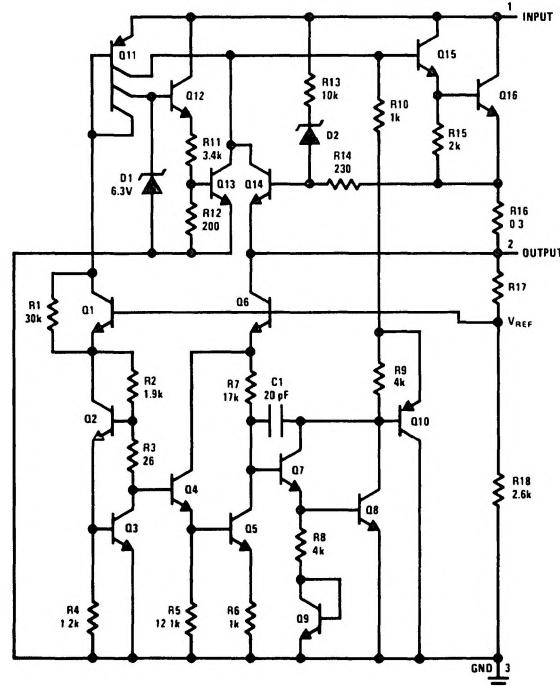
Features

- Output current in excess of 1A
- Internal thermal overload protection
- No external components required
- Output transistor safe area protection
- Internal short circuit current limit
- Available in the aluminum TO-3 package

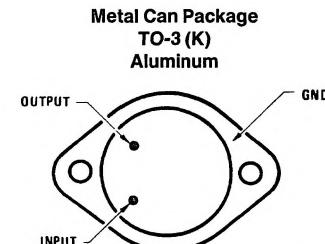
Voltage Range

LM7805C	5V
LM7812C	12V
LM7815C	15V

Schematic and Connection Diagrams

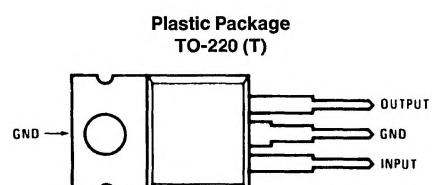


TL/H/7746-1



Bottom View
Order Number LM7805CK,
LM7812CK or LM7815CK
See NS Package Number KC02A

TL/H/7746-2



Top View
Order Number LM7805CT,
LM7812CT or LM7815CT
See NS Package Number T03B

TL/H/7746-3

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Input Voltage ($V_O = 5V, 12V$ and $15V$)	35V	Maximum Junction Temperature (K Package)	150°C
Internal Power Dissipation (Note 1)	Internally Limited	(T Package)	150°C
Operating Temperature Range (T_A)	$0^\circ C$ to $+70^\circ C$	Storage Temperature Range	$-65^\circ C$ to $+150^\circ C$
		Lead Temperature (Soldering, 10 sec.)	
		TO-3 Package K	300°C
		TO-220 Package T	230°C

Electrical Characteristics LM78XXC (Note 2) $0^\circ C \leq T_j \leq 125^\circ C$ unless otherwise noted.

Output Voltage			5V			12V			15V			Units
Input Voltage (unless otherwise noted)			10V			19V			23V			
Symbol	Parameter	Conditions	Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V_O	Output Voltage	$T_j = 25^\circ C, 5 mA \leq I_O \leq 1A$	4.8	5	5.2	11.5	12	12.5	14.4	15	15.6	V
		$P_D \leq 15W, 5 mA \leq I_O \leq 1A$ $V_{MIN} \leq V_{IN} \leq V_{MAX}$	4.75	5.25	(7.5 $\leq V_{IN} \leq 20$)	11.4 (14.5 $\leq V_{IN} \leq 27$)	12.6	14.25 (17.5 $\leq V_{IN} \leq 30$)	15.75	15.75	15.75	V
ΔV_O	Line Regulation	$I_O = 500 mA$ $T_j = 25^\circ C$ ΔV_{IN}	3	50	(7 $\leq V_{IN} \leq 25$)	4	120	(14.5 $\leq V_{IN} \leq 30$)	4	150	(17.5 $\leq V_{IN} \leq 30$)	mV V
		$0^\circ C \leq T_j \leq +125^\circ C$ ΔV_{IN}	50	(8 $\leq V_{IN} \leq 20$)	(15 $\leq V_{IN} \leq 27$)	120	(18.5 $\leq V_{IN} \leq 30$)	150	150	150	150	mV V
		$I_O \leq 1A$ $T_j = 25^\circ C$ ΔV_{IN}	50	(7.5 $\leq V_{IN} \leq 20$)	(14.6 $\leq V_{IN} \leq 27$)	120	(17.7 $\leq V_{IN} \leq 30$)	150	150	150	150	mV V
		$0^\circ C \leq T_j \leq +125^\circ C$ ΔV_{IN}	25	(8 $\leq V_{IN} \leq 12$)	(16 $\leq V_{IN} \leq 22$)	60	(20 $\leq V_{IN} \leq 26$)	75	75	75	75	mV V
ΔV_O	Load Regulation	$T_j = 25^\circ C$ $5 mA \leq I_O \leq 1.5A$ $250 mA \leq I_O \leq 750 mA$	10	50	25	12	120	60	12	150	75	mV mV
		$5 mA \leq I_O \leq 1A, 0^\circ C \leq T_j \leq +125^\circ C$	50			120			150			mV
I_Q	Quiescent Current	$I_O \leq 1A$ $T_j = 25^\circ C$ $0^\circ C \leq T_j \leq +125^\circ C$	8		8.5	8		8.5	8		8.5	mA mA
ΔI_Q	Quiescent Current Change	$5 mA \leq I_O \leq 1A$	0.5		0.5	0.5		0.5	0.5		0.5	mA
		$T_j = 25^\circ C, I_O \leq 1A$ $V_{MIN} \leq V_{IN} \leq V_{MAX}$	1.0		1.0	1.0		1.0	1.0		1.0	mA V
		$I_O \leq 500 mA, 0^\circ C \leq T_j \leq +125^\circ C$ $V_{MIN} \leq V_{IN} \leq V_{MAX}$	1.0		1.0	1.0		1.0	1.0		1.0	mA V
V_N	Output Noise Voltage	$T_A = 25^\circ C, 10 Hz \leq f \leq 100 kHz$	40		75	75		90	90		90	µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	$f = 120 Hz$ $\begin{cases} I_O \leq 1A, T_j = 25^\circ C \text{ or} \\ I_O \leq 500 mA \\ 0^\circ C \leq T_j \leq +125^\circ C \\ V_{MIN} \leq V_{IN} \leq V_{MAX} \end{cases}$	62	80	62	55	72	55	70	54	70	dB dB
		(8 $\leq V_{IN} \leq 18$)	(15 $\leq V_{IN} \leq 25$)	(18.5 $\leq V_{IN} \leq 28.5$)	(18.5 $\leq V_{IN} \leq 28.5$)	54	54	54	54	54	54	V
R_O	Dropout Voltage	$T_j = 25^\circ C, I_{OUT} = 1A$	2.0		2.0	2.0		2.0	2.0		2.0	V
	Output Resistance	$f = 1 kHz$	8		18	8		19	19		19	mΩ
	Short-Circuit Current	$T_j = 25^\circ C$	2.1		1.5	2.1		1.5	1.5		1.5	A
	Peak Output Current	$T_j = 25^\circ C$	2.4		2.4	2.4		2.4	2.4		2.4	A
	Average TC of V_{OUT}	$0^\circ C \leq T_j \leq +125^\circ C, I_O = 5 mA$	0.6		1.5	0.6		1.5	1.5		1.8	mV/°C
V_{IN}	Input Voltage Required to Maintain Line Regulation	$T_j = 25^\circ C, I_O \leq 1A$	7.5		14.6	7.5		14.6	14.6		17.7	V

Note 1: Thermal resistance of the TO-3 package (K, KC) is typically $4^\circ C/W$ junction to case and $35^\circ C/W$ case to ambient. Thermal resistance of the TO-220 package (T) is typically $4^\circ C/W$ junction to case and $50^\circ C/W$ case to ambient.

Note 2: All characteristics are measured with capacitor across the input of $0.22 \mu F$, and a capacitor across the output of $0.1 \mu F$. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_w \leq 10 ms$, duty cycle $\leq 5\%$). Output voltage changes due to changes in internal temperature must be taken into account separately.

Typical Performance Characteristics

