



LM833 Dual Audio Operational Amplifier

General Description

The LM833 is a dual general purpose operational amplifier designed with particular emphasis on performance in audio systems.

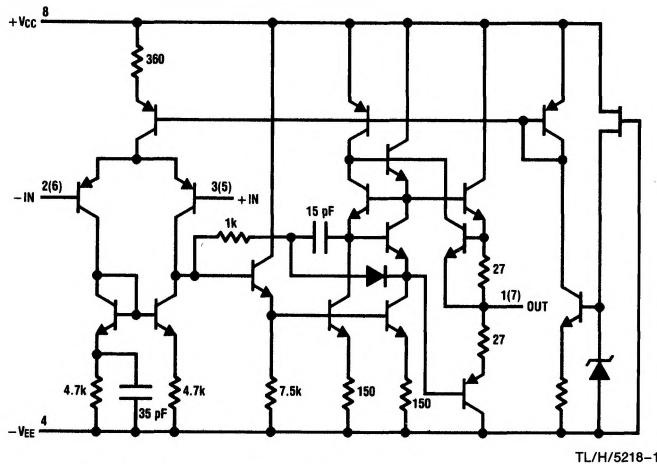
This dual amplifier IC utilizes new circuit and processing techniques to deliver low noise, high speed and wide bandwidth without increasing external components or decreasing stability. The LM833 is internally compensated for all closed loop gains and is therefore optimized for all preamp and high level stages in PCM and HiFi systems.

The LM833 is pin-for-pin compatible with industry standard dual operational amplifiers.

Features

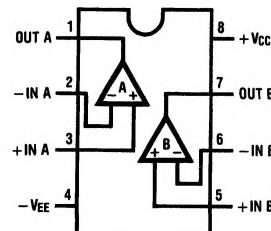
- Wide dynamic range > 140 dB
- Low input noise voltage 4.5 nV/Hz
- High slew rate 7 V/ μ s (typ)
- High gain bandwidth product 5 V/ μ s (min)
- Wide power bandwidth 15 MHz (typ)
- Low distortion 10 MHz (min)
- Low offset voltage 120 kHz
- Large phase margin 0.002%
- Low offset voltage 0.3 mV
- Large phase margin 60°

Schematic Diagram (1/2 LM833)



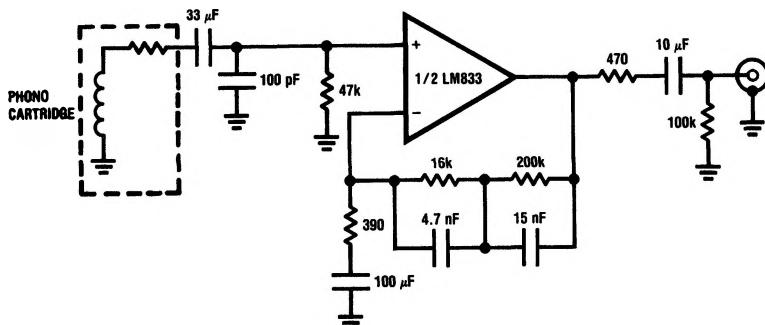
TL/H/5218-1

Connection Diagram



TL/H/5218-2
Order Number LM833M or LM833N
See NS Package Number
M08A or N08E

Typical Application RIAA Preamp



TL/H/5218-3

$A_v = 35 \text{ dB}$ $f = 1 \text{ kHz}$
 $E_n = 0.33 \mu\text{V}$ A Weighted
 $S/N = 90 \text{ dB}$ A Weighted, $V_{IN} = 10 \text{ mV}$
@ $f = 1 \text{ kHz}$

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	V _{CC} -V _{EE}	36V
Differential Input Voltage (Note 1)	V _{ID}	±30V
Input Voltage Range (Note 1)	V _{IC}	±15V
Power Dissipation (Note 2)	P _D	500 mW
Operating Temperature Range	T _{OPR}	-40 ~ 85°C
Storage Temperature Range	T _{STG}	-60 ~ 150°C

Soldering Information

Dual-In-Line Package

Soldering (10 seconds)

260°C

Small Outline Package

Vapor Phase (60 seconds)

215°C

Infrared (15 seconds)

220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

ESD tolerance (Note 3)

1600V

DC Electrical Characteristics ($T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V _{OS}	Input Offset Voltage	R _S = 10Ω		0.3	5	mV
I _{OS}	Input Offset Current			10	200	nA
I _B	Input Bias Current			500	1000	nA
A _V	Voltage Gain	R _L = 2 kΩ, V _O = ±10V	90	110		dB
V _{OM}	Output Voltage Swing	R _L = 10 kΩ R _L = 2 kΩ	±12 ±10	±13.5 ±13.4		V V
V _{CM}	Input Common-Mode Range		±12	±14.0		V
CMRR	Common-Mode Rejection Ratio	V _{IN} = ±12V	80	100		dB
PSRR	Power Supply Rejection Ratio	V _S = 15~5V, -15~-5V	80	100		dB
I _Q	Supply Current	V _O = 0V, Both Amps		5	8	mA

AC Electrical Characteristics ($T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$, $R_L = 2\text{ k}\Omega$)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
SR	Slew Rate	R _L = 2 kΩ	5	7		V/μs
GBW	Gain Bandwidth Product	f = 100 kHz	10	15		MHz

Design Electrical Characteristics ($T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$)

The following parameters are not tested or guaranteed.

Symbol	Parameter	Conditions	Typ	Units
ΔV _{OS} /ΔT	Average Temperature Coefficient of Input Offset Voltage		2	μV/°C
THD	Distortion	R _L = 2 kΩ, f = 20~20 kHz V _{OUT} = 3 Vrms, A _V = 1	0.002	%
e _n	Input Referred Noise Voltage	R _S = 100Ω, f = 1 kHz	4.5	nV/√Hz
i _n	Input Referred Noise Current	f = 1 kHz	0.7	pA/√Hz
PBW	Power Bandwidth	V _O = 27 V _{pp} , R _L = 2 kΩ, THD ≤ 1%	120	kHz
f _U	Unity Gain Frequency	Open Loop	9	MHz
φ _M	Phase Margin	Open Loop	60	deg
	Input Referred Cross Talk	f = 20~20 kHz	-120	dB

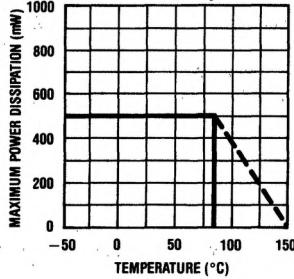
Note 1: If supply voltage is less than ±15V, it is equal to supply voltage.

Note 2: This is the permissible value at $T_A \leq 85^\circ\text{C}$.

Note 3: Human body model, 1.5 kΩ in series with 100 pF.

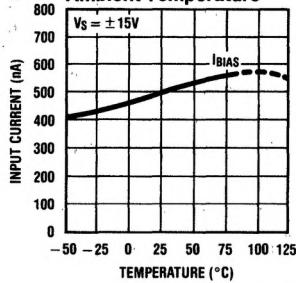
Typical Performance Characteristics

Maximum Power Dissipation vs Ambient Temperature



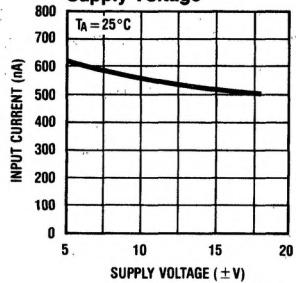
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Input Bias Current vs Ambient Temperature



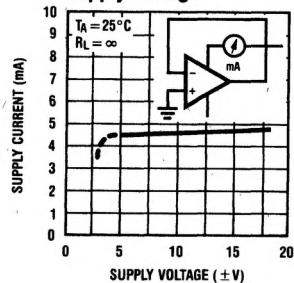
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Input Bias Current vs Supply Voltage



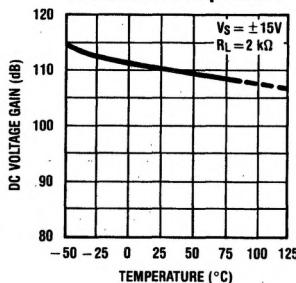
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Supply Current vs Supply Voltage



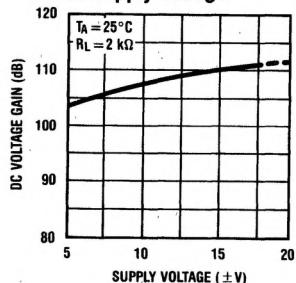
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DC Voltage Gain vs Ambient Temperature



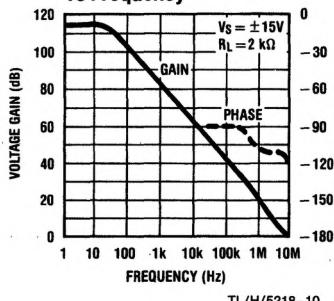
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DC Voltage Gain vs Supply Voltage



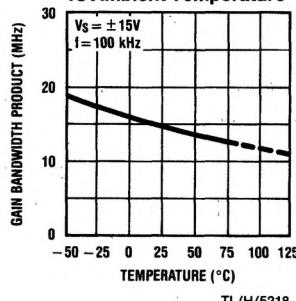
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Voltage Gain & Phase vs Frequency



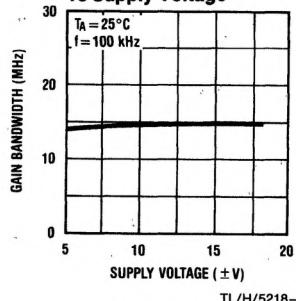
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Gain Bandwidth Product vs Ambient Temperature



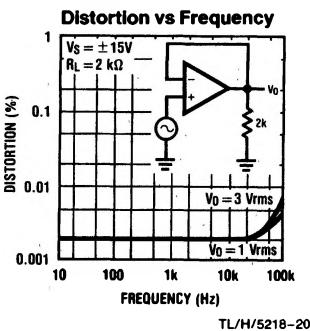
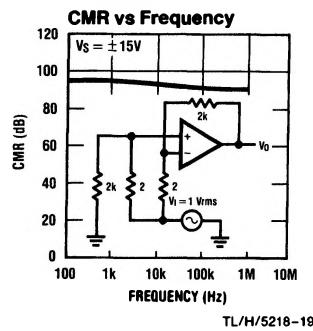
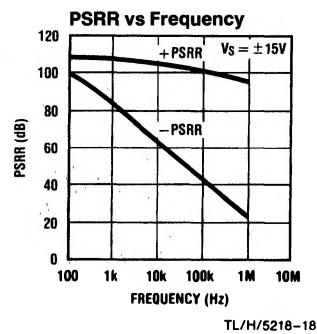
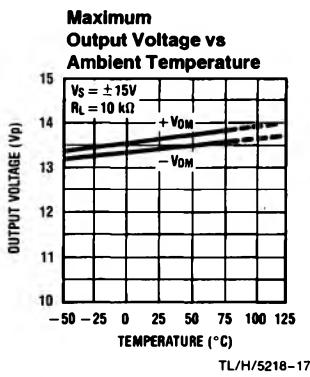
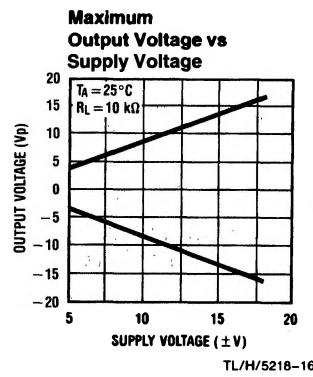
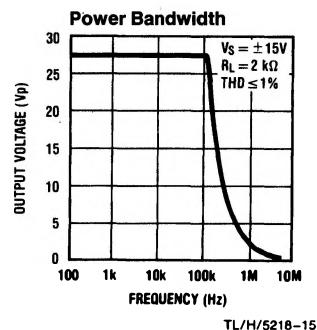
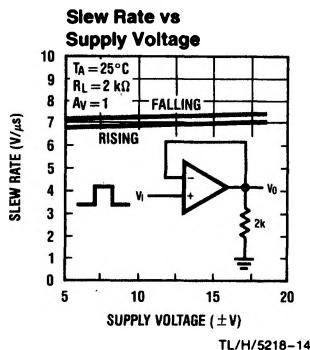
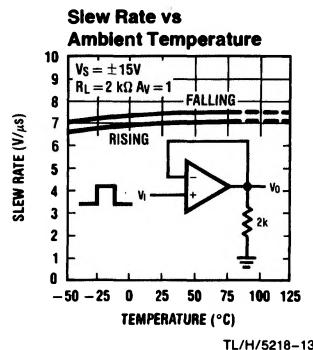
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Gain Bandwidth vs Supply Voltage

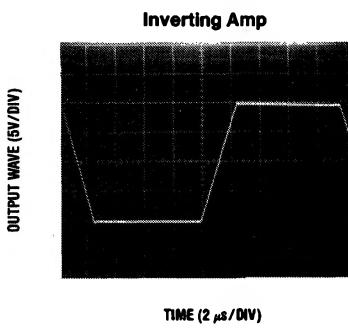
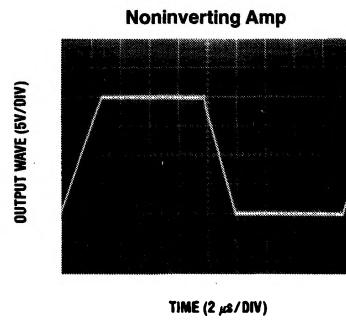
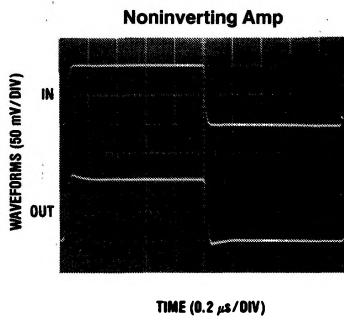
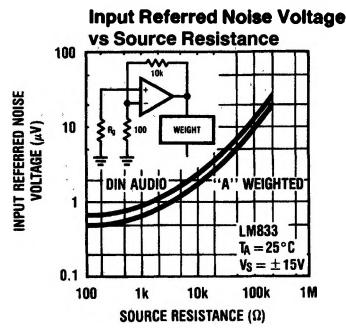
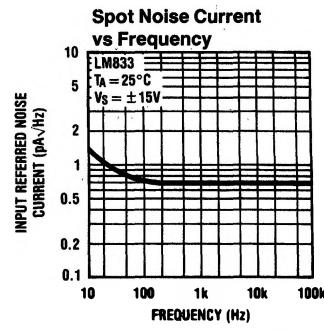
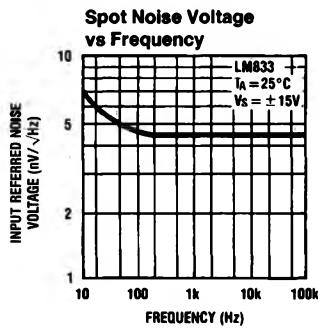


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Typical Performance Characteristics (Continued)



Typical Performance Characteristics (Continued)



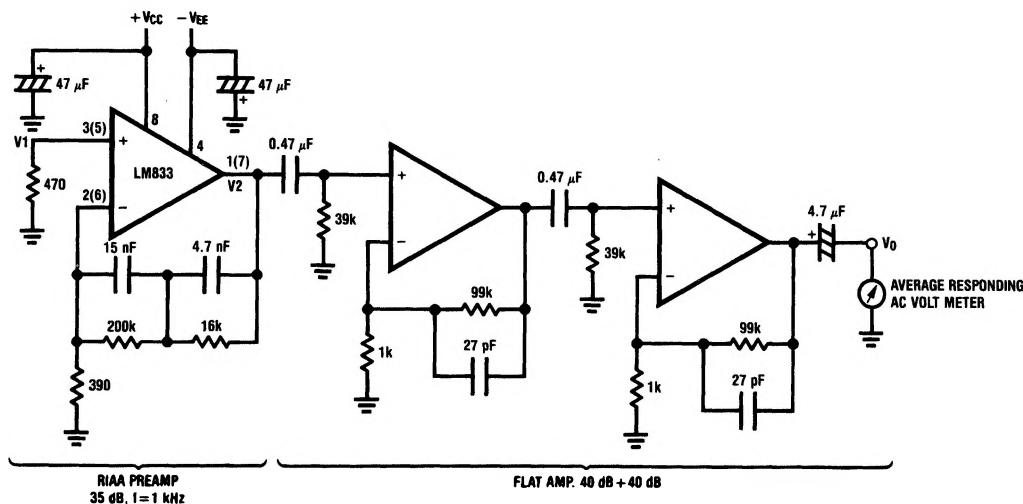
Application Hints

The LM833 is a high speed op amp with excellent phase margin and stability. Capacitive loads up to 50 pF will cause little change in the phase characteristics of the amplifiers and are therefore allowable.

Capacitive loads greater than 50 pF must be isolated from the output. The most straightforward way to do this is to put a resistor in series with the output. This resistor will also prevent excess power dissipation if the output is accidentally shorted.

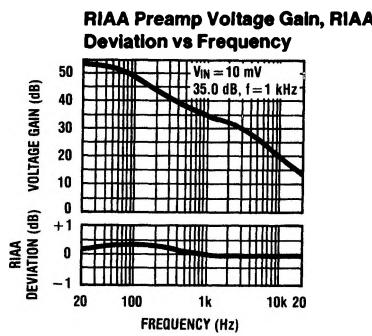
Noise Measurement Circuit

Complete shielding is required to prevent induced pick up from external sources. Always check with oscilloscope for power line noise.

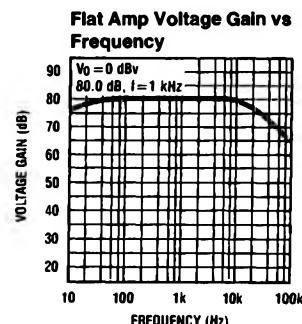


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Total Gain: 115 dB @ $f = 1 \text{ kHz}$
Input Referred Noise Voltage: $e_n = V_0/560,000 (\text{V})$



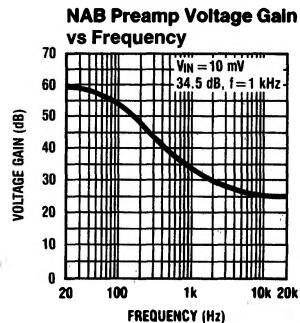
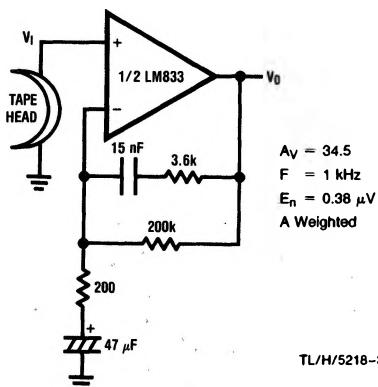
TL/H/5218-28



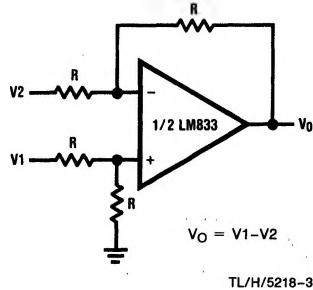
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Typical Applications

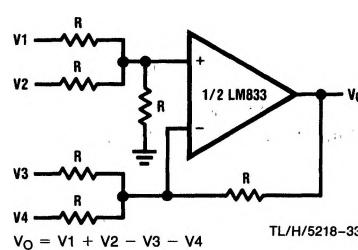
NAB Preamp



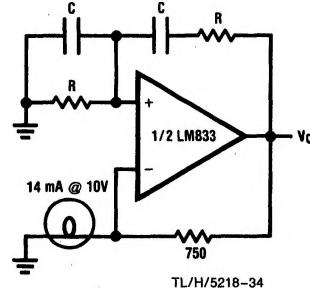
Balanced to Single Ended Converter



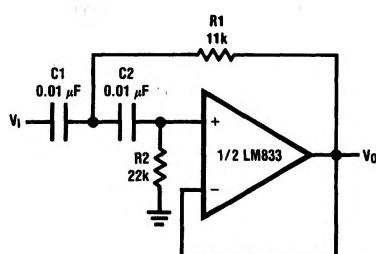
Adder/Subtractor



Sine Wave Oscillator



Second Order High Pass Filter (Butterworth)



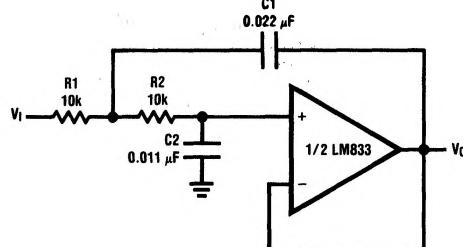
If $C_1 = C_2 = C$

$$R_1 = \frac{\sqrt{2}}{2\omega_0 C}$$

$$R_2 = 2R_1$$

Illustration is $f_0 = 1 \text{ kHz}$

Second Order Low Pass Filter (Butterworth)



If $R_1 = R_2 = R$

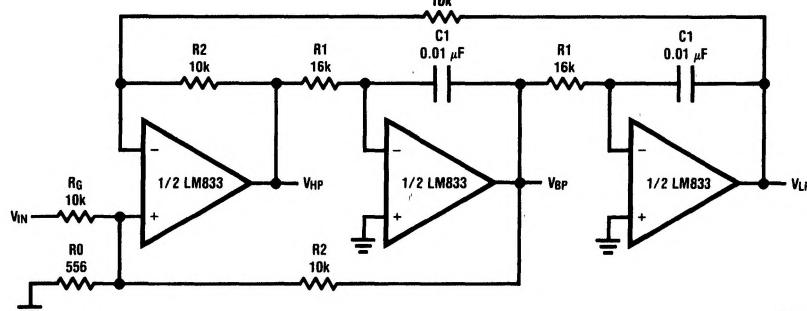
$$C_1 = \frac{\sqrt{2}}{\omega_0 R}$$

$$C_2 = \frac{C_1}{2}$$

Illustration is $f_0 = 1 \text{ kHz}$

Typical Applications (Continued)

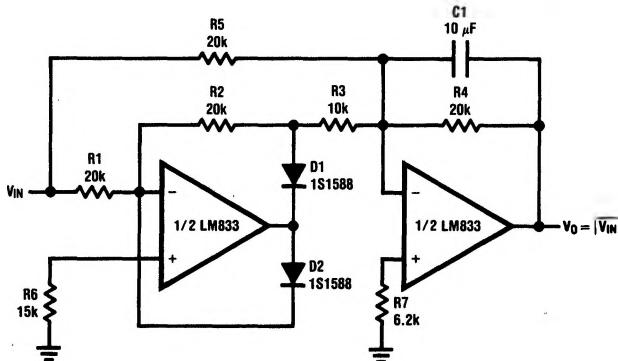
State Variable Filter



$$f_0 = \frac{1}{2\pi C_1 R_1}, Q = \frac{1}{2} \left(1 + \frac{R_2}{R_0} + \frac{R_2}{R_G} \right), A_{BP} = Q A_{LP} = Q A_{LH} = \frac{R_2}{R_G}$$

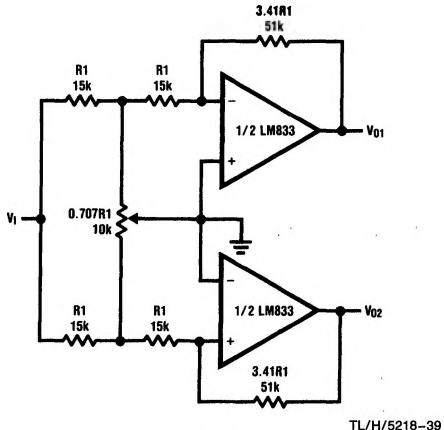
Illustration is $f_0 = 1$ kHz, $Q = 10$, $A_{BP} = 1$

AC/DC Converter



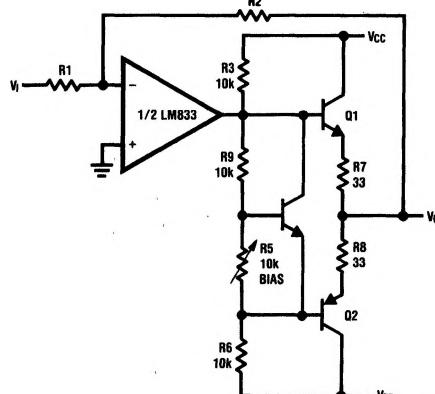
TL/H/5218-38

2 Channel Panning Circuit (Pan Pot)

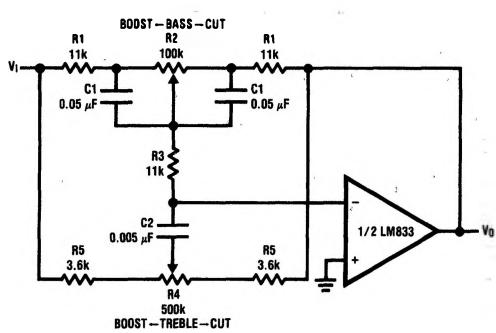


TL/H/5218-39

Line Driver



TL/H/5218-40

Typical Application (Continued)**Tone Control**

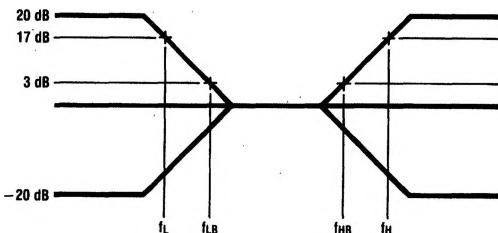
$$f_L = \frac{1}{2\pi R_2 C_1}, f_{LB} = \frac{1}{2\pi R_1 C_1}$$

$$f_H = \frac{1}{2\pi R_5 C_2}, f_{HB} = \frac{1}{2\pi(R_1 + R_5 + 2R_3)C_2}$$

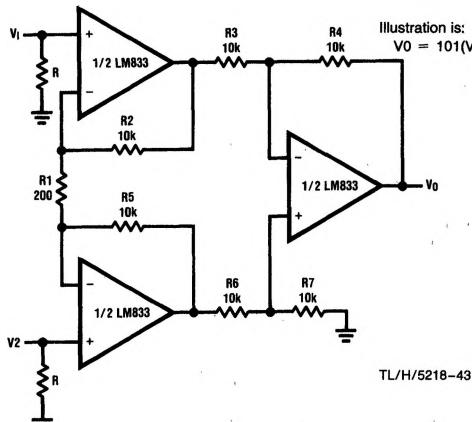
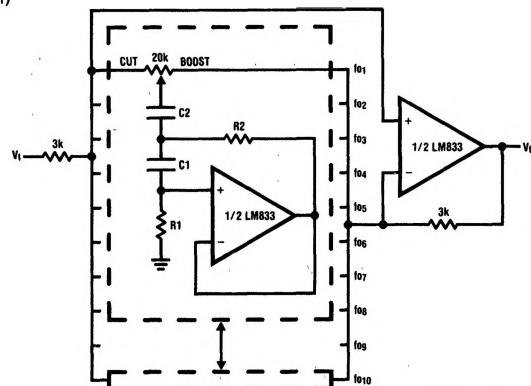
Illustration is:

$$f_L = 32 \text{ Hz}, f_{LB} = 320 \text{ Hz}$$

$$f_H = 11 \text{ kHz}, f_{HB} = 1.1 \text{ kHz}$$

**Balanced Input Mic Amp**If $R_2 = R_5, R_3 = R_6, R_4 = R_7$

$$V_O = \left(1 + \frac{2R_2}{R_1}\right) \frac{R_4}{R_3} (V_2 - V_1)$$

Illustration is:
 $V_O = 101(V_2 - V_1)$ **10 Band Graphic Equalizer**

$f_o(\text{Hz})$	C_1	C_2	R_1	R_2
32	$0.12\mu\text{F}$	$4.7\mu\text{F}$	$75\text{k}\Omega$	500Ω
64	$0.056\mu\text{F}$	$3.3\mu\text{F}$	$68\text{k}\Omega$	510Ω
125	$0.033\mu\text{F}$	$1.5\mu\text{F}$	$62\text{k}\Omega$	510Ω
250	$0.015\mu\text{F}$	$0.82\mu\text{F}$	$68\text{k}\Omega$	470Ω
500	8200pF	$0.39\mu\text{F}$	$62\text{k}\Omega$	470Ω
1k	3900pF	$0.22\mu\text{F}$	$68\text{k}\Omega$	470Ω
2k	2000pF	$0.1\mu\text{F}$	$68\text{k}\Omega$	470Ω
4k	1100pF	$0.056\mu\text{F}$	$62\text{k}\Omega$	470Ω
8k	510pF	$0.022\mu\text{F}$	$68\text{k}\Omega$	510Ω
16k	330pF	$0.012\mu\text{F}$	$51\text{k}\Omega$	510Ω

At volume of change = $\pm 12 \text{ dB}$ $\Omega = 1.7$

Reference: "AUDIO/RADIO HANDBOOK", National Semiconductor, 1980, Page 2-61