NXP LPCOpen LPC11U6x and LPC11E6x Release Notes

LPCOpen LPC11U6x and LPC11E6x version release history and known issues

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The version history and known issue lists on this page are for v3.xx, and 2.xx releases of LPCOpen only. Version history previous to v2.xx releases can be found HERE.

Some issues are known at the time of the versioned package release. Issues found after the release can be found on the LPCOpen bug tracker pages

LPCOpen v3.03.000 release (Released: 08/13/2018): NXP LPC11U6x and LPC11E6x

Features Added

- None

Changes and Fixes

- In the sysinit_11u6x.c, the flash time settings (changed to FLASHTIM_3CLK_CPU) corrected for CPU frequency operations above 40 MHz.

Known issues

- None

LPCOpen v3.02.000 release (Released: 12/11/2017): NXP LPC11U6x and LPC11E6x

Features Added

- None

Changes and Fixes

- In the syscon_11u6x.h, in the STARTERP1 register, updated the RTC interrupt wakeup bit from bit 19 to bit 12

Known issues

- None

LPCOpen v3.01.000 release (Released: 03/29/2017): NXP LPC11U6x and LPC11E6x

Features Added

- V2 LPCOpen code was converted to V3 LPCOpen. This simplifies code structure and decrease path lengths, LPCOpen has been reorganized to a flatter structure. These changes have been made in an effort to improve usability and maintainability of LPCOpen software

Changes and Fixes

- Definition issue fixed in Chip_GPIO_SetPortDIR(LPC_GPIO_T *pGPIO, uint8_t port, uint8_t pinMask, bool outSet) (file: gpio_11u6x.c) and miss "}" in Chip_SYSCTL_SetPinInterrupt(uint32_t intno, uint8_t port, uint8_t pin) (file: syscon_11u6x.c).
- Wrong type declaration fixed in gpio_11u6x.c and syscon_11u6x.c
- For EP0 transfers, HW will do auto handshake as long as the ACTIVE is set in EP0_IN/OUT command list. Unlike for other endpoints, hardware will not clear the ACTIVE bit after transfer is done. To handle this, LPCOpen software has been updated to manually clear the bit whenever it receives new setup packet and sets it only after it has queued the data for control transfer as shown in “Flowchart for control endpoint EP0” in the user manual.
- Fixed the ADC DMA issue

- Fix EEPROM failure (declared a byte array)

Known issues

- None

LPCOpen v2.12.000 release (Released: 11/18/2016): NXP LPC11U6x and LPC11E6x

Features Added

- None

Changes and Fixes

- Read blocking issue and proper return on SetBaud (uart_0_11u6xc.c:)
- Proper Flash read delay (fmc_11u6x.h, sysinit_11u6x.c)
- Proper max ADC clock rate and calibration procedure (adc.c, adc_11u6x.h adc_11u6x.c)
- Removed some dead code (never used) that was in error (I2cm_11u6x.c)
- Error in PININT assignment – fixed in this 11u6x release (syscon_11u6x.c)
- Error in GPIO port direction and set/clear (gpio_11u6x.h)

Known issues

None

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