

# Signetics

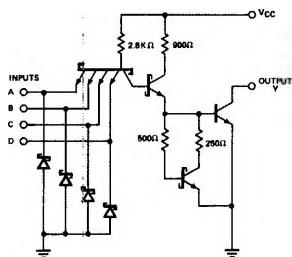
## DUAL 4-INPUT POSITIVE NAND GATE WITH OPEN-COLLECTOR OUTPUTS

S54S22-A,F,W • N74S22-A,P

**S54S22**  
**N74S22**

DIGITAL 54/74 TTL SERIES

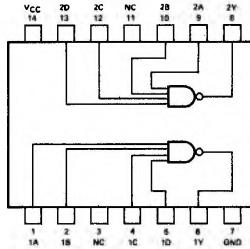
### SCHEMATIC (each gate)



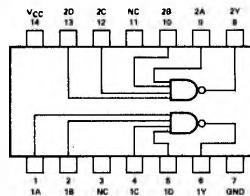
NOTE: Component values shown are nominal.

### PIN CONFIGURATIONS

#### W PACKAGE



#### A,F PACKAGE



### RECOMMENDED OPERATING CONDITIONS

	S54S22			N74S22			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
Supply Voltage $V_{CC}$	4.5	5	5.5	4.75	5	5.25	V
Normalized Fan-Out from any Output, N			10			10	
Operating Free-Air Temperature, $T_A$	-55		125	0		70	°C

### ELECTRICAL CHARACTERISTICS (over recommended operating free-air temperature range unless otherwise noted)

PARAMETER	TEST CONDITIONS *	MIN	TYP **	MAX	UNIT
$V_{IH}$	High-level input voltage		2		V
$V_{IL}$	Low-level input voltage			0.8	V
$V_I$	Input clamp voltage			-1.2	V
$i_{OH}$	High-level output current	$V_{CC} = \text{MIN}$ , $V_{CC} = \text{MIN}$ , $V_{OH} = 5.5V$		250	$\mu A$
$V_{OL}$	Low-level output voltage	$V_{CC} = \text{MIN}$ , $I_{OL} = 20mA$		0.5	V
$I_I$	Input current at maximum input voltage	$V_{CC} = \text{MAX}$ , $V_I = 5.5V$		1	$mA$
$i_{IH}$	High-level input current (each input)	$V_{CC} = \text{MAX}$ , $V_I = 2.7V$		50	$\mu A$
$i_{IL}$	Low-level input current (each input)	$V_{CC} = \text{MAX}$ , $V_I = 0.5V$		-2	$mA$
$i_{CCH}$	Supply current, high-level output (average per gate)	$V_{CC} = \text{MAX}$ , All inputs at 0V		1.5	$mA$
$i_{CCL}$	Supply current, low-level output (average per gate)	$V_{CC} = \text{MAX}$ , All inputs at 5V		5	$mA$

SWITCHING CHARACTERISTICS,  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$ ,  $N = 10$ 

PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT
$t_{PLH}$ Propagation delay time, low-to-high-level output	$C_L = 15pF$ ,	$R_L = 280\Omega$	2	5	7.5	ns
	$C_L = 50pF$ ,	$R_L = 280\Omega$			7.5	
$t_{PHL}$ Propagation delay time, high-to-low-level output	$C_L = 15pF$ ,	$R_L = 280\Omega$	2	4.5	7	ns
	$C_L = 50pF$ ,	$R_L = 280\Omega$			7	

\* For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable device type.

\*\* All typical values are at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$ .

NOTE 1: Load circuit and waveforms are shown on page 2-293