

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

SMA5101 — RF Double Balanced Mixer IC

Features

· Wide band : up to Ku band

• Low distortion : IIP3=20dBm (@ICC > 11mA)

• SMT, Ultra small package : 2.0×2.1×0.85mm

• High conversion gain : -0.5dB (@450MHz)

• Low voltage available: 1.2V and above

· Halogen free compliance

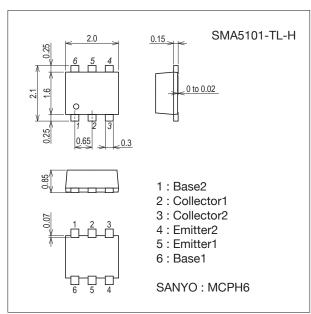
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		8	V
Collector-to-Emitter Voltage	VCEO		6	V
Emitter-to-Base Voltage	VEBO		2	V
Collector Current	Icc		50	mA
Max Power Dissipation	PC		280	mW
Operating Temperature	Topr		-40 to +85	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7022A-020



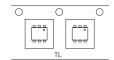
Product & Package Information

• Package : MCPH6

• JEITA, JEDEC : SC-88, SC-70-6, SOT-363

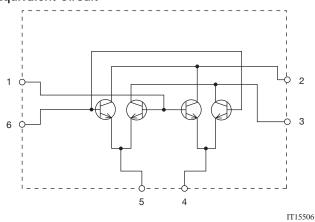
• Minimum Packing Quantity : 3,000 pcs./reel

Packing Type : TL Marking





Equivalent Circuit



Recommended Operating Condition at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Supply Voltage	VC1E1		1.2	3	6	V
	VC2E1		1.2	3	6	V
	VC1E2		1.2	3	6	V
	V _{C2E2}		1.2	3	6	V

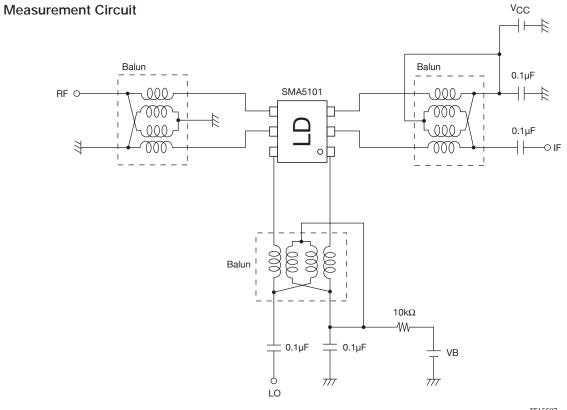
Electrical Characteristics at Ta=25°C

Parameter	Cymphal	Conditions	Ratings			Unit
	Symbol	Conditions	min	typ	max	
	IC1B1O	VC1B1=5V			1	μΑ
Collector Cutoff Current	IC2B1O	VC2B1=5V			1	μΑ
	lC1B2O	V _{C1B2} =5V			1	μΑ
	IC2B2O	V _{C2B2} =5V			1	μΑ
Emitter Cutoff Current	lE1B1O	V _{E1B1} =1V			1	μΑ
	IE2B1O	VE2B1=1V			1	μΑ
	lE1B2O	V _{E1B2} =1V			1	μΑ
	IE2B2O	V _{E2B2} =1V			1	μΑ
	hFE1	V _{C1E1} =1V, I _{C1E1} =3mA	20		120	
DC Current Cain	hFE2	VC2E1=1V, IC2E1=3mA	20		120	
DC Current Gain	hFE3	V _{C1E2} =1V, I _{C1E2} =3mA	20		120	
	hFE4	V _{C2E2} =1V, I _{C2E2} =3mA	20		120	
		V _{CC} =5V, I _{CC} =6mA,				
Conversion Gain *1	Gc	f(RF)=450MHz, f(LO)=500MHz,		-0.5		dB
		P(RF)=-15dBm, P(LO)=-6dBm				
Input Intercept Point *1		VCC=5V, ICC=6mA,				
	upo.	f(RF1)=450MHz, f(RF2)=451MHz,		15		-ID
	IIP3	f(LO)=500MHz,				dBm
		P(RF1)=P(RF2)=-15dBm, P(LO)=-6dBm				

^{*1 :} On evaluation board

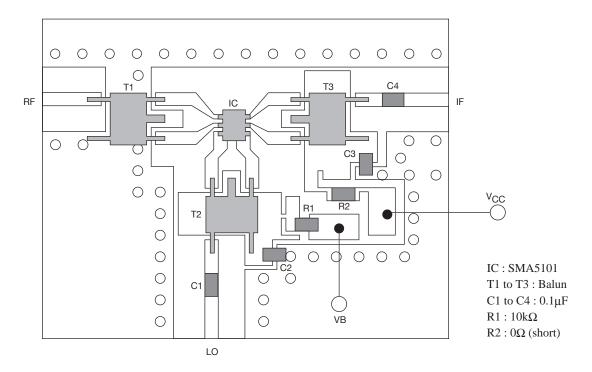
Ordering Information

Device	Device Package		memo	
SMA5101-TL-H	MA5101-TL-H MCPH6		Pb Free and Halogen Free	



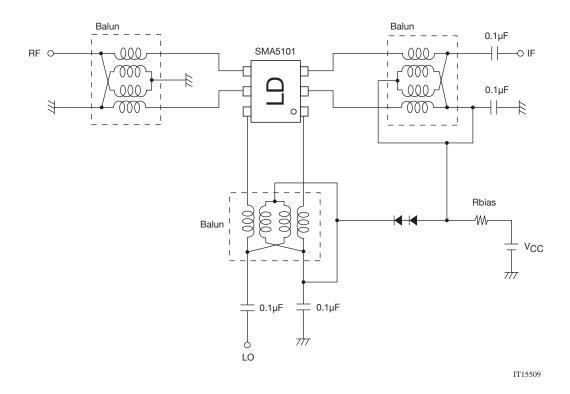
IT15507

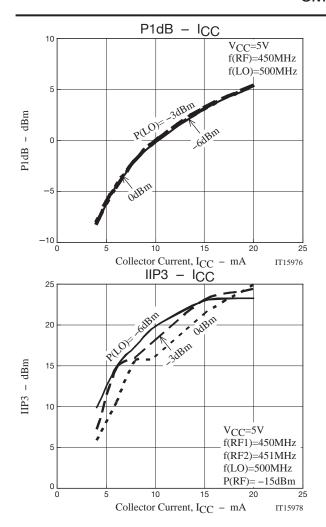
Evaluation Board

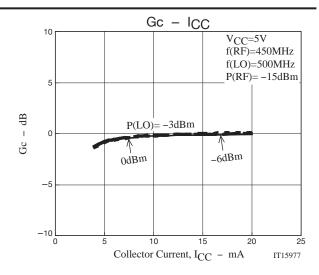


IT15508

Circuit Example (Self Bias)





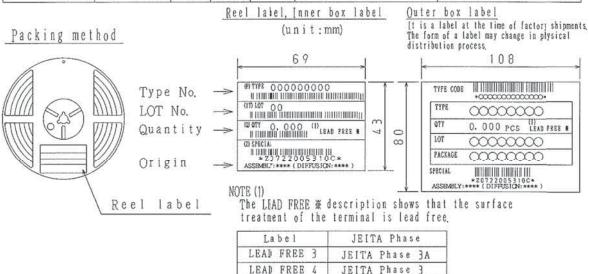


Embossed Taping Specification

SMA5101-TL-H

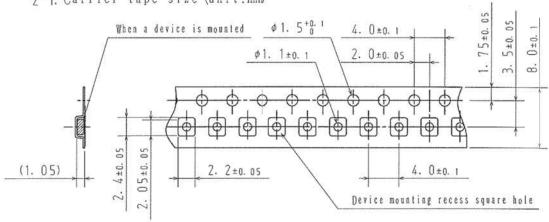
1. Packing Format

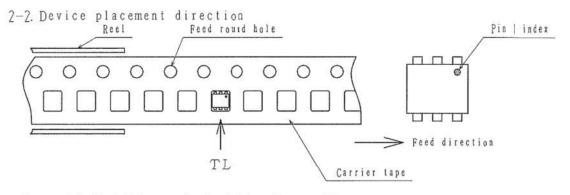
Package Name Carrier Tape Type	Maximum Number of devices contained (yes)			Packing format		
	Reel	[nner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)	
мсрн6	MCP4	3, 000	15, 000	90, 000	Dimensions:mm (external)	6 inner boxes contained Dimensions:mm (external) $440 \times 195 \times 210$



2. Taping configuration

2-1. Carrier tape size (unit:mm)

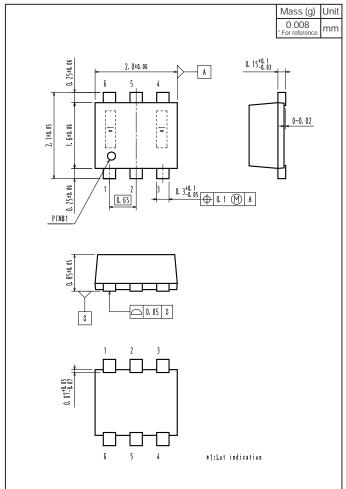




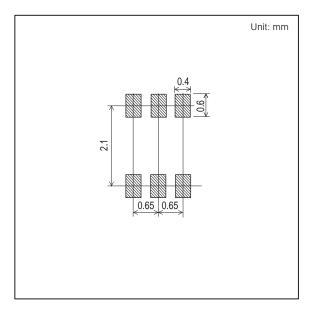
Those with pin 1 index on the feed hole side ·····TL

Outline Drawing

SMA5101-TL-H



Land Pattern Example



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