- Noninverters
- Package Options Include Plastic Small Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

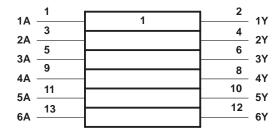
These devices contain six independent noninverters. They perform the Boolean function Y = A.

The SN54ALS34 and SN54AS34 are characterized for operation over the full military temperature range of – 55°C to 125°C. The SN74ALS34 and SN74AS34 are characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each buffer)

INPUT	OUTPUT
Α	Υ
Н	Н
L	L

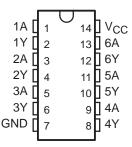
logic symbol†



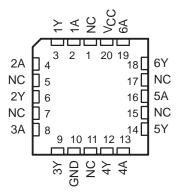
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54ALS34, SN54AS34 . . . J PACKAGE SN74ALS34, SN74AS34 . . . D OR N PACKAGE (TOP VIEW)

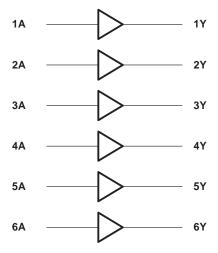


SN54ALS34, SN54AS34 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}	
Input voltage	
	–55°C to 125°C
SN74ALS34	0°C to 70°C
Storage temperature range	_65°C to 150°C

recommended operating conditions

		SI	SN54ALS34			SN74ALS34			
		MIN NOM MAX MIN NOM		MAX	UNIT				
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V	
V_{IH}	High-level input voltage	2			2			V	
VIL	Low-level input voltage			0.7			0.8	V	
ІОН	High-level output current			-0.4			-0.4	mA	
lOL	Low-level output current			4			8	mA	
TA	Operating free-air temperature	-55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN	SN54ALS34			SN74ALS34			
PARAMETER	1231 00	INDITIONS	MIN	TYP	MAX	MIN	TYP†	MAX	UNIT	
VIK	$V_{CC} = 4.5 \text{ V},$	$I_{ } = -18 \text{ mA}$			-1.2			-1.2	V	
Voн	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2			V _{CC} -2			V	
Vo	$V_{CC} = 4.5 \text{ V},$	I _{OL} = 4 mA		0.25	0.4		0.25	0.4	V	
VOL	$V_{CC} = 4.5 \text{ V},$	IOL = 8 mA					0.35	0.5	V	
ΙΙ	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA	
lН	V _{CC} = 5.5 V,	V _I = 2.7 V			20			20	μΑ	
Ι _Ι Γ	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA	
IO [‡]	$V_{CC} = 5.5 V,$	V _O = 2.25 V	-30		-112	-30		- 112	mA	
IССН	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		3.1	5		3.1	5	mA	
ICCL	V _{CC} = 5.5 V,	V _I = 0 V		5	8		5	8	mA	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = 25^{\circ}\text{C}$ 'ALS34 TYP	C _L R _L	= 50 p = 500 s = MIN t	Ω,		UNIT
^t PLH	Α	V	9.4	4	18	4	15	ns
t _{PHL}	Λ	'	5	1	12	1	10	113

NOTE 1: Load circuit and voltage waveforms are shown in Section 1 of ALS/AS Logic Data Book, 1986.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted),

Supply voltage, V _{CC}		 7 V
Input voltage		
Operating free-air temperature range:		
	SN74AS34	 0°C to 70°C
Storage temperature range		 –65°C to 150°C

recommended operating conditions

		SN54AS34			S	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.8			0.8	V
IOH	High-level output current			-2			-2	mA
loL	Low-level output current			20			20	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	TEST CONDITIONS		SN54AS34			SI	LINUT		
PARAMETER			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT
VIK	$V_{CC} = 4.5 \text{ V},$	I _I = –18 mA			-1.2			-1.2	V
Voн	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2			V _{CC} -2			V
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 20 \text{ mA}$		0.35	0.5		0.35	0.5	V
ΙĮ	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V			0.1			0.1	mA
liΗ	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V			20			20	μΑ
Iμ	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
IO]	$V_{CC} = 5.5 \text{ V},$	V _O = 2.25 V	-30		-112	-30		- 112	mA
^I ССН	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		7.4	12		7.4	12	mA
ICCL	$V_{CC} = 5.5 \text{ V},$	V _I = 0 V		21.3	34.6		21.3	34.6	mA

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R _L T _A	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = \text{MIN to MAX}$ $\text{SN54AS34} \qquad \text{SN74AS34}$			UNIT
			MIN	MAX	MIN	MAX	
t _{PLH}	^	V	1	6.5	1	5.5	no
t _{PHL}	А	ſ	1	7	1	6	ns

NOTE 2: Load circuit and voltage waveforms are shown in Section 1 of ALS/AS Logic Data Book, 1986.



[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

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