SCBS053B - MAY 1990 - REVISED APRIL 1994

- State-of-the-Art BiCMOS Design Significantly Reduces I_{CCZ}
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015; Exceeds 200 V Using Machine Model (C = 200 pF, R = 0)
- Designed to Facilitate Incident-Wave Switching for Line Impedances of 25 Ω or Greater
- Distributed V_{CC} and GND Pins Minimize Noise Generated by the Simultaneous Switching of Outputs
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (JT, NT)

description

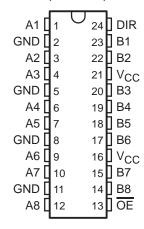
The 'BCT25245 is a 25- Ω octal bus transceiver designed for asynchronous communication between data buses. It improves both the performance and density of 3-state memory address drivers, clock drivers, and bus-oriented transceivers.

The device allows data transmission from the A bus to the B bus or from the B bus to the A bus depending upon the logic level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can disable the device so that both buses are effectively isolated.

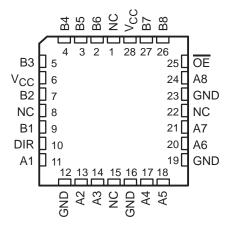
These transceivers are capable of sinking 188-mA I_{OL} , which facilitates switching 25- Ω transmission lines on the incident wave. The distributed V_{CC} and GND pins minimize switching noise for more reliable system operation.

The SN54BCT25245 is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74BCT25245 is characterized for operation from 0°C to 70°C.

SN54BCT25245 . . . JT OR W PACKAGE SN74BCT25245 . . . DW OR NT PACKAGE (TOP VIEW)



SN54BCT25245 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

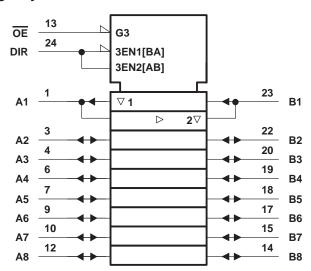
FUNCTION TABLE

INP	UTS	
ŌĒ	DIR	OPERATION
L	L	B data to A bus
L	Н	A data to B bus
Н	X	Isolation

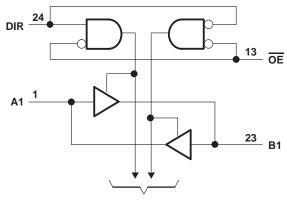


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logic symbol[†]



logic diagram (positive logic)



To Seven Other Channels

Pin numbers shown are for the DW, JT, NT, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	–0.5 V to 7 V
Input voltage range, V _I (see Note 1): Control inputs	–0.5 V to 7 V
I/O ports	0.5 V to 5.5 V
Voltage range applied to any output in the disabled or power-off state, VO	0.5 V to 5.5 V
Voltage range applied to any output in the high state, VO (B port)	–0.5 V to V _{CC}
Input clamp current, I _{IK}	–30 mA
Current into any output in the low state, IO: SN54BCT25245 (A port)	250 mA
SN54BCT25245 (B port)	40 mA
SN74BCT25245 (A port)	376 mA
SN74BCT25245 (B port)	48 mA
Operating free-air temperature range: SN54BCT25245	–55°C to 125°C
SN74BCT25245	0°C to 70°C
Storage temperature range	65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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recommended operating conditions

			SN5	4BCT25	245	SN74BCT25245			LINIT
			MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage		4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage		2		7	2			V
VIL	Low-level input voltage			Š	0.8			8.0	V
lik	Input clamp current		-18			-1			
	I Pale I and autout annual	A port		-53 -3			-80 -3		4
IOH	High-level output current	B port							mA
	Law law law and a summer	A port		5	125			188	0
IOL	Low-level output current		Q		20			24	mA
TA	Operating free-air temperature		-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED				SN5	4BCT25	245	SN7	4BCT25	245		
PARAMETER VIK		TEST	CONDITIONS	MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT	
		V _{CC} = 4.5 V,	I _I = -18 mA			-1.2			-1.2	V	
		V 45V	$I_{OH} = -53 \text{ mA}$	2							
Vон	A port	V _{CC} = 4.5 V	$I_{OH} = -80 \text{ mA}$				2			.,	
		$V_{CC} = 4.75 \text{ V},$	$I_{OH} = -3 \text{ mA}$				2.7			V	
	B port	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3			
			I _{OL} = 94 mA		0.38	0.55		0.42	0.55		
	A port	$V_{CC} = 4.5 \text{ V}$	$I_{OL} = 125 \text{ mA}$			0.8					
V_{OL}			I _{OL} = 188 mA						0.7	V	
	Dunant	V 45V	$I_{OL} = 20 \text{ mA}$		0.3	0.5					
	B port	V _{CC} = 4.5 V	$I_{OL} = 24 \text{ mA}$					0.35	0.5		
	A or B port	\\ F F \\	V. 55V		0.25				0.25	A	
I _I	Control input	$V_{CC} = 5.5 \text{ V},$	V _I = 5.5 V							mA	
†	A or B port	\\ F F \\	V. 07V		Ö	70			70	^	
I _{IH} ‡	Control input	$V_{CC} = 5.5 \text{ V},$	$V_{I} = 2.7 \text{ V}$		20				20	μΑ	
. +	A or B port	V 55V	V: 05V	Q	7	-0.6			-0.6	Λ	
I _{IL} ‡	Control input	$V_{CC} = 5.5 \text{ V},$	V _I = 0.5 V						-0.6	mA	
los§	B port only¶	V _{CC} = 5.5 V,	VO = 0	-60		-150	-60		-150	mA	
	A to B			1	36	46		36	46	_	
ICCH	B to A	V _{CC} = 5.5 V			63	80		63	80	mA	
	A to B				48	60		48	60		
ICCL	B to A	V _{CC} = 5.5 V			95	125		95	125	mA	
ICCZ	•	V _{CC} = 5.5 V			12	16		12	16	mA	
Ci	Control input	V _{CC} = 5 V,	V _I = 2.5 V or 0.5 V		8			8		pF	
	A port		V 05V c= 05V		18			18			
C _{io}	B port	$V_{CC} = 5 V$	$V_0 = 2.5 \text{ V or } 0.5 \text{ V}$		8	1		8		pF	

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

 $[\]P$ Testing for this parameter on the A port is not recommended.



[‡] For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

[§] Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

SN54BCT25245, SN74BCT25245 25- Ω OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

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switching characteristics (see Note 2)

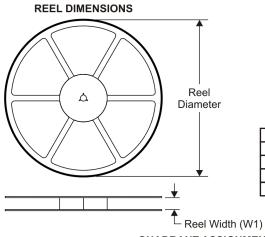
PARAMETER	FROM TO (INPUT) (OUTPUT)		C _L R1 R2	C = 5 V = 50 p = 500 g = 500 g = 25°C	F, Ω, Ω,	V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R1 = 500 Ω , R2 = 500 Ω , T_A = MIN to MAX \dagger				UNIT	
			′B	′BCT25245			Г25245	SN74BC			
			MIN	TYP	MAX	MIN	MAX	MIN	MAX		
t _{PLH}		Б	1.2	3.3	5.1	1.2	5.8	1.2	5.7		
t _{PHL}	Α	В	1.9	4.3	6.7	1.9	7.6	1.9	7.2	ns	
t _{PLH}	r.	^	1.2	3.3	4.8	1.2	.2 5.7 1.2	1.2	5.5	ns	
t _{PHL}	В	A	2.1	4	5.6	2.1	6.4	2.1	6.2		
^t PZH	ŌĒ		3.7	6.3	8.4	3.7	10.1	3.7	9.6		
t _{PZL}	OE	А	4.5 7.4	9.2	4.5	11.1	4.5	10.3	ns		
^t PHZ	ŌĒ	•	1.8	3.7	5.5	1.8	6.4	1.8	6.2		
tPLZ	OE	А	3.3	5.1	7.2	3.3	9.6	3.3	8.3	ns	
^t PZH	ŌĒ	В	3.4	5.7	7.9	3.4	9.2	3.4	8.9		
t _{PZL}	OE	В	4.3	6.6	8.7	4.3	10.1	4.3	9.7	ns	
t _{PHZ}	ŌĒ	P	2.7	4.5	6.3	2.7	7.2	2.7	6.9	ns	
t _{PLZ}	OE	В	1.7	4.5	6.8	1.7	8.3	1.7	7.5	115	

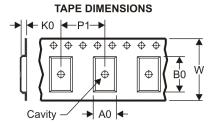
[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





		Dimension designed to accommodate the component width
		Dimension designed to accommodate the component length
P	〈 0	Dimension designed to accommodate the component thickness
		Overall width of the carrier tape
П	P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74BCT25245DWR	SOIC	DW	24	2000	330.0	24.4	10.75	15.7	2.7	12.0	24.0	Q1

PACKAGE MATERIALS INFORMATION

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74BCT25245DWR	SOIC	DW	24	2000	346.0	346.0	41.0

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