SCBS678C - SEPTEMBER 1996 - REVISED JANUARY 2001

- Member of Texas Instruments' Widebus™
 Family
- State-of-the-Art Advanced Low-Voltage BiCMOS (ALB) Technology Design for 3.3-V Operation
- Schottky Diodes on All Inputs to Eliminate Overshoot and Undershoot
- Industry Standard '16245 Pinout
- Distributed V_{CC} and GND Pins Minimize High-Speed Switching Noise
- Flow-Through Architecture Optimizes PCB Layout

description

The SN74ALB16245 is a 16-bit transceiver designed for high-speed, low-voltage (3.3-V) V_{CC} operation. This device is intended to replace the conventional transceiver in any speed-critical path. The small propagation delay is achieved using a unity-gain amplifier on the input and feedback resistors from input to output, which allows the output to track the input with a small offset voltage.

This device can be used as two 8-bit transceivers or one 16-bit transceiver. It allows data transmission from the A bus to the B bus or from the B bus to the A bus, depending on the logic level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can be used to disable the device so that the buses are effectively isolated.

DGG, DGV, OR DL PACKAGE (TOP VIEW)

| | $\overline{}$ | $\overline{}$ | |
|-------------------|---------------|---------------|-------------------|
| 1DIR [| 1 | ر 48 | 1 <u>0E</u> |
| 1B1 [| 2 | 47 | 1A1 |
| 1B2 [| 3 | 46 | 1A2 |
| GND [| 4 | 45 |] GND |
| 1B3 [| 5 | 44 | 1A3 |
| 1B4 [| 6 | 43 | _ |
| v _{cc} [| 7 | 42 |] v _{cc} |
| 1B5 | 8 | 41 | 1A5 |
| 1B6 | 9 | 40 | 1A6 |
| GND [| 10 | 39 | GND |
| 1B7 | | | 1A7 |
| 1B8 | | | 1A8 |
| 2B1 | | 36 | 2A1 |
| 2B2 | | 35 | 2A2 |
| GND [| | L | GND |
| 2B3 | 16 | | 2A3 |
| 2B4 | | 32 | 2A4 |
| v _{cc} [| | 31 | - 00 |
| 2B5 | | 30 | 2A5 |
| 2B6 | 20 | | 2A6 |
| GND [| | | GND |
| 2B7 | | | 2A7 |
| 2B8 | | | 2A8 |
| 2DIR [| 24 | 25 | 20E |
| | | | |

ORDERING INFORMATION

| TA | PACK | AGE† | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
|---------------|-------------|---------------|--------------------------|---------------------|
| | SSOP – DL | Tube | SN74ALB16245DL | ALB16245 |
| –40°C to 85°C | 330F - DL | Tape and reel | SN74ALB16245DLR | ALD 10245 |
| -40 C to 65 C | TSSOP – DGG | Tape and reel | SN74ALB16245DGGR | ALB16245 |
| | TVSOP - DGV | Tape and reel | SN74ALB16245DGVR | AV245 |

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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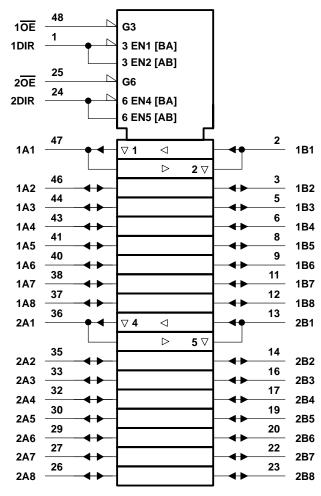
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FUNCTION TABLE (each 8-bit section)

| INP | UTS | OPERATION |
|-----|-----|-----------------|
| OE | DIR | OPERATION |
| L | L | B data to A bus |
| L | Н | A data to B bus |
| Н | X | Isolation |

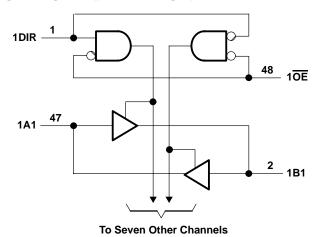
logic symbol†

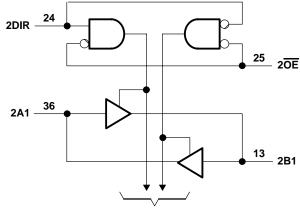


[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.



logic diagram (positive logic)





To Seven Other Channels

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

| Supply voltage range, V _{CC} | | –0.5 V to 4.6 V |
|---|-------------|---------------------------|
| Input voltage range, V _I : Except I/O ports (see N | Note 1) | –0.5 V to 4.6 V |
| I/O ports (see Notes 1 a | and 2)–0.5 | V to V_{CC} + 0.5 V |
| Output voltage range, VO (see Notes 1 and 2) | 0.5 | V to V_{CC} + 0.5 V |
| Input clamp current, I_{IK} ($V_I < 0$) | | –50 mA |
| Output clamp current, IOK (VO < 0 or VO > VCC) | ;) | ±50 mA |
| Continuous output current, I_O ($V_O = 0$ to V_{CC}) | | ±50 mA |
| Continuous current through each V _{CC} or GND | | ±100 mA |
| Package thermal impedance, θ_{JA} (see Note 3): | DGG package | 70°C/W |
| | DGV package | 58°C/W |
| | DL package | 63°C/W |
| Storage temperature range, T _{stg} | | . −65°C to 150°C |

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 4.6 V maximum.
- 3. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions

| | | | MIN | MAX | UNIT |
|-------------------|---|------|-----|-----|------|
| Vcc | Supply voltage | | 3 | 3.6 | V |
| I _{OH} ‡ | High-level output current | | | -25 | mA |
| l _{OL} ‡ | Low-level output current | | | 25 | mA |
| Δt/Δν | Input transition rise or fall rate Outputs enal | oled | | 5 | ns/V |
| TA | Operating free-air temperature | | -40 | 85 | °C |

[‡] See Figures 1 and 2 for typical I/O ranges.



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PA | RAMETER | | TEST CONDITIONS | | MIN | TYP† | MAX | UNIT |
|---------------------|--------------------|---|---|-----------------------|-----|------|----------------------|------|
| 1/114 | A or B ports | VCC = 3 V | I _I = 18 mA | | | 3.7 | V _{CC} +1.2 | V |
| VIK | A or B ports | ∧CC = 2 ∧ | I _I = -18 mA | | | -0.9 | -1.2 | v |
| | Control inputs | V _{CC} = 3.6 V, | $V_I = V_{CC}$ or GND | | | | ±10 | μΑ |
| | | | V _I = V _{CC} | OE low | | 0.4 | 0.6 | mA |
| Ιį | A or B ports | VCC = 3.6 V | ΛI = ΛCC | OE high | | | 25 | μΑ |
| | A of P bolts ACC = | VCC = 3.0 V | V _I = 0 | OE low | | -0.7 | -1 | mA |
| | | | V = 0 | OE high | | | -60 | μΑ |
| lozh | | V _{CC} = 3.6 V, | V _O = 3 V | | | 0.7 | 20 | μΑ |
| lozL | | $V_{CC} = 3.6 \text{ V},$ | V _O = 0.5 V | | | -0.2 | - 50 | μΑ |
| I _{CC} /bı | uffer | $V_{CC} = 3.6 \text{ V},$ | $I_{O} = 0$, | $V_I = V_{CC}$ or GND | | 3.7 | 5.6 | mA |
| ICCZ | | $V_{CC} = 3.6 \text{ V}$, Control inputs = V_{CC} or GND | | | | | 0.8 | mA |
| ∆lcc [‡] | | $V_{CC} = 3 \text{ V to } 3.6 \text{ V, C}$ | 3 V to 3.6 V, One input at V _{CC} –0.6 V, Other inputs at V _{CC} or GND | | | | 600 | μΑ |
| Ci | | V _I = 3 V or 0 | | | 3.5 | | pF | |
| C _{io} | | $V_O = 3 V \text{ or } 0$ | | | | 7.5 | | pF |

switching characteristics over recommended operating free-air temperature range, C_L = 50 pF (unless otherwise noted) (see Figure 3)

| PARAMETER | FROM | то | V _{CC} = | UNIT | | |
|------------------|---------|----------|-------------------|------------------|-----|------|
| PARAMETER | (INPUT) | (OUTPUT) | MIN | TYP [†] | MAX | UNIT |
| ^t pd | A or B | B or A | 0.6 | 1.3 | 2 | ns |
| ^t en | ŌĒ | A or B | 1.5 | 3.2 | 6 | ns |
| ^t dis | ŌĒ | A or B | 1.8 | 2.8 | 4.2 | ns |

[†] All typical values are at $V_{CC} = 3.3 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



[†] All typical values are at $V_{CC} = 3.3 \text{ V}$, $T_A = 25^{\circ}\text{C}$. ‡ This is the increase in supply current for each input that is at the specified TTL voltage level rather than V_{CC} or GND.

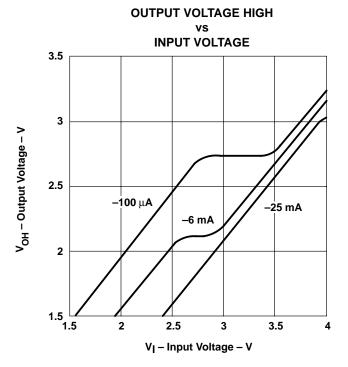


Figure 1. $V_{\mbox{OH}}$ Over Recommended Free-Air Temperature Range

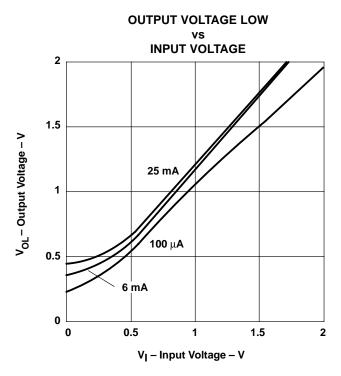
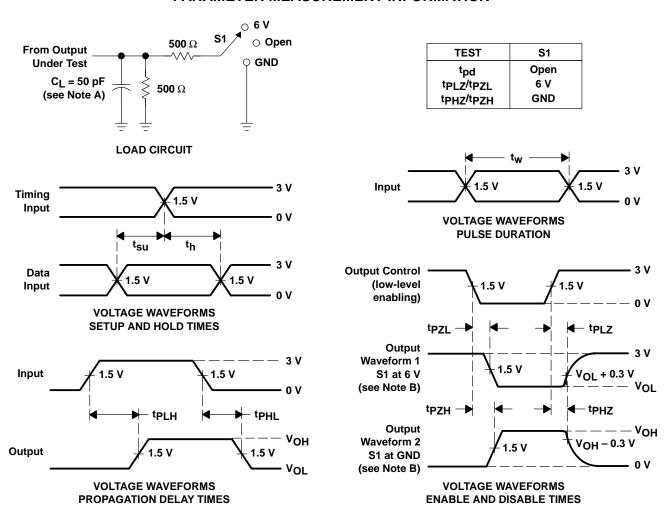


Figure 2. $V_{\mbox{\scriptsize OL}}$ Over Recommended Free-Air Temperature Range



PARAMETER MEASUREMENT INFORMATION



- NOTES: A. C_L includes probe and jig capacitance.
 - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 - C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_Q = 50 \Omega$, $t_f \leq$ 2.5 ns, $t_f \leq$ 2.5 ns.
 - D. The outputs are measured one at a time with one transition per measurement.
 - E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .
 - F. tpzL and tpzH are the same as ten.
 - G. tpLH and tpHL are the same as tpd.

Figure 3. Load Circuit and Voltage Waveforms







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PACKAGING INFORMATION

| Orderable Device | Status ⁽¹⁾ | Package Type | Package Drawing | Pins | Package Qty | e Eco Plan ⁽²⁾ | Lead/Ball Finish | MSL Peak Temp ⁽³⁾ |
|-------------------|-----------------------|-----------------|--------------------|------|----------------|---------------------------|------------------|------------------------------|
| 74ALB16245DGGRE4 | ACTIVE | TSSOP | DGG | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| 74ALB16245DGGRG4 | ACTIVE | TSSOP | DGG | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| 74ALB16245DGVRE4 | ACTIVE | TVSOP | DGV | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| 74ALB16245DGVRG4 | ACTIVE | TVSOP | DGV | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DGGR | ACTIVE | TSSOP | DGG | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DGVR | ACTIVE | TVSOP | DGV | 48 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DL | ACTIVE | SSOP | DL | 48 | 25 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DLG4 | ACTIVE | SSOP | DL | 48 | 25 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DLR | ACTIVE | SSOP | DL | 48 | 1000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74ALB16245DLRG4 | ACTIVE | SSOP | DL | 48 | 1000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

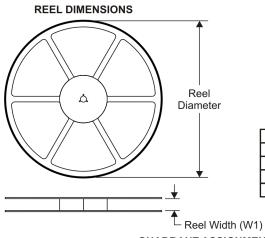
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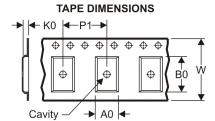
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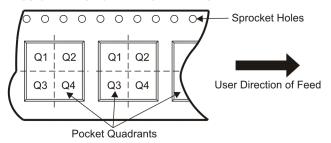
TAPE AND REEL INFORMATION





| | Dimension designed to accommodate the component width |
|----|---|
| | Dimension designed to accommodate the component length |
| K0 | Dimension designed to accommodate the component thickness |
| W | Overall width of the carrier tape |
| P1 | Pitch between successive cavity centers |

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

| Device | Package Type | Package Drawing | | SPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|------------------|-----------------|--------------------|----|------|--------------------------|--------------------------|------------|------------|------------|------------|-----------|------------------|
| SN74ALB16245DGGR | TSSOP | DGG | 48 | 2000 | 330.0 | 24.4 | 8.6 | 15.8 | 1.8 | 12.0 | 24.0 | Q1 |
| SN74ALB16245DGVR | TVSOP | DGV | 48 | 2000 | 330.0 | 16.4 | 7.1 | 10.2 | 1.6 | 12.0 | 16.0 | Q1 |
| SN74ALB16245DLR | SSOP | DL | 48 | 1000 | 330.0 | 32.4 | 11.35 | 16.2 | 3.1 | 16.0 | 32.0 | Q1 |

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*All dimensions are nominal

| 7 til dillionolollo alo nominal | | | | | | | |
|---------------------------------|--------------|-----------------|-----------------|------|-------------|------------|-------------|
| Device | Package Type | Package Drawing | ge Drawing Pins | | Length (mm) | Width (mm) | Height (mm) |
| SN74ALB16245DGGR | TSSOP | DGG | 48 | 2000 | 346.0 | 346.0 | 41.0 |
| SN74ALB16245DGVR | TVSOP | DGV | 48 | 2000 | 346.0 | 346.0 | 33.0 |
| SN74ALB16245DLR | SSOP | DL | 48 | 1000 | 346.0 | 346.0 | 49.0 |

DL (R-PDSO-G**)

48 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).

D. Falls within JEDEC MO-118

DGG (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

48 PINS SHOWN



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

DGV (R-PDSO-G**)

24 PINS SHOWN

PLASTIC SMALL-OUTLINE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.

D. Falls within JEDEC: 24/48 Pins – MO-153 14/16/20/56 Pins – MO-194



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