

2K BIT (128 × 16) SERIAL CMOS EEPROM

ADVANCE DATA

- 128 × 16 SERIAL EEPROM
- SINGLE POWER SUPPLY = FROM 2.7 TO 5.5 VOLTS
- 10 YEAR DATA RETENTION AFTER 100.000 ERASE/WRITE CYCLES PER WORD
- CMOS LOW POWER CONSUMPTION = 3 MA MAX ACTIVE CURRENT AND 0.1 MA MAX STANDBY CURRENT
- 4 BYTE WRITE MODE
- SELF TIMED PROGRAMMING CYCLE (WITH AUTOERASE)
- WRITE PROTECTION IN USER DEFINED SECTION OF MEMORY
- SEQUENTIAL REGISTER READ

DESCRIPTION

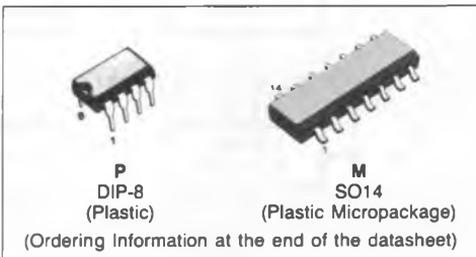
The ST93C56 is a 2048 bit non-volatile sequential access memory manufactured using SGS-THOMSON Single Floating Gate process.

It is designed to operate from 3 to 5 Volts in order to match telecommunications requirements.

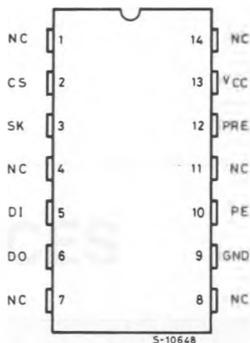
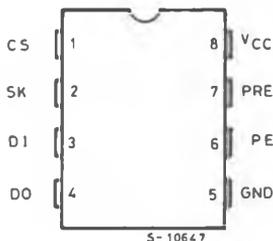
Moreover, a double cell per bit architecture will allow to guarantee 100K erase/write cycles.

PIN NAMES

CS	CHIP SELECT
SK	SERIAL DATA CLOCK
DI	SERIAL DATA INPUT
DO	SERIAL DATA OUTPUT
GND	GROUND
PE	PROGRAM ENABLE
PRE	PROTECT REGISTER ENABLE
V _{CC}	POWER SUPPLY
NC	NO CONNECT



PIN CONNECTIONS



BLOCK DIAGRAM

