



Analog Solutions—Robust, Reliable Performance

VR500

Multi-Output DC/DC Regulator

Overview

The VR500 power management solution for network processor systems is a high-efficiency, quad buck regulator with up to 4.5 A output and five user-programmable LDOs. With four buck regulators and five LDO output channels, the VR500 powers more than the network processor, significantly reducing design complexity and the overall bill of materials (BOM). The highly integrated VR500 output voltage, frequency and turn-on sequence is user programmable using I²C.

The VR500 is ideally suited to power system solutions based on QorIQ LS1 networking communications processors with unique programmable multiple DC/DC and LDO outputs. The VR500 has been incorporated into multiple QorIQ LS1/T1 networking communications processor reference platforms. This collaboration provides platform-level solutions from a single supplier to enable faster time to market and reduced engineering effort.

Features

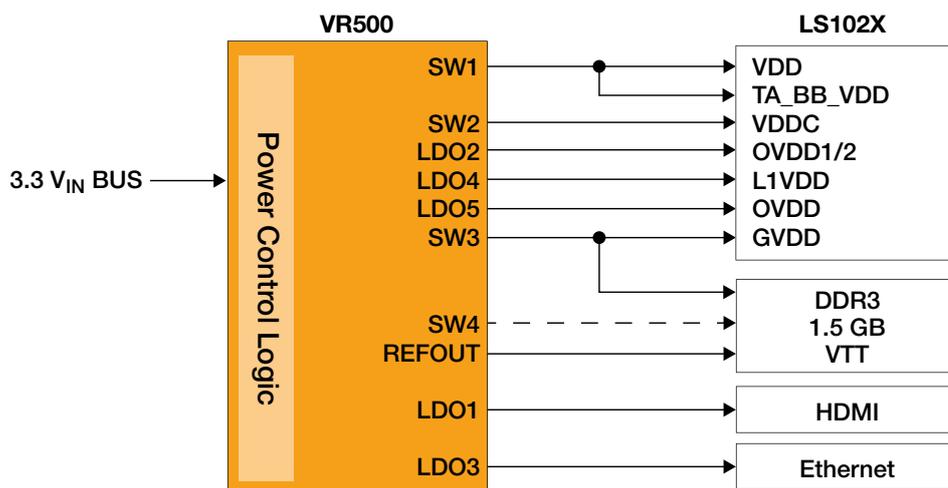
- Optimized to work with QorIQ LS102x/T102x communications processors
- High full load efficiency with 91% peak
- Customizable preprogrammed output voltages, sequencing or timing available
- Dynamic regulator control of voltage, current limit, and frequency via I²C
- Forced PWM/PFM or APS operation
- Power control logic with processor interface and event detection
- Vin = 3.3 Vbus (2.8 V to 4.5 V supply)
- Four independent buck converters
- Five user programmable LDOs
- DDR reference voltage LDO
- High power 8 x 8 mm QFN wettable flank package
- Auto qualified AEC Q100 grade 2

Target Applications

- Internet of Things (IoT) gateway
- Industrial automation and control
- M2M devices
- Mobile wireless router
- MFP printer
- Network attached storage
- Automatic teller machine (ATM)
- Automotive wireless router



VR500 Simplified Application Block Diagram



Freescale: A Leader in Analog Solutions

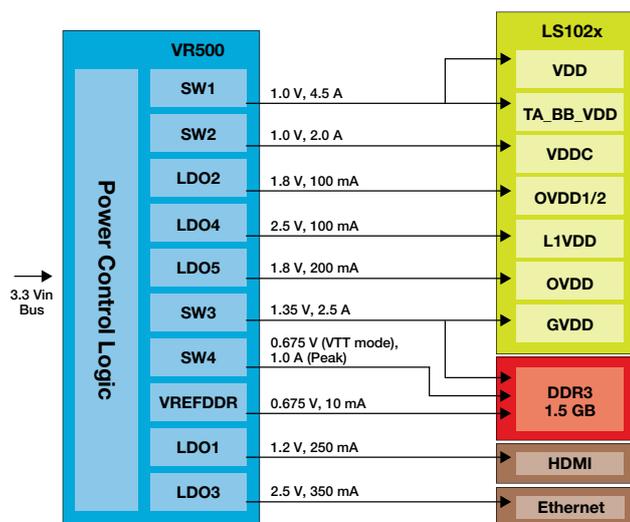
Expanding on more than 30 years of innovation, Freescale is a leading provider of high-performance products that use SMARTMOS technology combining digital, power and standard analog functions. Freescale supplies analog and power management ICs that are advancing the automotive, consumer, industrial and networking markets. Analog solutions interface with real world signals to control and drive complete embedded systems.

Complete Enablement, Rich Ecosystem

For customer evaluation, the VR500 multi-output DC/DC regulator and QorIQ LS102x communications processors will be supported by modular tools along with third-party platforms developed by Freescale's embedded board solution partners. The VR500 powers the complete QorIQ LS102x IoT reference design as well as LS1021_TWR_Rev2 board.

All QorIQ LS series devices are supported by our extensive third-party ecosystem, the largest and most established in the communications market. In conjunction with our expertise and worldwide support infrastructure, the ecosystem helps customers accelerate their migration from both competitive solutions and from legacy Freescale devices, preserve investment costs and reduce time to market.

VR500 Multi-Output DC/DC Regulator and QorIQ LS102x Communications Processor System Block Diagram



VR500 Differentiators

Features	Benefits
Four buck converters	High-efficiency, lower power dissipation, longer battery life
Five LDOs	Flexibility to power peripherals
No external resistor divider required to set output voltage	Lower external component count; Better overall Vout accuracy
I ² C digital interface for programmability	On-the-fly voltage scaling for better system efficiency
PWM/PFM or APS (Auto-Pulse Skipping Mode)	Higher light load efficiency—longer battery standby time
8 x 8 mm WF-QFN power package	Excellent thermal performance and improved inspection of the solder joints during fabrication process

Documentation

Freescale Document Number	Title	Description
VR500	Multi-Output DC/DC Regulator	Data Sheet
SG1002	Analog Product Selector Guide	Selector Guide
SG200	Industrial Product Selector Guide	Selector guide

Processor Line-up

VR500 Version	Target Processor	Reference Design	DDR Memory	SW4 VTT Mode
MC34VR500V1ES	LS1020/LS1021/ LS1022	TWR-LS1021A-PB/ LS1021A-OITB	DDR3L	0.675V
MC34VR500V2ES			N/A	Disabled
MC34VR500V3ES			DDR4	0.6V
MC34VR500V4ES	LS1043A/ T1023A/ T1013A	LS1043A RDB/ T1023RDB	DDR4	0.6V
MC34VR500V5ES			DDR3L	0.675V

For more information, visit freescale.com/analog or freescale.com/VR500

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