16-Lead Plastic Dual Inline Package (PDIP) 0.300" Body Width Package Dimensions



SEMICONDUCTOR TM

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Symbol	Inches		Millimeters		Natas
	Min.	Max.	Min.	Max.	Notes
А	—	.210	_	5.33	
A1	.015	_	.38	_	
A2	.115	.195	2.93	4.95	
В	.014	.022	.36	.56	
B1	.045	.070	1.14	1.78	
С	.008	.014	.20	.36	4
D	.745	.840	18.92	21.33	2
D1	.005		.13	_	
Е	.300	.325	7.62	8.26	
E1	.240	.280	6.10	7.11	2
е	.100 BSC		2.54 BSC		
eB	—	.430	—	10.92	
L	.115	.160	2.92	4.06	
Ν	16		16		5

Notes:

1. Dimensioning and tolerancing per ANSI Y14.5M-1982.

2. "D" and "E1" do not include mold flashing. Mold flash or protrusions shall not exceed .010 inch (0.25mm).

- 3. Terminal numbers are shown for reference only.
- 4. "C" dimension does not include solder finish thickness.
- 5. Symbol "N" is the maximum number of terminals.







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