

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

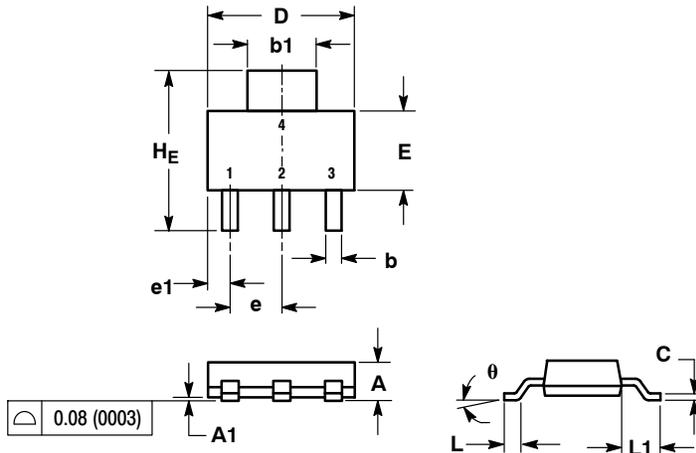


SOT-223 (TO-261) CASE 318E-04 ISSUE N

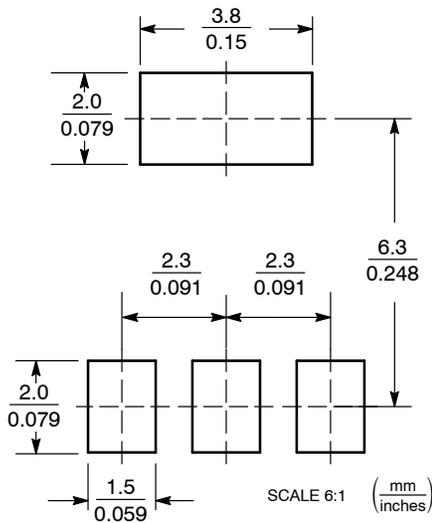
DATE 10 AUG 2009



SCALE 1:1



SOLDERING FOOTPRINT

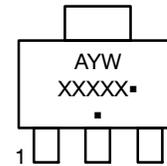


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
c	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
e	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L	0.20	---	---	0.008	---	---
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	-	10°	0°	-	10°

GENERIC MARKING DIAGRAM*



- A = Assembly Location
- Y = Year
- W = Work Week
- XXXXX = Specific Device Code
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

- | | | | | |
|--|---|---|---|---|
| <p>STYLE 1:
PIN 1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR</p> | <p>STYLE 2:
PIN 1. ANODE
2. CATHODE
3. NC
4. CATHODE</p> | <p>STYLE 3:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN</p> | <p>STYLE 4:
PIN 1. SOURCE
2. DRAIN
3. GATE
4. DRAIN</p> | <p>STYLE 5:
PIN 1. DRAIN
2. GATE
3. SOURCE
4. GATE</p> |
| <p>STYLE 6:
PIN 1. RETURN
2. INPUT
3. OUTPUT
4. INPUT</p> | <p>STYLE 7:
PIN 1. ANODE 1
2. CATHODE
3. ANODE 2
4. CATHODE</p> | <p>STYLE 8:
CANCELLED</p> | <p>STYLE 9:
PIN 1. INPUT
2. GROUND
3. LOGIC
4. GROUND</p> | <p>STYLE 10:
PIN 1. CATHODE
2. ANODE
3. GATE
4. ANODE</p> |
| <p>STYLE 11:
PIN 1. MT 1
2. MT 2
3. GATE
4. MT 2</p> | <p>STYLE 12:
PIN 1. INPUT
2. OUTPUT
3. NC
4. OUTPUT</p> | <p>STYLE 13:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR</p> | | |

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STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	SOT-223	PAGE 1 OF 2

