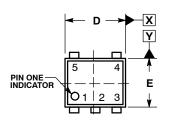
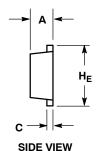


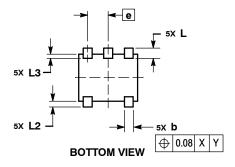
SOT-953 CASE 527AE **ISSUE E**

DATE 02 AUG 2011

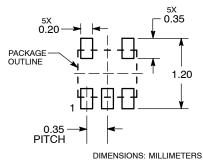


TOP VIEW





SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.34	0.37	0.40
b	0.10	0.15	0.20
C	0.07	0.12	0.17
D	0.95	1.00	1.05
Е	0.75	0.80	0.85
е	0.35 BSC		
Hε	0.95	1.00	1.05
L	0.175 REF		
L2	0.05	0.10	0.15
L3			0.15

GENERIC MARKING DIAGRAM*



= Specific Device Code

= Month Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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NEW STANDARD:		"CONTROLLED CO
DESCRIPTION:	SOT-953, 1X0.8, 0.35P	

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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY D. TRUHITTE.	14 FEB 2008
Α	UPDATED DIMENSION C VALUES. REQ. BY D. TRUHITTE.	01 APR 2008
В	CHANGED DATUM BOXES OF X AND Y TO JEDEC STANDARDS OF A, B & C. REQ. BY D. TRUHITTE.	11 APR 2008
С	CORRECTED DEVICE MARKING INFORMATION TO REFLECT ONLY ONE CHARACTER FOR DEVICE CODE. REQ. BY D. TRUHITTE.	05 MAY 2008
D	CORRECTED ENGLISH DIMENSIONS FOR PAD SIZES ON SOLDERING FOOT-PRINT FROM 0.08 TO 0.008. REQ. BY D. TRUHITTE.	30 JUL 2008
E	UPDATED DRAWING TO ADD BOTTOM VIEW. CORRECTED SOLDER FOOT-PRINT. REQ. BY D. TRUHITTE.	02 AUG 2011

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