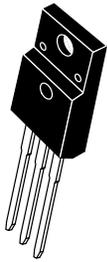


# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

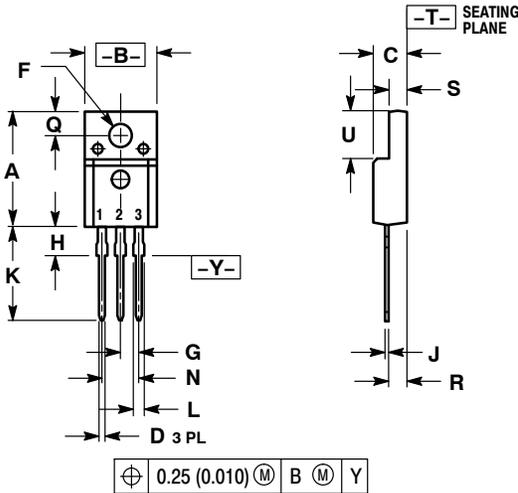
ON Semiconductor®



SCALE 1:1

## TO-220 FULLPAK CASE 221D-03 ISSUE K

DATE 27 FEB 2009

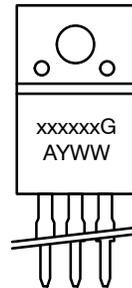


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH
  3. 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.617	0.635	15.67	16.12
B	0.392	0.419	9.96	10.63
C	0.177	0.193	4.50	4.90
D	0.024	0.039	0.60	1.00
F	0.116	0.129	2.95	3.28
G	0.100 BSC		2.54 BSC	
H	0.118	0.135	3.00	3.43
J	0.018	0.025	0.45	0.63
K	0.503	0.541	12.78	13.73
L	0.048	0.058	1.23	1.47
N	0.200 BSC		5.08 BSC	
Q	0.122	0.138	3.10	3.50
R	0.099	0.117	2.51	2.96
S	0.092	0.113	2.34	2.87
U	0.239	0.271	6.06	6.88

### MARKING DIAGRAMS

- |  |   |  |
|--|---|--|
| STYLE 1:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE     | STYLE 2:<br>PIN 1. BASE<br>2. COLLECTOR<br>3. EMITTER | STYLE 3:<br>PIN 1. ANODE<br>2. CATHODE<br>3. ANODE |
| STYLE 4:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE | STYLE 5:<br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE     | STYLE 6:<br>PIN 1. MT 1<br>2. MT 2<br>3. GATE      |



**Bipolar**



**Rectifier**

- |                               |                           |
|-------------------------------|---------------------------|
| xxxxxx = Specific Device Code | A = Assembly Location     |
| G = Pb-Free Package           | Y = Year                  |
| A = Assembly Location         | WW = Work Week            |
| Y = Year                      | xxxxxx = Device Code      |
| WW = Work Week                | G = Pb-Free Package       |
|                               | AKA = Polarity Designator |

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<b>STATUS:</b>	<b>ON SEMICONDUCTOR STANDARD</b>	
<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>TO-220 FULLPAK</b>	<b>PAGE 1 OF 2</b>

