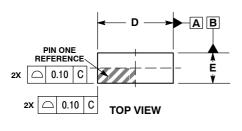
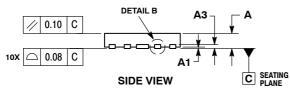


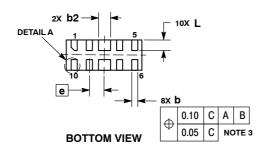
## UDFN10 2.5x1, 0.5P CASE 517BB-01 **ISSUE 0**

**DATE 17 NOV 2009** 

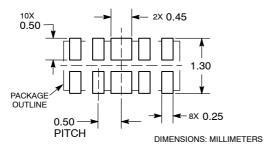




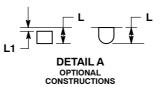


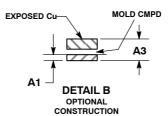


## RECOMMENDED **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.





## NOTES:

- DIMENSIONING AND TOLERANCING PER
  ASME Y14.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm FROM TERMINAL.

|     | MILLIMETERS |      |  |
|-----|-------------|------|--|
| DIM | MIN         | MAX  |  |
| Α   | 0.45        | 0.55 |  |
| A1  | 0.00        | 0.05 |  |
| A3  | 0.13 REF    |      |  |
| b   | 0.15        | 0.25 |  |
| b2  | 0.35        | 0.45 |  |
| D   | 2.50 BSC    |      |  |
| Ε   | 1.00 BSC    |      |  |
| е   | 0.50 BSC    |      |  |
| L   | 0.30        | 0.40 |  |
| L1  |             | 0.05 |  |

## **GENERIC MARKING DIAGRAM\***



= Specific Device Code XXX

= Date Code М

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

| DOCUMENT NUMBER: | 98AON47059E               | Electronic version                       |  |
|------------------|---------------------------|--|--|
| STATUS:          | ON SEMICONDUCTOR STANDARD | accessed directly from versions are unco |  |
| NEW STANDARD:    |                           | "CONTROLLED CO                           |  |
| DESCRIPTION:     | UDFN10 2.5X1, 0.5P        |  |  |

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| 0     | RELEASED FOR PRODUCTION. REQ. BY A. TAM. | 17 NOV 2009 |
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