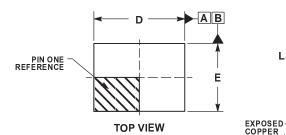


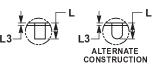
## WDFNW14 4x3, 0.5P CASE 511CM **ISSUE A**

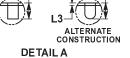
PLATED

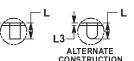
**DATE 03 JUL 2018** 

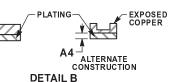


**TOP VIEW** 

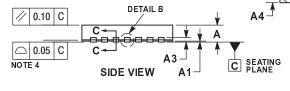


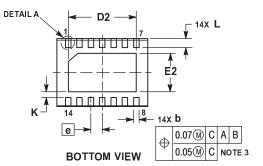




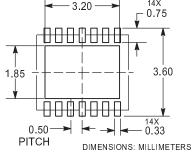


SECTION C-C





## **RECOMMENDED** SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## NOTES

- NOTES:

  1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.

  2. CONTROLLING DIMENSION: MILLIMETERS.

  3. DIMESNION 5 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM TERMINAL.
- MM FROM LERMINAL.
  COPLANARITY APPLIES TO THE EXPOSED
  PAD AS WELL AS THE TERMINALS.
  THIS DEVICE CONTAINS WETTABLE FLANK
  DESIGN FEATURES TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.70	0.75	0.80
A1	0.00	0.03	0.05
A3	(	0.20 REF	
A4	0.10		
b	0.20	0.25	0.30
D	3.90	4.00	4.10
D2	2.90	3.00	3.10
Е	2.90	3.00	3.10
E2	1.60	1.70	1.80
е	0.50 BSC		
K	0.25		
L	0.30	0.40	0.50
L3	0.50 REF		

## **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code = Assembly Location Υ = Year WW = Work Week

(\*Note: Microdot may be in either location)

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

DOCUMENT NUMBER:	98AON13091G	
STATUS:	ON SEMICONDUCTOR STANDARD	{
NEW STANDARD:		
DESCRIPTION:	WDFNW14 4X3, 0.5P	

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PAGE 1 OF 2