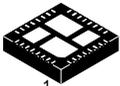


MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®

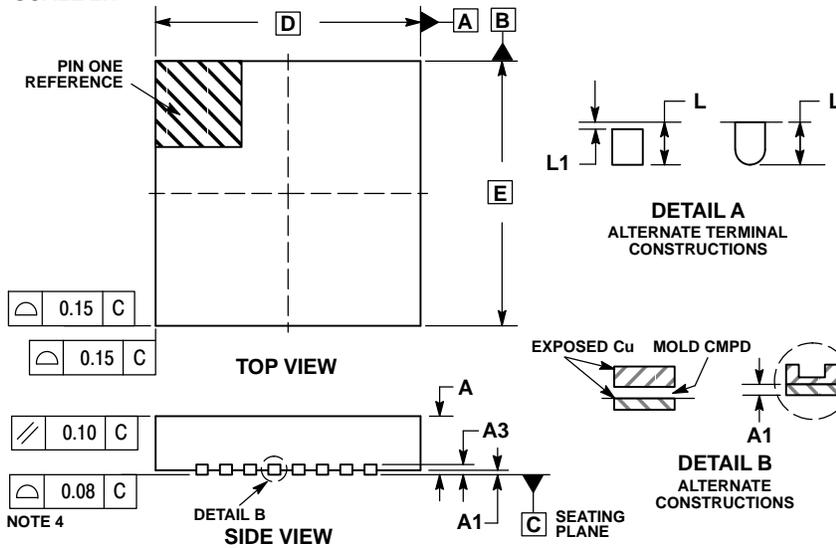


SCALE 2:1

WQFN32 4.4x4.4, 0.4P

CASE 485BN
ISSUE A

DATE 04 NOV 2015

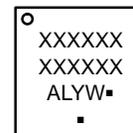


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.25 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. POSITIONAL TOLERANCE APPLIES TO ALL OF THE EXPOSED PADS.

MILLIMETERS		
DIM	MIN	MAX
A	0.70	0.80
A1	0.00	0.05
A3	0.20	REF
b	0.15	0.25
D	4.40	BSC
D2	1.30	1.50
D3	1.25	1.45
E	4.40	BSC
E2	1.70	1.90
E3	0.90	1.10
e	0.40	BSC
F	1.55	BSC
L	0.30	0.50
L1	0.05	0.15

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code

A = Assembly Location

L = Wafer Lot

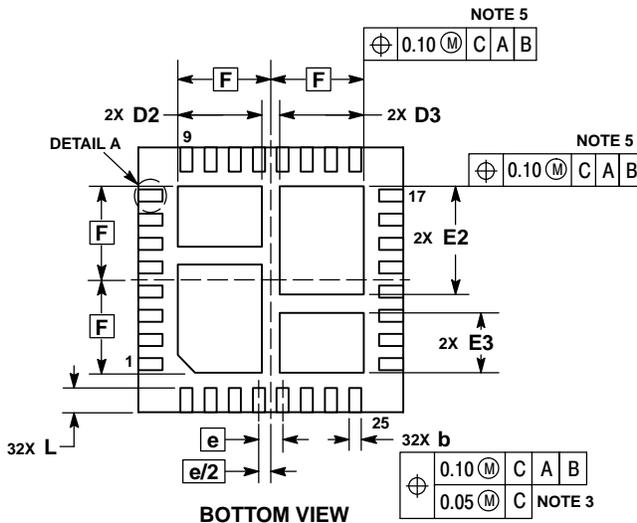
Y = Year

W = Work Week

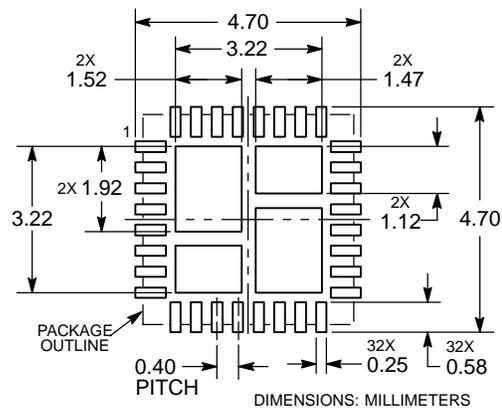
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.



RECOMMENDED MOUNTING FOOTPRINT



DOCUMENT NUMBER:	98AON53288E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	WQFN32 4.4X4.4, 0.4P	PAGE 1 OF 2

