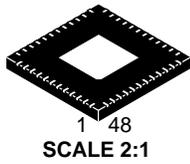


# MECHANICAL CASE OUTLINE

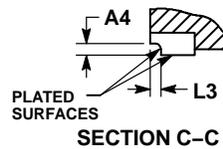
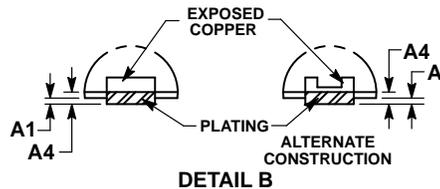
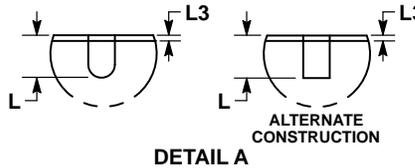
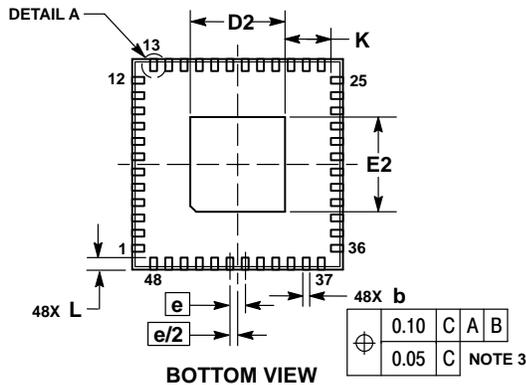
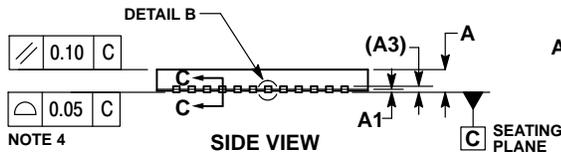
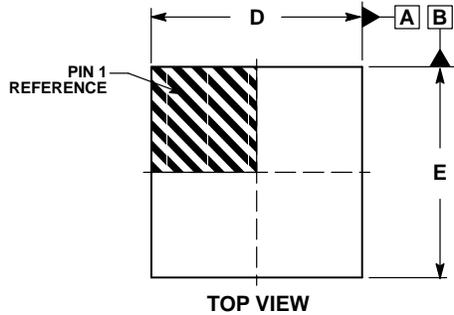
## PACKAGE DIMENSIONS

ON Semiconductor®



WQFNW48 7x7, 0.5P  
CASE 512AD  
ISSUE A

DATE 10 AUG 2018

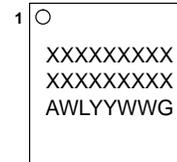


NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED ABETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	---	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.20	0.25	0.30
D	6.90	7.00	7.10
D2	3.00	3.10	3.20
E	6.90	7.00	7.10
E2	3.00	3.10	3.20
e	0.50 BSC		
K	1.55 REF		
L	0.30	0.40	0.50
L3	0.05 REF		

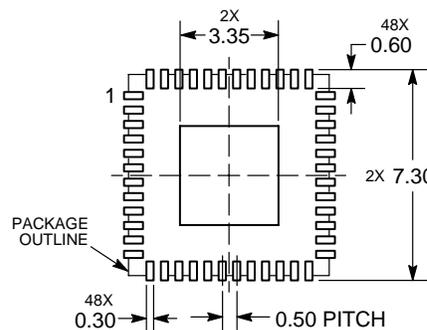
GENERIC MARKING DIAGRAM\*



- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	WQFNW48 7x7, 0.5P	PAGE 1 OF 1

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