

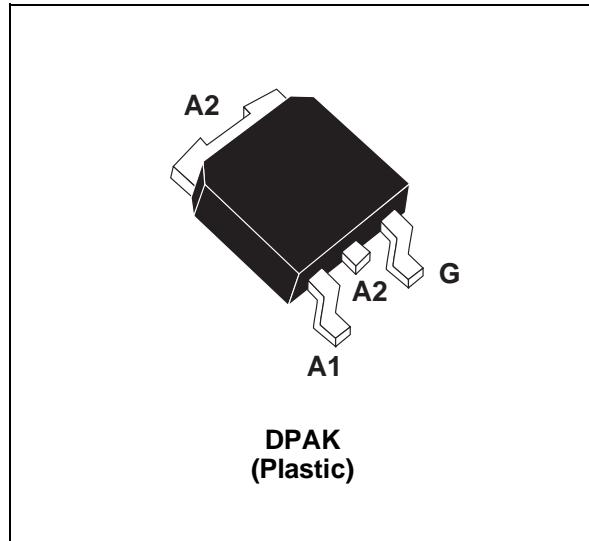
HIGH PERFORMANCE TRIACS

FEATURES

$I_{TRMS} = 4\text{ A}$
 $V_{DRM} = 400\text{ V to }600\text{ V}$
 SENSITIVE GATE : $I_{GT} \leq 5\text{ mA}$
 LOGIC LEVEL
 HIGH NOISE IMMUNITY

DESCRIPTION

The T405-B triac is using a high performance TOP-GLASS PNPN technology. This device is intended for AC control applications using surface mount technology. The high commutation performances combined with high sensitivity make this triac perfect for direct drive from microprocessors in all applications like appliances, power tools, small motor drives etc...



ABSOLUT MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
$I_{T(RMS)}$	RMS on-state current (360° conduction angle)	4	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)	$t_p = 8.3\text{ ms}$	A
		$t_p = 10\text{ ms}$	
I^2t	I^2t value for fusing	4.5	A^2s
dI/dt	Critical rate of rise of on-state current $I_G = 50\text{mA}$ $dI/dt = 0.1\text{A}/\mu\text{s}$	10	$\text{A}/\mu\text{s}$
		50	
T_{stg} T_j	Storage temperature range Operating junction temperature range	- 40 to + 150 - 40 to + 110	°C °C
T_l	Maximum lead temperature for soldering during 10 s	260	°C

Symbol	Parameter	T405-		Unit
		400B	600B	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage $T_j = 110\text{ °C}$	400	600	V

T405-B

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
R _{th} (j-c)	Junction to case for DC	3.5	°C/W
R _{th} (j-c)	Junction to case for AC 360° conduction angle (F = 50 Hz)	2.6	°C/W

GATE CHARACTERISTICS (maximum values)

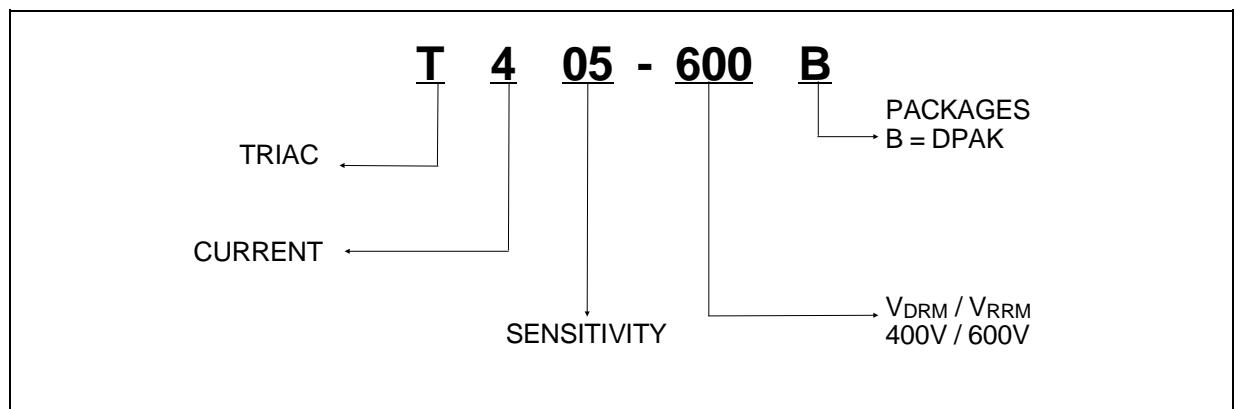
P_{G(AV)} = 1 W P_{GM} = 10 W (tp = 20 μs) I_{GM} = 4 A (tp = 20 μs) V_{GM} = 16 V (tp = 20 μs).

ELECTRICAL CHARACTERISTICS

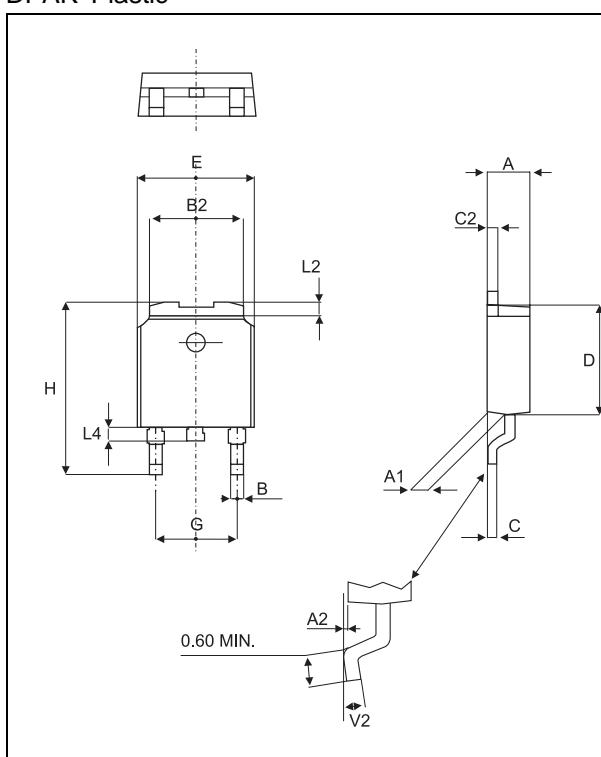
Symbol	Test Conditions		Quadrant		Value	Unit
I _{GT}	V _D =12V (DC) R _L =33Ω	T _j =25°C	I-II-III	MAX	5	mA
V _{GT}	V _D =12V (DC) R _L =33Ω	T _j =25°C	I-II-III	MAX	1.5	V
V _{GD}	V _D =V _{DRM} R _L =3.3kΩ	T _j =110°C	I-II-III	MIN	0.2	V
t _{gt}	V _D =V _{DRM} I _G = 40mA dI _G /dt = 0.5A/μs I _{TM} = 5.5A	T _j =25°C	I-II-III	TYP	2	μs
I _L	I _G =1.2 I _{GT}	T _j =25°C	I-II-III	MAX	15	mA
I _H *	I _T = 100mA gate open	T _j =25°C		MAX	10	mA
V _{TM} *	I _{TM} = 5.5A tp= 380μs	T _j =25°C		MAX	1.75	V
I _{DRM} I _{RRM}	V _{DRM} Rated V _{RRM} Rated	T _j =25°C T _j =110°C		MAX	10	μA
dV/dt *	Linear slope up to V _D =67%V _{DRM} gate open	T _j =110°C		MIN	5	V/μs
(dI/dt)c *	(dV/dt)c = 0.1V/μs	T _j =110°C		TYP	20	
				MIN	1.8	A/ms

* For either polarity of electrode A₂ voltage with reference electrode A₁.

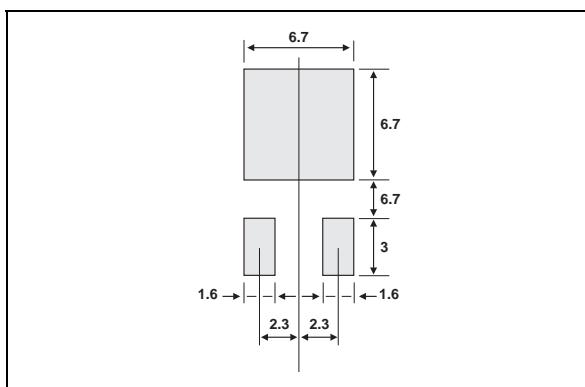
ORDERING INFORMATION



PACKAGE MECHANICAL DATA
DPAK Plastic



REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.094
A1	0.90		1.10	0.035		0.043
A2	0.03		0.23	0.001		0.009
B	0.64		0.90	0.025		0.035
B2	5.20		5.40	0.204		0.212
C	0.45		0.60	0.017		0.023
C2	0.48		0.60	0.018		0.023
D	6.00		6.20	0.236		0.244
E	6.40		6.60	0.251		0.259
G	4.40		4.60	0.173		0.181
H	9.35		10.10	0.368		0.397
L2		0.80			0.031	
L4	0.60		1.00	0.023		0.039
V2	0°		8°	0°		8°

FOOT PRINT DIMENSIONS (in millimeters)

MARKING

TYPE	MARKING
T405-400B	T4 0540
T405-600B	T4 0560

WEIGHT : 0.30g

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