Silicon Controlled Rectifiers Reverse Blocking Triode Thyristors

... all diffused PNP devices designed for gating operation in mA/ μ A signal or detection circuits.

- Low-Level Gate Characteristics I_{GT} = 200 μA (Max) @ 25°C
- Low Holding Current $I_{H} = 2 \text{ mA} (Max) @ 25^{\circ}C$
- Anode Common to Case
- Glass-to-Metal Bond for Maximum Hermetic Seal









*MAXIMUM RATINGS (TJ = 25°C, RGK = 1000 ohms unless otherwise noted.)

Rating		Symbol	Value	Unit
Peak Repetitive Forward and Revers (T _J = 25 to 125°C)	e Blocking Voltage, Note 1 2N2323 2N2324 2N2326 2N2326 2N2329	VDRM or VRRM	50 100 200 400	Volts
Non-Repetitive Peak Reverse Blocki (t ≪ 5 ms)	ng Voltage (TJ = 25 to 125°C) 2N2323 2N2324 2N2326 2N2326 2N2329	V _{RSM}	75 150 300 500	Volts
RMS On-State Current (All Conduction Angles, T _C = 85°C)		IT(RMS)	1.6	Amps
Average On-State Current	$T_{C} = 85^{\circ}C$ $T_{A} = 30^{\circ}C$	IT(AV)	1 0.45	Amp
Peak Non-Repetitive Surge Current (One cycle, 60 Hz, T _C = 80°C) Preceded and followed by rated c	urrent and voltage	ITSM	15	Amps

*Indiciates JEDEC Registered Data.

Note 1. VDRM and VRRM for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

(cont.)

2N2323 thru 2N2329

*MAXIMUM RATINGS — continued (T_C = 25°C unless otherwise noted, R_{GK} = 1000 ohms.)

Rating	Symbol	Value	Unit
Peak Gate Power	PGM	0.1	Watt
Average Gate Power	PG(AV)	0.01	Watt
Peak Gate Current	IGM	0.1	Amp
Peak Gate Voltage	VGM	6	Volts
Operating Junction Temperature Range	Тј	-65 to +125	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C
Lead Solder Temperature (>1/16" from case, 10 s max)	-	+ 230	°C

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted, $R_{GK} = 1000$ ohms.)

Characteristic	Symbol	Min	Max	Unit
*Peak Forward or Reverse Blocking Current (VAK = Rated V _{DRM} or V _{RRM}) $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	DRM ^{, I} RRM	_	10 100	μΑ μΑ
Peak On-State Voltage (I _{TM} = 1 A peak) (I _{TM} = 3.14 A Peak, T _C = 85°C)*	VTM	_	1.5 2	Volts
Gate Trigger Current (Continuous dc), Note 1 $(V_D = 6 \text{ Vdc}, R_L = 100 \text{ ohms})$ $(V_D = 6 \text{ Vdc}, R_L = 100 \text{ ohms}, T_C = -65^{\circ}\text{C})$	lgt	=	200 350*	μΑ
Gate Trigger Voltage (Continuous dc) $(V_D = 6 Vdc, R_L = 100 \text{ ohms})$ $(V_D = 6 Vdc, R_L = 100 \text{ ohms}, T_C = -65^{\circ}C)^*$ $(V_D = Rated V_{DRM}, R_L = 100 \text{ ohms}, T_J = 125^{\circ}C)^*$	VGT	 0.1	0.8 1 —	Volts
Holding Current (V _D = 6 Vdc) (V _D = 6 Vdc, T _C = -65°C)* (V _D = 6 Vdc, T _C = 125°C)*	ι _Η	 0.15	2 3 —	mA

*Indicates JEDEC Registered Data.

Note 1. RGK current is not included in measurement.



CURRENT DERATING

FIGURE 2 - AMBIENT TEMPERATURE



MOTOROLA THYRISTOR DEVICE DATA

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