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50RIA SERIES

MEDIUM POWER THYRISTORS

Stud Version

Features

- High current rating
- Excellent dynamic characteristics
- dv/dt = 1000V/μs option
- Superior surge capabilities
- Standard package
- Metric threads version available
- Types up to 1600V V_{DRM}/V_{RRM}

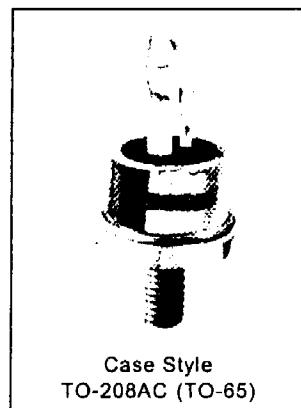
50 A

Typical Applications

- Phase control applications in converters
- Lighting circuits
- Battery charges
- Regulated power supplies and temperature and speed control circuit
- Can be supplied to meet stringent military, aerospace and other high-reliability requirements

Major Ratings and Characteristics

Parameters	50RIA		Units
	10 to 120	140 to 160	
$I_{T(AV)}$	50	50	A
@ T_c	94	90	°C
$I_{T(RMS)}$	80	80	A
I_{TSM}	1430	1200	A
@ 60Hz	1490	1257	A
I^2t	10.18	7.21	KA ² s
@ 60Hz	9.30	6.58	KA ² s
V_{DRM}/V_{RRM}	100 to 1200	1400 to 1600	V
t_q typical	110		μs
T_j	- 40 to 125		°C



E E T R I A S P E C I A L I T Y O S

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM} , max. repetitive peak and off-state voltage (1) V	V_{RSM} , maximum non-repetitive peak voltage (2) V	I_{DRM}/I_{RRM} max. @ $T_j = T_{j\max}$ mA
50RIA	10	100	150	15
	20	200	300	
	40	400	500	
	60	600	700	
	80	800	900	
	100	1000	1100	
	120	1200	1300	
	140	1400	1500	
	160	1600	1700	

(1) Units may be broken over non-repetitively in the off-state direction without damage, if dI/dt does not exceed 20A/μs

(2) For voltage pulses with $t_p \leq 5ms$

On-state Conduction

Parameter	50RIA		Units	Conditions		
	10 to 120	140 to 160				
$I_{T(AV)}$ @ Case temperature	50 94	50 90	A °C	180° sinusoidal conduction		
$I_{T(RMS)}$	80	80	A			
I_{TSM} Max. peak, one-cycle non-repetitive surge current	1430	1200	A	$t = 10ms$	Sinusoidal half wave, Initial $T_j = T_j$ max.	
	1490	1257		$t = 8.3ms$		
I^2t Maximum I^2t for fusing	1200	1010	KA^2s	$t = 10ms$		
	1255	1057		$t = 8.3ms$		
$I^2\sqrt{t}$ Maximum $I^2\sqrt{t}$ for fusing	10.18	7.21	$KA^2\sqrt{s}$	$t = 10ms$	Initial $T_j = T_j$ max.	
	9.30	6.58		$t = 8.3ms$		
$V_{T(TO)1}$ Low level value of threshold voltage	7.20	5.10	$KA^2\sqrt{s}$	$t = 10ms$		
	6.56	4.65		$t = 8.3ms$		
$V_{T(TO)2}$ High level value of threshold voltage	101.8	72.1	$KA^2\sqrt{s}$	$t = 0.1$ to $10ms$, no voltage reapplied, $T_j = T_j$ max.		
r_{11} Low level value of on-state slope resistance	0.94	1.02	V	$(16.7\% \times \pi \times I_{T(AV)} < < \pi \times I_{T(AV)})$, $T_j = T_j$ max.		
	1.08	1.17		$(\pi \times I_{T(AV)} < < 20 \times \pi \times I_{T(AV)})$, $T_j = T_j$ max.		
r_{12} High level value of on-state slope resistance	4.08	4.78	$m\Omega$	$(16.7\% \times \pi \times I_{T(AV)} < < \pi \times I_{T(AV)})$, $T_j = T_j$ max.		
	3.34	3.97		$(\pi \times I_{T(AV)} < < 20 \times \pi \times I_{T(AV)})$, $T_j = T_j$ max.		
V_{TM} Max. on-state voltage	1.60	1.78	V	$I_{pk} = 157 A$, $T_j = 25^\circ C$		
I_H Maximum holding current	200		mA	$T_j = 25^\circ C$, Anode supply 22V, resistive load, Initial $I_T = 2A$		
I_L Latching current	400			Anode supply 6V, resistive load		

Switching

Parameter	50RIA	Units	Conditions
di/dt Max. rate of rise of turned-on current $V_{DRM} \leq 600V$	200	A/ μs	$T_c = 125^\circ C$, V_{DM} = rated V_{DRM} Gate pulse = 20V, 15Ω , $t_p = 6\mu s$, $t_r = 0.1\mu s$ max. $I_{TM} = (2x$ rated $di/dt)$ A
$V_{DRM} \leq 1600V$	100		
t_d Typical delay time	0.9	μs	$T_c = 25^\circ C$ V_{DM} = rated V_{DRM} $I_{TM} = 10A$ dc resistive circuit Gate pulse = 10V, 15Ω source, $t_p = 20\mu s$
t_q Typical turn-off time	110		$T_c = 125^\circ C$, $I_{TM} = 50A$, reapplied $dv/dt = 20V/\mu s$ $dir/dt = -10A/\mu s$, $V_R = 50V$

Blocking

Parameter	50RIA	Units	Conditions
dv/dt Max. critical rate of rise of off-state voltage	200	$V/\mu s$	$T_j = T_j$ max, linear to 100% rated V_{DRM}
	500 (*)		$T_j = T_j$ max, linear to 67% rated V_{DRM}

(*) Available with $dv/dt = 1000V/\mu s$, to complete code add S90 i.e. 50RIA160S90.

Triggering

Parameter	50RIA	Units	Conditions
P _{GM}	Maximum peak gate power	10	T _J = T _J max, t _p ≤ 5ms
P _{G(AV)}	Maximum average gate power	2.5	
I _{CM}	Max. peak positive gate current	2.5	A
+V _{GM}	Maximum peak positive gate voltage	20	
-V _{GM}	Maximum peak negative gate voltage	10	V
I _{GT}	DC gate current required to trigger	250 100 50	mA
V _{GT}	DC gate voltage required to trigger	3.5 2.5	V
I _{GD}	DC gate current not to trigger	5.0	mA
V _{GD}	DC gate voltage not to trigger	0.2	V

Thermal and Mechanical Specification

Parameter	50RIA	Units	Conditions
T_j Max. operating temperature range	- 40 to 125	°C	
T_{sig} Max. storage temperature range	- 40 to 125	°C	
R_{thJC} Max. thermal resistance, junction to case	0.35	K/W	DC operation
R_{thCS} Max. thermal resistance, case to heatsink	0.25	K/W	Mounting surface, smooth, flat and greased
T Mounting torque	Min.	2.8 (25)	Nm (lbf-in)
	Max.	3.4 (30)	
wt	Approximate weight	28 (1.0)	g (oz)
Case style	TO-208AC (TO-65)	See Outline Table	

$\Delta R_{\text{in}, \text{sc}}$ Conduction

(The following table shows the increment of thermal resistance $R_{th,inc}$ when devices operate at different conduction angles than DC)

Conduction angle	Sinusoidal conduction	Rectangular conduction	Units	Conditions
180°	0.078	0.057	K/W	$T_j = T_{j\max}$
120°	0.094	0.098		
90°	0.120	0.130		
60°	0.176	0.183		
30°	0.294	0.296		

Ordering Information Table

Ordering Information Table	
Design Code	
50	RIA
0	S 0
M	
1	2
3	4
5	



- Current code
- Essential part number
- Voltage code: Code x 10 = V_{RRM} (See Voltage Rating Table)
- Critical dv/dt: None = 500V/μs (Standard value)
- Special selection
- 5 - Stud base TO-208AC (TO-65) 1/4" 28UNF-2A
- M - Stud base TO-208AC (TO-65) M6 X 1

Outline Table

