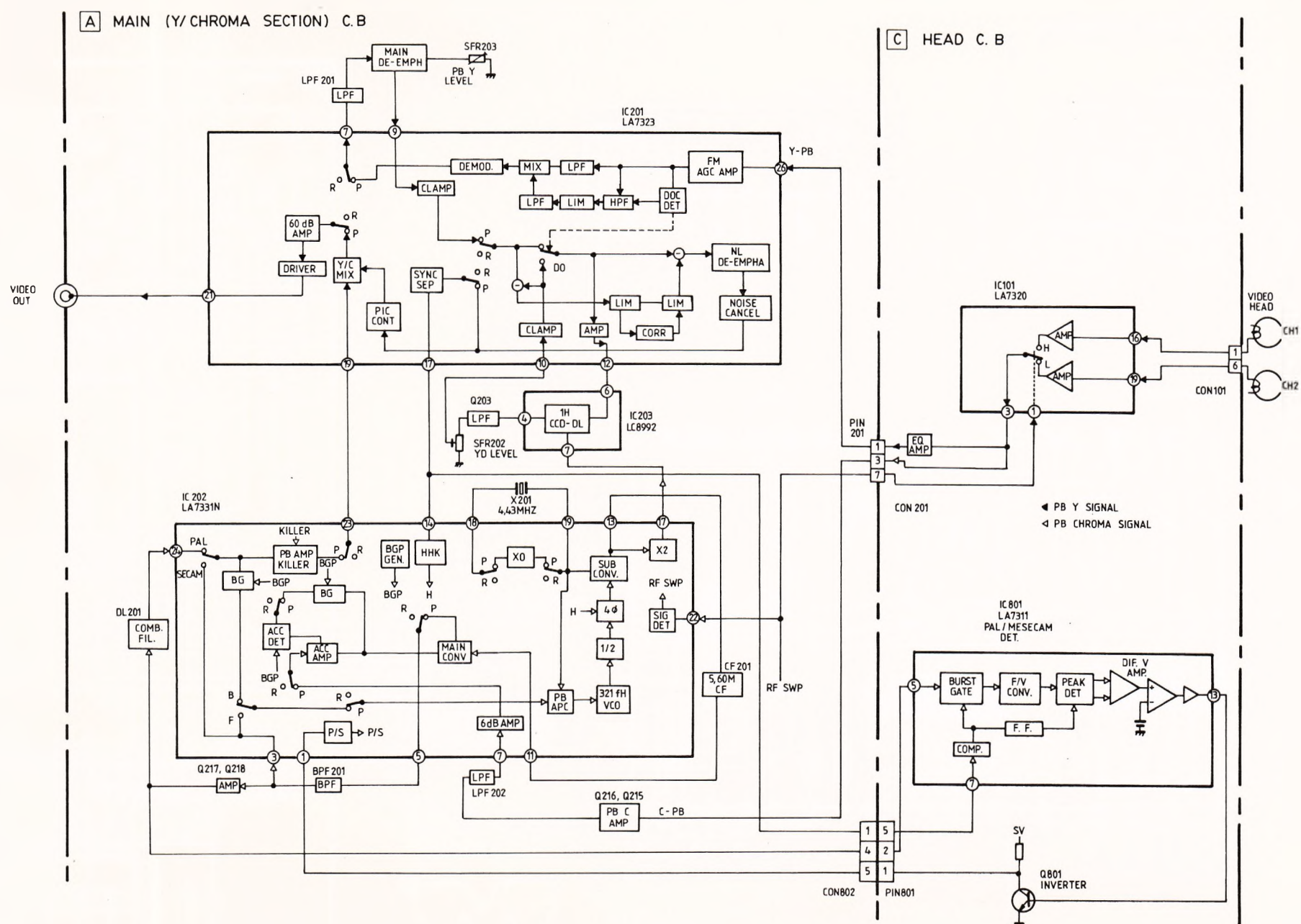
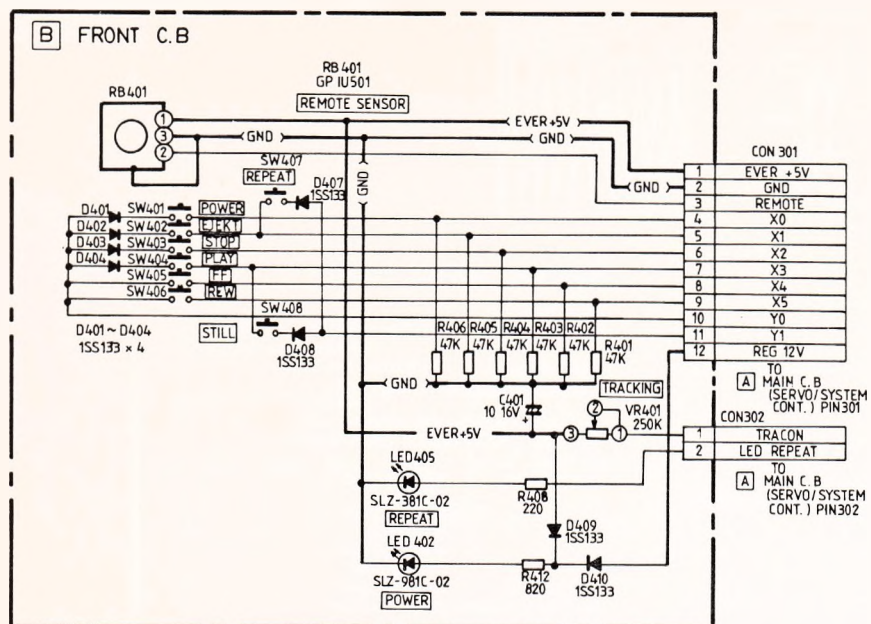


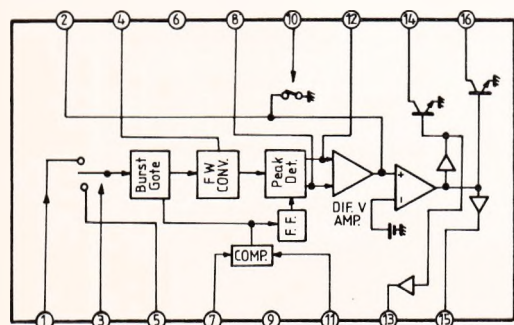
BLOCK DIAGRAM - 1 (VIDEO)



SCHEMATIC DIAGRAM - 3(FRONT/HEAD AMP)

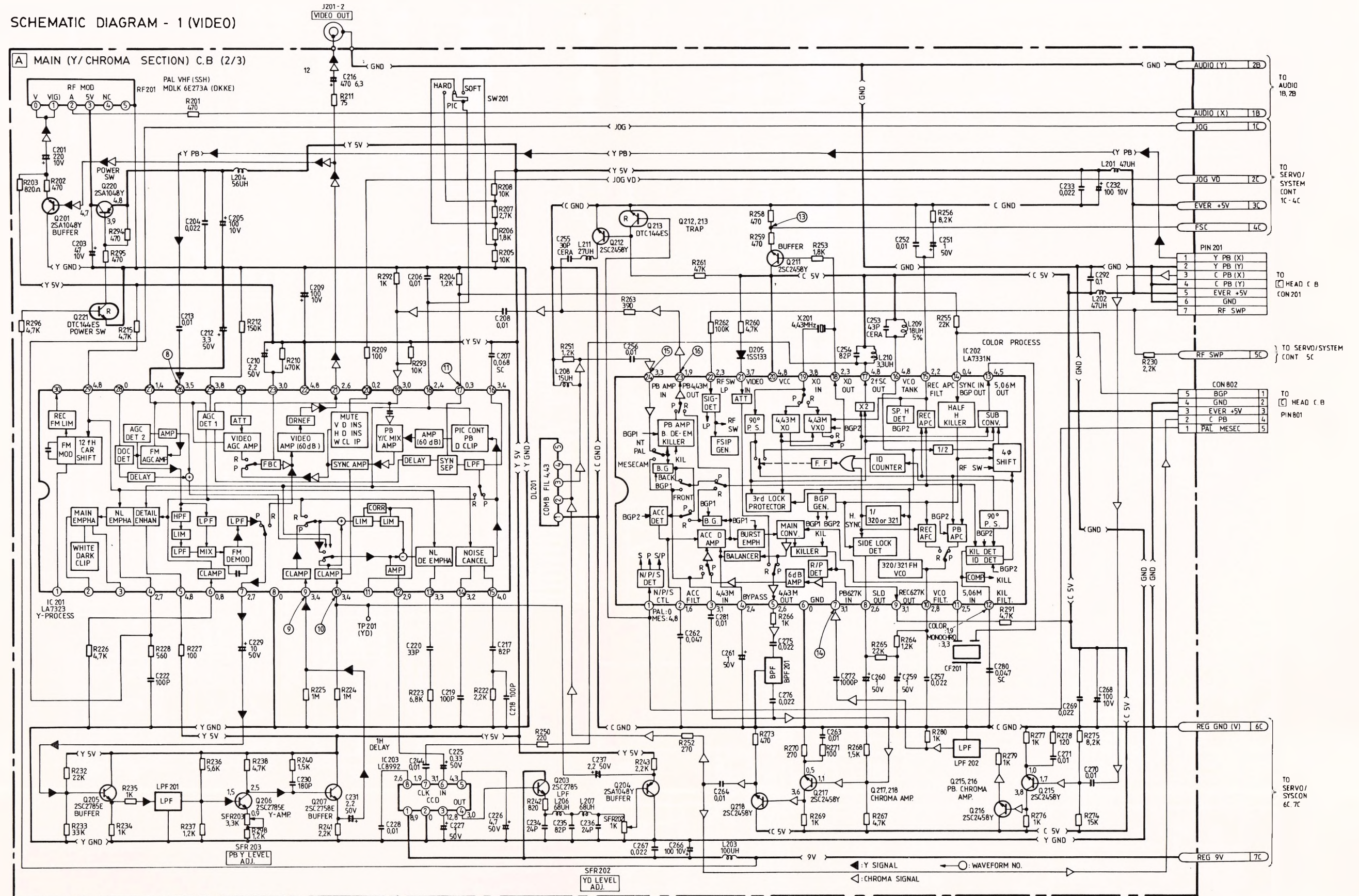


IC.LA7311



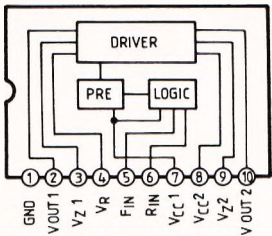
AIWA
HV - E101S SH
HV - E101DK KE

SCHEMATIC DIAGRAM - 1 (VIDEO)

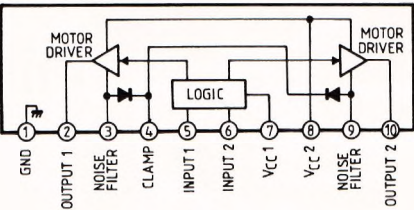


IC BLOCK DIAGRAM

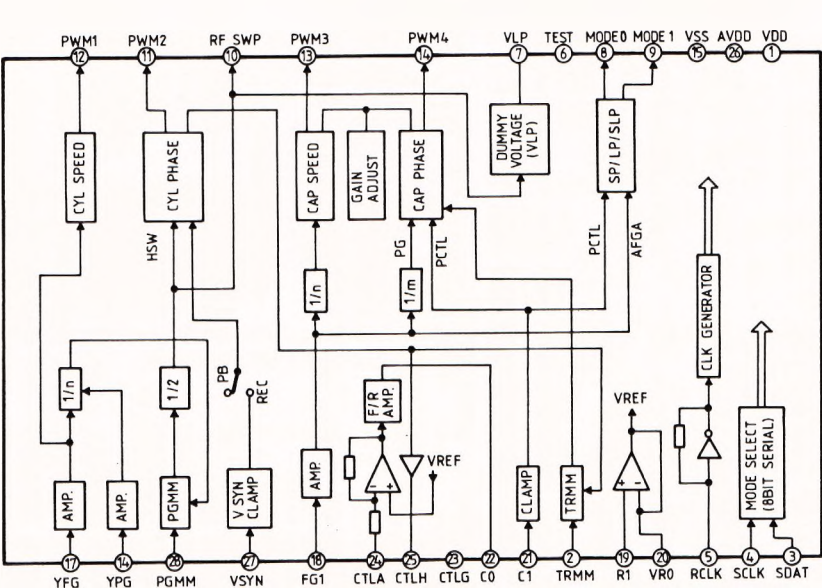
IC, BA6209



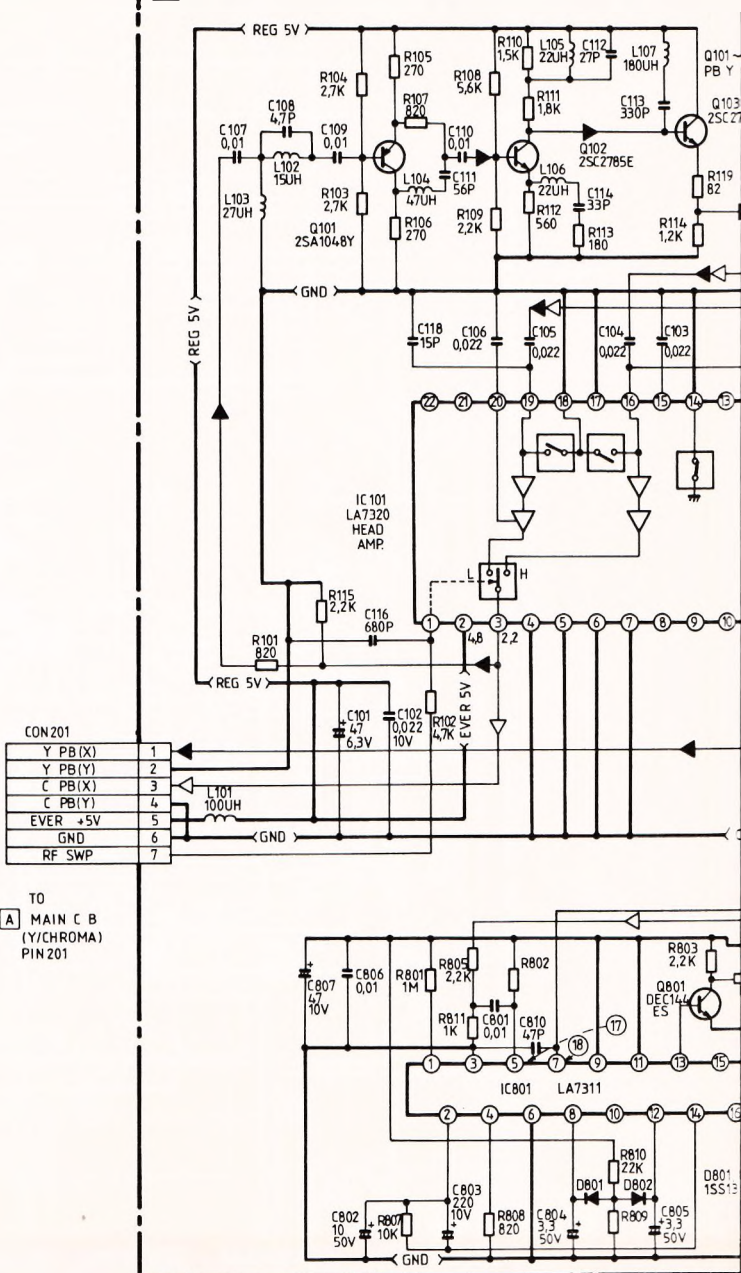
IC, BA6219B



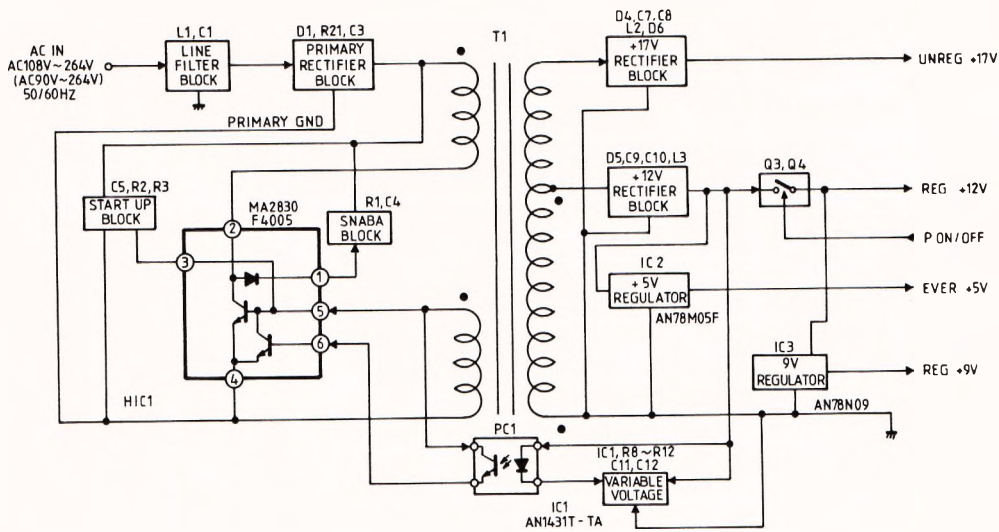
IC, MN67481



HEAD C.B



BLOCK DIAGRAM - 3 (POWER UNIT)



TRANSISTOR ILLUSTRATION



PRACTICAL SERVICE FIGURE

- Output level
Video (Impedance) : $1.0 \pm 0.2V_{p-p}$ (75 Ω $\pm$ 20%, Unbalance)
Audio (Impedance) : $-8 \pm 3dB$ (Less than 3.3k Ω)
RF Modulated (Impedance) : $75 \pm 3dB_{\mu V}$ (75 Ω , Unbalance)
SSH : 55.25MHz (3CH) $\sim$ 62.25MHz (4CH)
DKKE : 54.3, 25MHz (30CH) $\sim$ 61.5, 25Hz (39CH)
S/N Ratio
Video (SP mode) : More than 42dB (Y-Signal)
More than 38dB (C-Signal, AM)
More than 36dB (C-Signal, PM)
Audio (SP mode) : More than 36dB
Audio Frequency Response : 200Hz $\sim$ 6kHz $\pm$ 4dB (SP mode)
Horizontal Resolution : More than 230 lines
Tape Speed (SP mode) : 23.39mm/sec $\pm$ 0.5%
Wow & Flutter : Less than 0.4%
FF time : Less than 300 sec (E-240)
REW time : Less than 300 sec (E-240)
Back tension : 46.5 $\pm$ 12g-cm
Loading time : Less than 3sec
Unloading time : Less than 3.5sec
Test Tape : MVP - 08 (PB)
TPV - 160S
TTV - 06T
TTV - P5
E-240 (or HS-T120)

WAVE FORM - 3
SCHEMATIC DIA

PB CHROMA S/C
CHROMA system

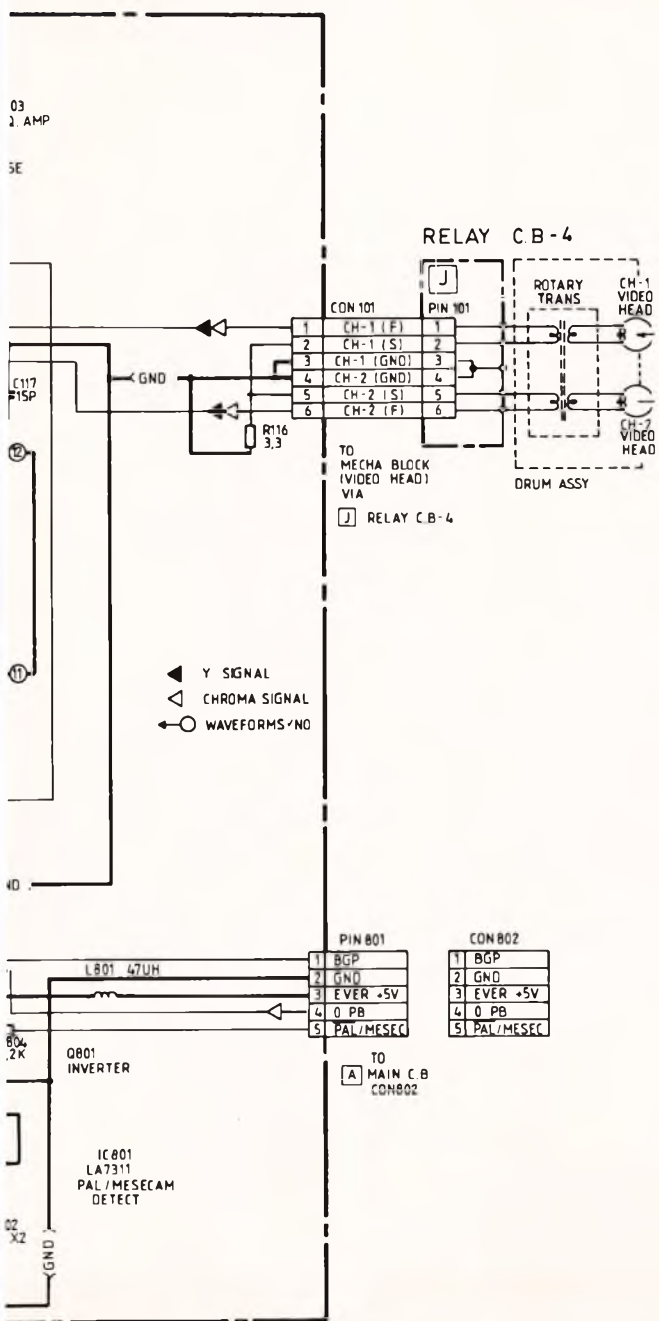


PB CHROMA S/C
IC801 Pin 5



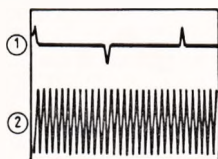
IC801 Pin 7



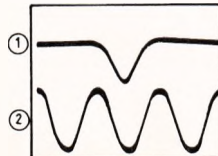


WAVE FORM - 1
SERVO WAVE FORM

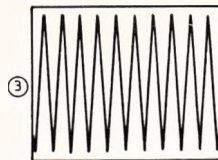
1. DRUM PG AND FG
Upper: DRUM PG AC 0.1V/DIV (IC306 ⑩ pin)
Lower: DRUM FG AC 50 mV/DIV (IC306 ⑪ pin)
Time 5msec/DIV



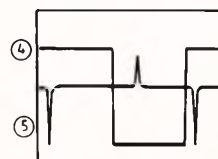
2. DRUM PG AND FG
Enlargement of 1
When the PG pulse (upper) is generated (either positive or negative) the FG pulse (lower) is a negative half wave



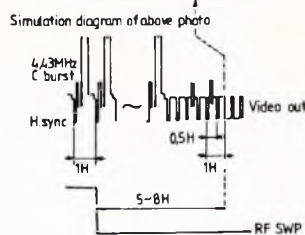
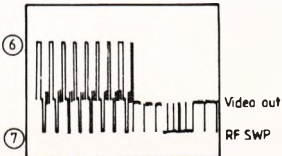
3. CAP FG (IC306 ⑩ pin)
AC 50mV/DIV
Time 2msec/DIV



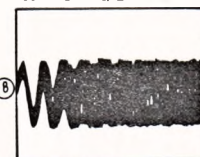
4. RF SW PULSE AND CTL
RF SW Pulse DC 1V/DIV (IC306 ⑩ pin)
CTL AC 0.5V/DIV (IC306 ⑪ pin)
Time 5msec/DIV
The CTL pulse (negative) is 2.3±2.5msec from the leading edge of the RF SW pulse.



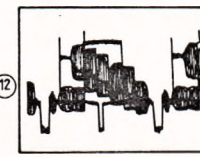
5. RF SW PULSE AND VIDEO OUT
RF SW Pulse DC 1V/DIV (IC306 ⑩ pin)
Video out (open) AC 0.5V/DIV
Time 0.1msec/DIV
The RF SW pulse rises or falls 5-8H (300-512μ sec) before the VD (vertical sync) signal in the video signal



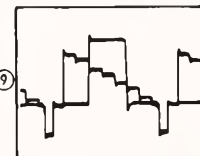
Y-PROCESS WAVE FORM
Y FM signal IC201 pin ② Tape used: TTV-P1
200mV/DIV 0.5μs/DIV



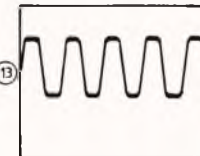
VIDEO OUT jack (terminated with 75 ohm)
200mV/DIV 10μs/DIV



Y-signal (DEMODULATION) level
IC201 pin ③ Tape used: TTV-P1
100mV/DIV 10μs/DIV



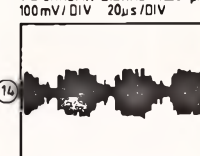
fsc: 4.43MHz OSC Center of R25B and R259
100mV/DIV 0.1μs/DIV



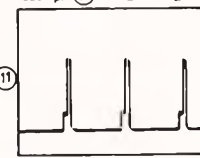
Y DL LEVEL
IC201 pin ⑩ 100mV/DIV 10μs/DIV



CHROMA-PROCESS WAVE FORM
PB CHROMA SIGNAL IC202 pin ⑦
100mV/DIV 20μs/DIV



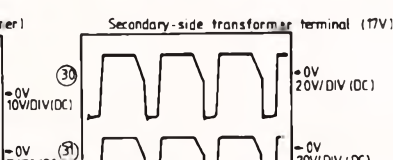
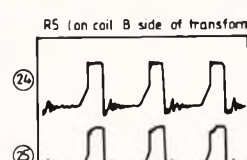
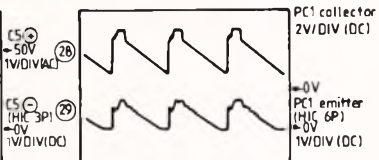
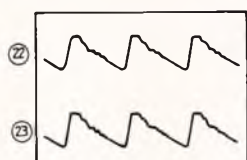
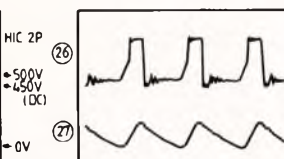
COMP SYNC
IC201 pin ⑪ 1V/DIV 20μs/DIV



PB CHROMA SIGNAL (DEMODULATION)
IC202 pin ⑫ 50mV/DIV 20μs/DIV



WAVE FORM - 3
POWER UNIT WAVE FORM



RAM - 2

AL IC202 pin ⑬ 50mV/DIV 20μs/DIV



IGNAL (TTV-P1) 1mV/DIV 20μs/DIV

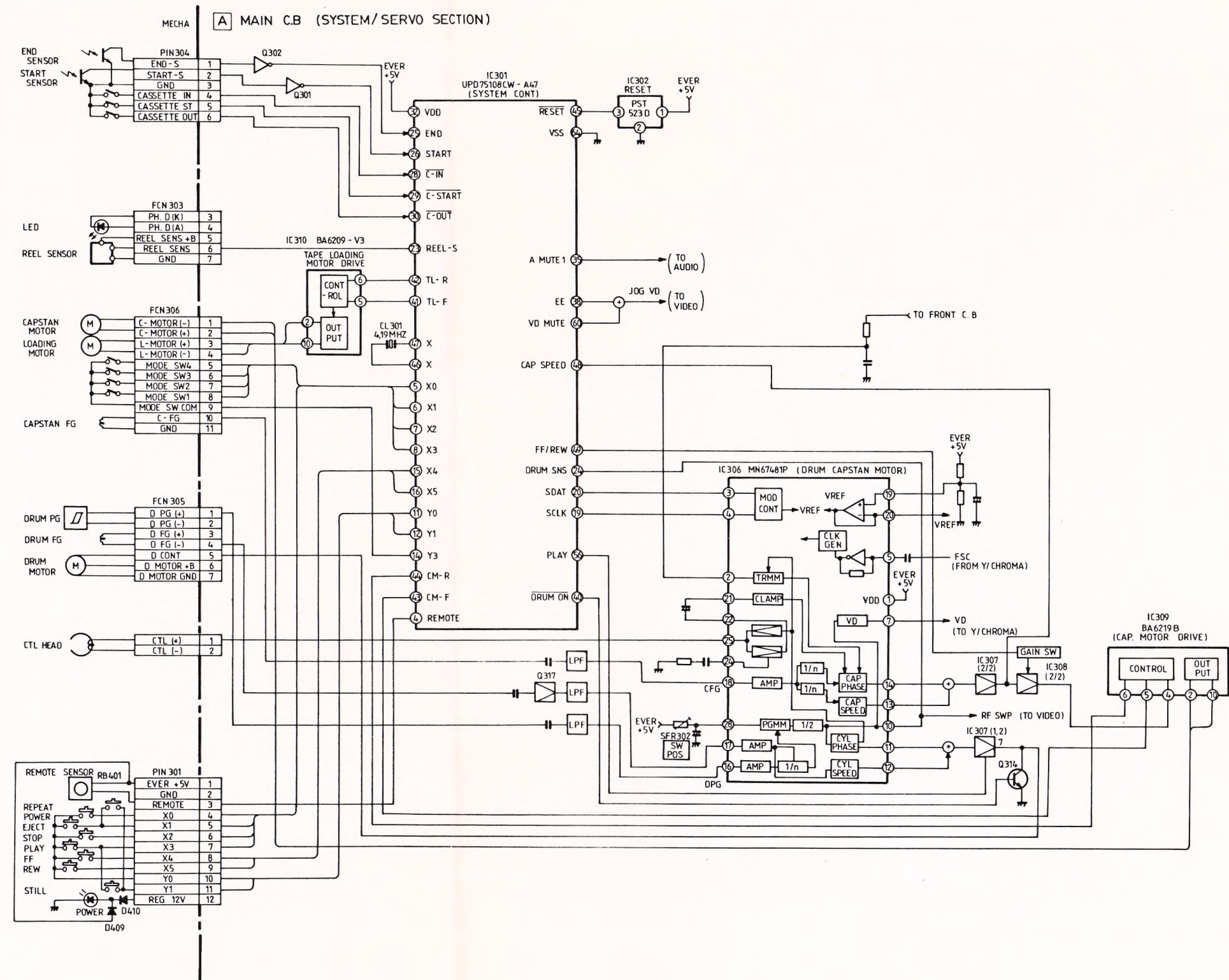


V/DIV 20μs/DIV



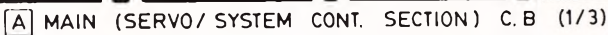
AIWA
HV-E101S SH
HV-E101DK KE

BLOCK DIAGRAM - 2 (SYSTEM/ SERVO CONTROL)

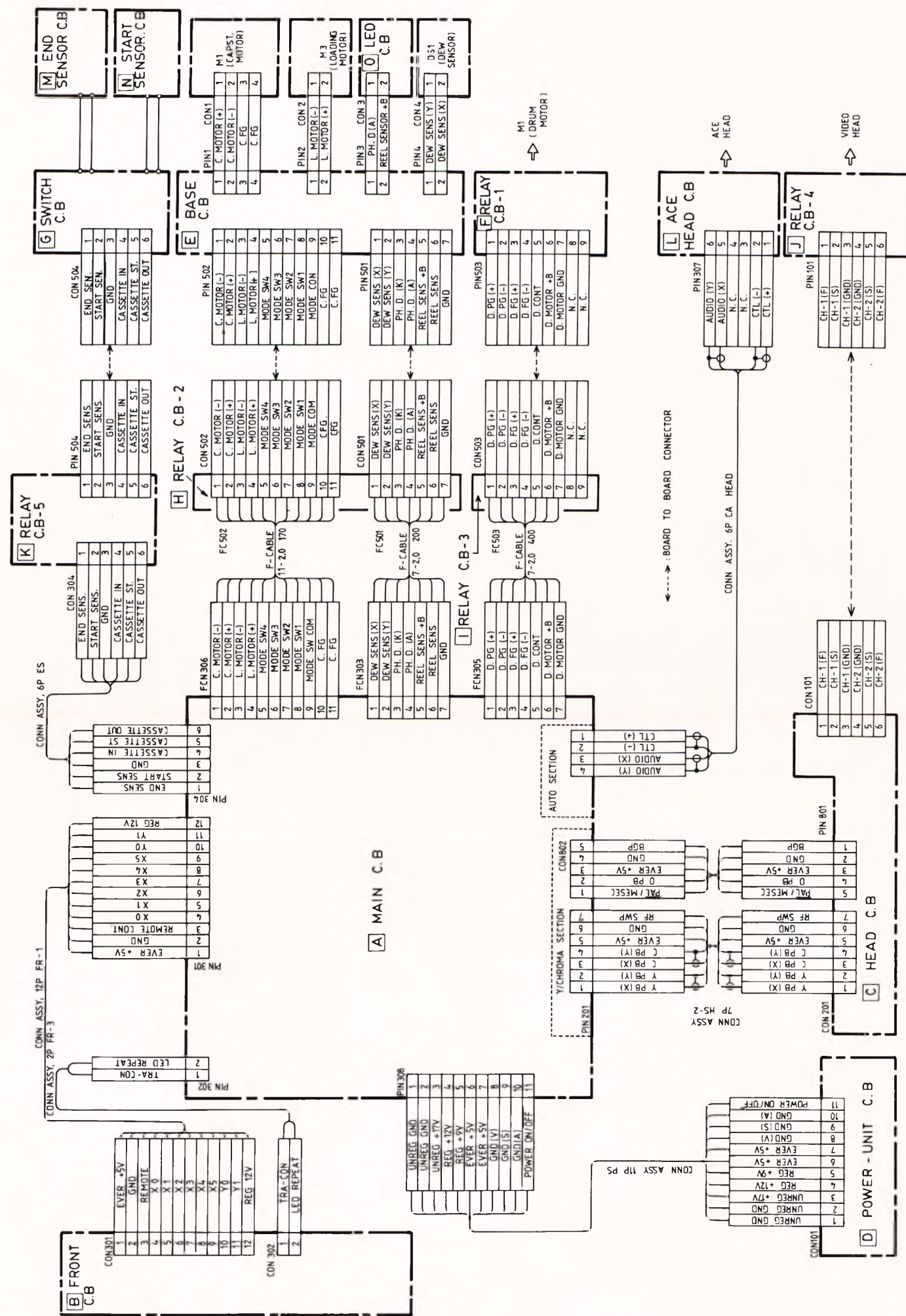


AIWA
HV-E101S SH
HV-E101DK KE

SCHEMATIC DIAGRAM - 2 (SYSTEM / SERVO CONTROL)



WIRE HARNESS DIAGRAM



Ref No	DE
IC1	H2 OF OF
IC2	A OF
IC3	AN OF OF OF
Q3	25
Q4	C OF
D1	S
D2	H OF OF
D3	H OF OF
D4,5	S
D6	H OF

SCHEMATIC DIAGRAM - 4 (POWER UNIT/AUDIO) WIRING - 3 (POWER UNIT)

